

2MBI150VA-060-50

IGBT Modules

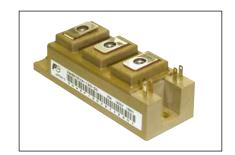
IGBT MODULE (V series) 600V / 150A / 2 in one package

Features

High speed switching Voltage drive Low Inductance module structure

Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as Welding machines



■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings (at T_c=25°C unless otherwise specified)

Items	-	Symbols Conditions			Maximum ratings	Units	
Collector-Emitter voltage		Vces		,	600	V	
Gate-Emitter voltage		V _{GES}			±20	V	
Collector current		Ic	Continuous	Tc=100°C	150		
		C pulse	1ms		300	Α	
		-lc			150		
		-I _C pulse	1ms	,	300		
Collector power dissipation		Pc	1 device		650	W	
Junction temperature		Tj			175		
Operating junction temperature (under switching conditions)		Tjop			150	°c	
Case temperature		Tc		125		C	
Storage temperature		T _{stg}		,	-40 ~ 125		
Isolation voltage between terminal and copper base (*1)		Viso	AC: 1min.		2500	VAC	
Screw torque	Mounting (*2)	-			5.0	N m	
	Terminals (*3)	-			5.0	IN III	

Note *1: All terminals should be connected together when isolation test will be done.

Note *2: Recommendable Value : 3.0-5.0 Nm (M5 or M6) Note *3: Recommendable Value : 2.5-3.5 Nm (M5)

■ Electrical characteristics (at T_i= 25°C unless otherwise specified)

ltama	Cumbala	Conditions		Characteristics			Heite
Items	Symbols			min.	typ.	max.	Units
Zero gate voltage collector current	Ices	V _{GE} = 0V, V _{CE} = 600V		-	-	1.0	mA
Gate-Emitter leakage current	-Emitter leakage current I_{GES} $V_{CE} = 0V$, $V_{GE} = \pm 20V$			-	-	200	nA
Gate-Emitter threshold voltage	V _{GE (th)}	V _{CE} = 20V, I _C = 150mA		6.2	6.7	7.2	V
	V _{CE (sat)}	V _{GE} = 15V I _C = 150A	T _j =25°C	-	1.75	2.20	V
	(terminal)		T _j =125°C	-	2.05	-	
Collector Emitter saturation voltage			T _j =150°C		2.25		
Collector-Emitter saturation voltage	V _{CE (sat)} (chip)	V _{GE} = 15V I _C = 150A	T _j =25°C	-	1.60	2.05	
			T _j =125°C	-	1.90	-	
			T _j =150°C		2.00		
Internal gate resistance	R _G (int)	-	-	6	-	Ω	
Input capacitance	Cies	Vce = 10V, Vge = 0V, f = 1N	-	9.7	-	nF	
	ton	$V_{\rm CC} = 300V$		-	650	-	nsec
Turn-on time	t _r			-	300	-	
	t r (i)			-	100	-	
Turn-off time	toff			-	600	-	
Turn-on time	t _f			-	40	-	
	VF	V _{GE} = 0V I _F = 150A	T _j =25°C	-	1.70	2.15	V
	(terminal)		T _j =125°C	-	1.60	-	
Forward on voltage	(terrillal)	IF - 150A	T _j =150°C		1.57		
rorward on voltage		V _{GE} = 0V I _F = 150A	T _j =25°C	-	1.60	2.05	
			T _j =125°C	-	1.50	-	
		IF - 150A	T _j =150°C		1.47		
Reverse recovery time t_{rr} $I_F = 150A$			-	200	-	nsec	

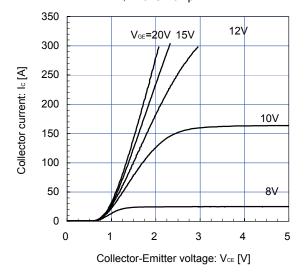
Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units	
items		Conditions	min.	typ.	max.	Units	
Thermal resistance (Aderrica)	R _{th(j-c)}	IGBT	-	-	0.31		
Thermal resistance (1device)		FWD	-	-	0.60	°C/W	
Contact thermal resistance (1device) (*4)	R _{th(c-f)}	with Thermal Compound	-	0.050	-		

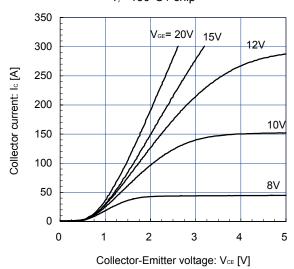
Note *4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

■ Characteristics (Representative)

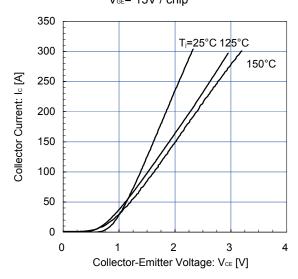
Collector current vs. Collector-Emitter voltage (typ.) T_i = 25°C / chip



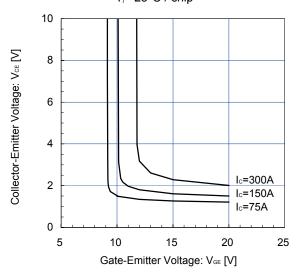
Collector current vs. Collector-Emitter voltage (typ.) T_i= 150°C / chip



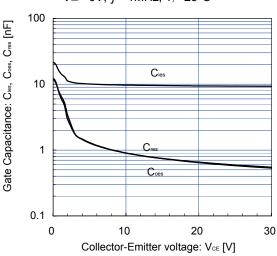
Collector current vs. Collector-Emitter voltage (typ.) V_{GE} = 15V / chip



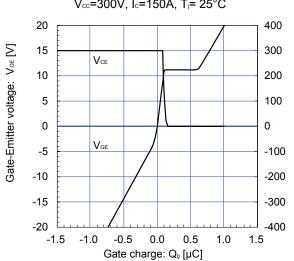
Collector-Emitter voltage vs. Gate-Emitter voltage T_j= 25°C / chip



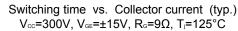
Gate Capacitance vs. Collector-Emitter Voltage V_{GE} = 0V, f= 1MHz, T_{J} = 25°C

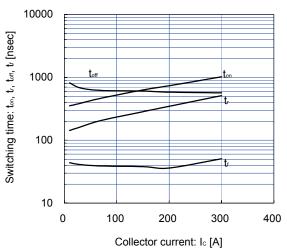


Dynamic Gate Charge (typ.) V_{cc}=300V, I_c=150A, T_i= 25°C

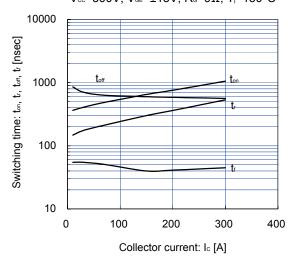


Collector-Emitter voltage: VoE [V]

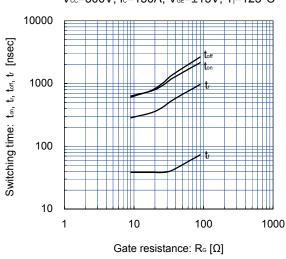




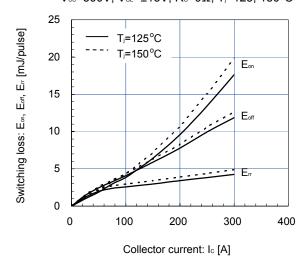
Switching time vs. Collector current (typ.) $V_{\text{CC}}=300\text{V}$, $V_{\text{GE}}=\pm15\text{V}$, $R_{\text{G}}=9\Omega$, $T_{\text{j}}=150^{\circ}\text{C}$



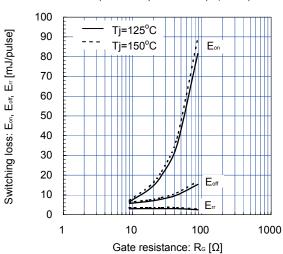
Switching time vs. Gate resistance (typ.) V_{CC} =300V, I_{C} =150A, V_{GE} =±15V, T_{J} =125°C



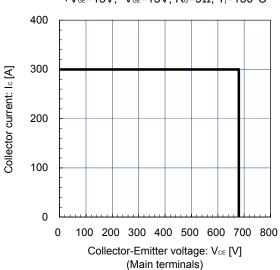
Switching loss vs. Collector current (typ.) V_{cc} =300V, V_{ce} =±15V, R_{c} =9 Ω , T_{j} =125, 150°C



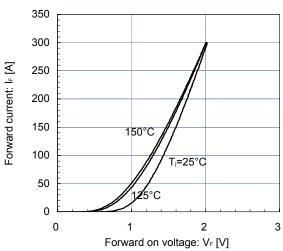
Switching loss vs. Gate resistance (typ.) V_{CC} =300V, I_{C} =150A, V_{GE} =±15V, T_{I} =125, 150°C



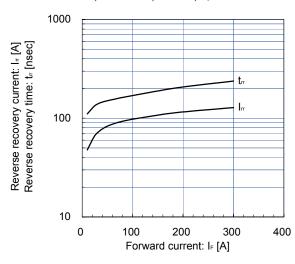
Reverse bias safe operating area (max.) $+V_{\text{GE}}=15V$, $-V_{\text{GE}}=15V$, $R_{\text{G}}=9\Omega$, $T_{\text{J}}=150^{\circ}\text{C}$



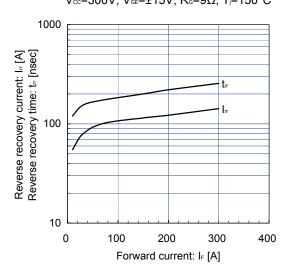
Forward Current vs. Forward Voltage (typ.) chip



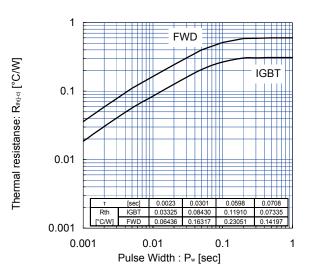
Reverse Recovery Characteristics (typ.) V_{CC} =300V, V_{GE} =±15V, R_{G} =9 Ω , T_{J} =125°C



Reverse Recovery Characteristics (typ.) V_{cc} =300V, V_{ge} =±15V, R_{g} =9 Ω , T_{j} =150°C

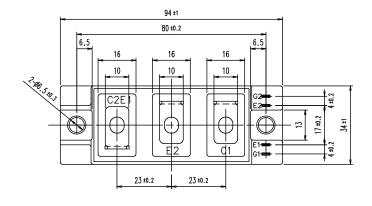


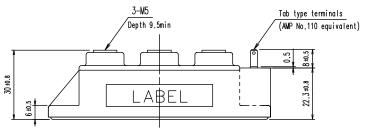
Transient Thermal Resistance (max.)



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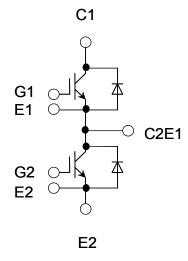
■ Outline Drawings, mm





Weight:180g(typ.)

■ Equivalent Circuit Schematic



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