

# 2MBI75VA-120-50

**IGBT Modules** 

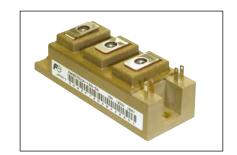
### IGBT MODULE (V series) 1200V / 75A / 2 in one package

#### Features

High speed switching Voltage drive Low Inductance module structure

#### Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as Welding machines



#### **■** Maximum Ratings and Characteristics

● Absolute Maximum Ratings (at T<sub>c</sub>=25°C unless otherwise specified)

Items		Symbols	Conditions		Maximum ratings	Units	
Collector-Emitter voltage		Vces		,	1200	V	
Gate-Emitter voltage		V <sub>GES</sub>			±20	V	
Collector current		Ic	Continuous	Tc=100°C	75		
		I <sub>C pulse</sub>	1ms		150	Α	
		-lc			75		
		-I <sub>C pulse</sub>	1ms	,	150		
Collector power dissipation		Pc	1 device		390	W	
Junction temperature		T <sub>j</sub>			175		
Operating junction temperature (under switching conditions)		T <sub>jop</sub>			150	°C	
Case temperature		Tc			125	C	
Storage temperature		T <sub>stg</sub>			-40 ~ 125		
Isolation voltage between terminal and copper base (*1)		Viso	AC: 1min.		2500	VAC	
Screw torque	Mounting (*2)	-		·	5.0	N m	
	Terminals (*3)	-			5.0	IN III	

Note \*1: All terminals should be connected together when isolation test will be done.

Note \*2: Recommendable Value : 3.0-5.0 Nm (M5 or M6) Note \*3: Recommendable Value : 2.5-3.5 Nm (M5)

■ Electrical characteristics (at T<sub>i</sub>= 25°C unless otherwise specified)

Itama	Cumbala	Conditions		Characteristics			Haita
Items	Symbols			min.	typ.	max.	Units
Zero gate voltage collector current	Ices	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1200V		-	-	1.0	mA
Gate-Emitter leakage current	Emitter leakage current IGES VCE = 0V, VGE = ±20V			-	-	200	nA
Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	$V_{CE} = 20V, I_{C} = 72mA$		6.0	6.5	7.0	V
	V <sub>CE (sat)</sub>	V <sub>GE</sub> = 15V I <sub>C</sub> = 75A	T <sub>j</sub> =25°C	-	1.95	2.40	V
	(terminal)		T <sub>j</sub> =125°C	-	2.30	-	
Collector Emitter saturation voltage			T <sub>j</sub> =150°C		2.35		
Collector-Emitter saturation voltage	V <sub>CE (sat)</sub> (chip)	V <sub>GE</sub> = 15V I <sub>C</sub> = 75A	T <sub>j</sub> =25°C	-	1.85	2.30	
			T <sub>j</sub> =125°C	-	2.20	-	
			T <sub>j</sub> =150°C		2.25		
Internal gate resistance	R <sub>G (int)</sub> -			-	10	-	Ω
Input capacitance	Cies	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0V, f = 1MHz		-	6.0	-	nF
	ton	V <sub>cc</sub> = 600V L <sub>s</sub> = 30nH I <sub>c</sub> = 75A V <sub>se</sub> = ±15V		-	600	-	nsec
Turn-on time	tr			-	200	-	
	<b>t</b> r (i)			-	50	-	
Turn-off time	toff	$R_G = 2.2\Omega$		-	600	-	
Turn-on time	t <sub>f</sub>	$T_{j} = 150^{\circ}C$		-	40	-	
	VF	V <sub>GE</sub> = 0V	T <sub>j</sub> =25°C	-	1.80	2.25	V
	(terminal)	I <sub>F</sub> = 75A	T <sub>j</sub> =125°C	-	1.95	-	
Forward on voltage	(terrillial)	IF - 75A	T <sub>j</sub> =150°C		1.90		
rorward on voltage	VF	V <sub>GE</sub> = 0V I <sub>F</sub> = 75A	T <sub>j</sub> =25°C	-	1.70	2.15	
			T <sub>j</sub> =125°C	-	1.85	-	
	(chip)	IF - 73A	T <sub>j</sub> =150°C		1.80		
everse recovery time $t_{rr}$ $I_F = 75A$		-	150	-	nsec		

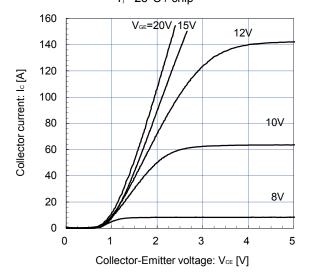
#### Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
items		Conditions	min.	typ.	max.	Units
Thermal resistance (Aderrica)	Ь	IGBT	-	-	0.38	°C/W
Thermal resistance (1device)	R <sub>th(j-c)</sub>	FWD	-	-	0.58	
Contact thermal resistance (1device) (*4)	R <sub>th(c-f)</sub>	with Thermal Compound	-	0.050	-	

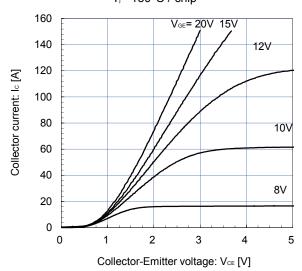
Note \*4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

#### **■** Characteristics (Representative)

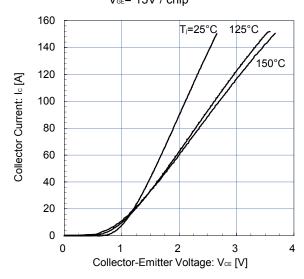
Collector current vs. Collector-Emitter voltage (typ.)  $T_i$ = 25°C / chip



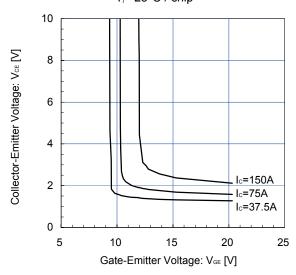
Collector current vs. Collector-Emitter voltage (typ.)  $T_j=150^{\circ}C$  / chip



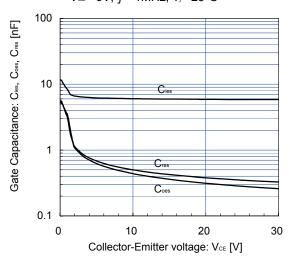
Collector current vs. Collector-Emitter voltage (typ.)  $V_{\text{GE}}$ = 15V / chip



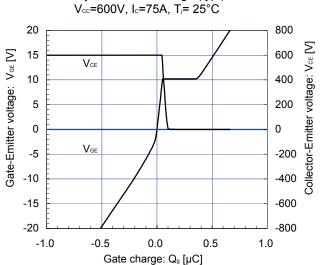
Collector-Emitter voltage vs. Gate-Emitter voltage T<sub>j</sub>= 25°C / chip

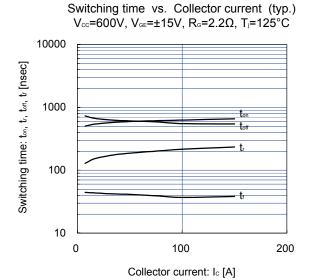


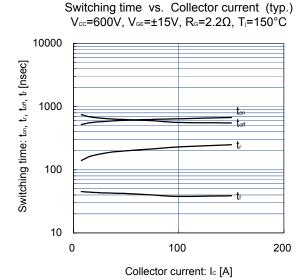
Gate Capacitance vs. Collector-Emitter Voltage  $V_{GE}$ = 0V, f= 1MHz,  $T_{J}$ = 25°C

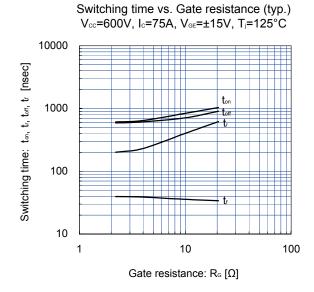


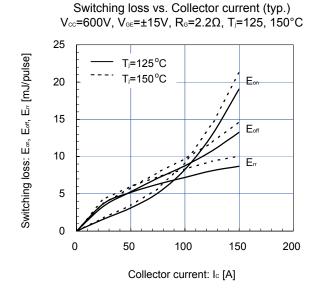
Dynamic Gate Charge (typ.)

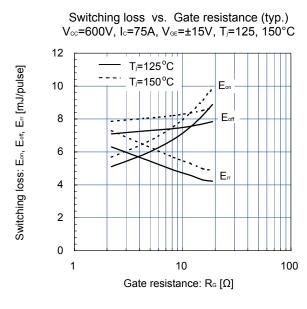


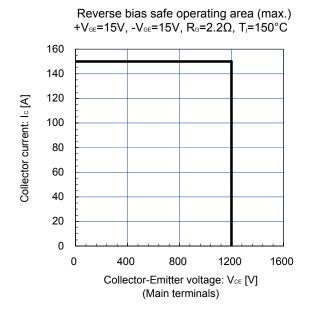


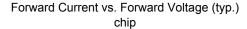


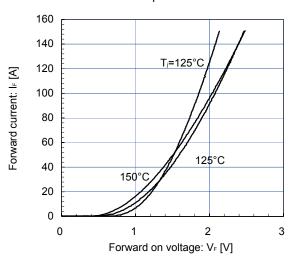




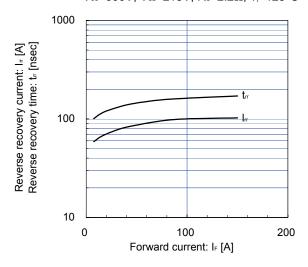




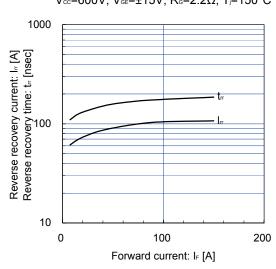




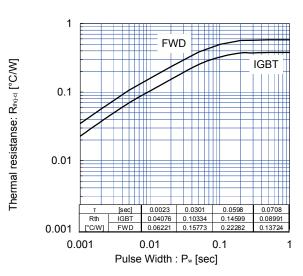
Reverse Recovery Characteristics (typ.)  $V_{\text{CC}}$ =600V,  $V_{\text{GE}}$ =±15V,  $R_{\text{G}}$ =2.2 $\Omega$ ,  $T_{\text{J}}$ =125°C



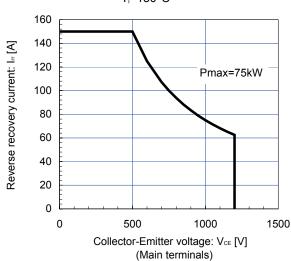
## Reverse Recovery Characteristics (typ.) $V_{cc}$ =600V, $V_{ee}$ =±15V, $R_{e}$ =2.2 $\Omega$ , $T_{i}$ =150°C



Transient Thermal Resistance (max.)

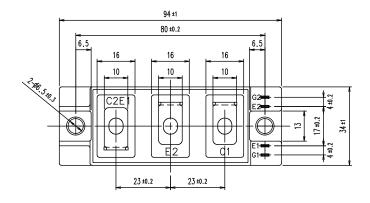


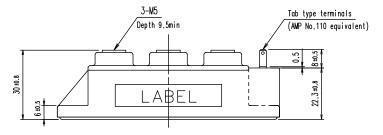
FWD safe operating area (max.) T=150°C



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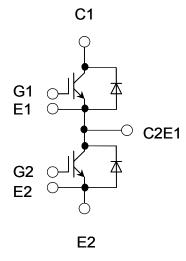
#### ■ Outline Drawings, mm





Weight:180g(typ.)

#### **■** Equivalent Circuit Schematic



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