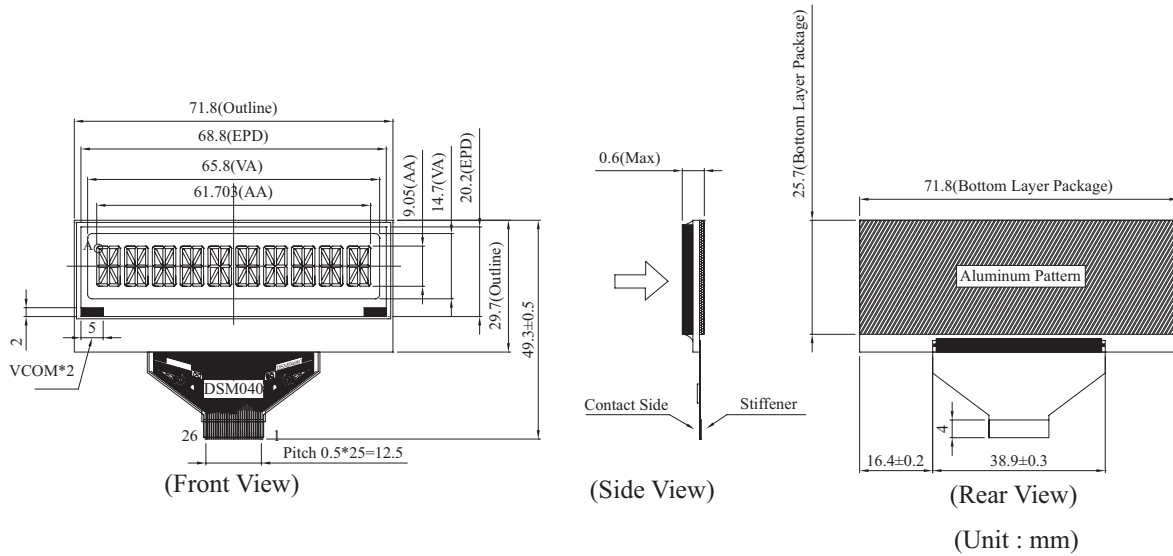


WAXN001001ABPW000000 E-Paper Module

Dimension drawing



No.	Symbol	Description
1	V0	Base power supply pin for EPD drive voltage
2	V1	Base power supply pin for EPD drive voltage
3	V2	Base power supply pin for EPD drive voltage
4	VSSE	Base power supply pin for EPD drive voltage
5	VSS	Ground pin
6	EIO1	Input/output pin for chip selection
7	XCK	Clock input pin for taking display data
8	VDD	Logic system power supply pin
9	LATCH	Latch pulse input pin for display data
10	RESER VED	NC
11	SHL	Input pin for selecting the shift direction of display data
12	SLEEPB	Control input pin for driver to enter power saving mode
13	DLY0	Input pin for selecting the driver output timing
14	DLY1	Input pin for selecting the driver output timing
15	RESER VED	NC
16	DI0	Input pin for display data
17	DI1	Input pin for display data
18	DI2	Input pin for display data
19	RESER VED	NC
20	EIO2	Input/output pin for chip selection
21	VSS	Ground pin
22	VSSE	Base power supply pin for EPD drive voltage
23	V2	Base power supply pin for EPD drive voltage
24	V1	Base power supply pin for EPD drive voltage
25	V0	Base power supply pin for EPD drive voltage
26	RESER VED	NC

Mechanical Date

Item	Dimension	Unit
Number of Segment	10 ICON x 1	-
Module dimension (Not included IC)	71.8 x 29.7 x 0.6(MAX)	mm
View area	65.8 x 14.7	mm
EDP type	Black/White	
View direction	All direction	

Absolute Maximum Rating

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	0	-	+50	°C
Storage Temperature	TST	-35	-	+60	°C
Input Voltage	VI	0	-	VDD	V
Supply Voltage For Logic	VDD	0	-	5.5	V
Supply Voltage For EPD	V0-V	0	-	40	V

Electronical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	VDD-VSS	-	2.4	5.0	5.5	V
Supply Voltage For EPD	V0-VSS	-	30	35	40	V
Input High Volt.	VIH	-	2.0	-	VDD	V
Input Low Volt.	VIL	-	0	-	0.8	V
Output High Volt.	VOH	-	2.4	-	VDD	V
Output Low Volt.	VOL	-	0	-	0.4	V
Supply Current	IDD	-	0.2	-	0.8	mA