Product data sheet

1. General description

WG75S65HFW1 uses advanced Fine Trench Field-stop IGBT technology with antiparallel diode in TO-247 package to provide extremely low Vce(sat), and excellent switching performance. This device is ideal for wide range switching frequency converters.





2. Features and benefits

- · Positive temperature efficient for easy paralleling
- · Very soft, fast recovery anti-parallel diode
- · High speed switching
- · EMI improved design

3. Applications

- Solar Converters
- UPS,ESS
- PFC
- Converters

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Parameter I			Value		Unit
V _{CE}	Collector-emitter voltage, T _j ≥ 25 °C			650			V
I _c	DC collector current, limited by $T_{j(max)}$ $T_C = 100 ^{\circ}C$				75		Α
Symbol	Parameter Conditions		Notes	Min	Тур	Max	Unit
Static characteristics							
V _{CE(sat)}	Collector-emitter saturation voltage	$V_{GE} = 15 \text{ V}; I_C = 75 \text{ A}; T_j = 25 \text{ °C}$		-	1.5	1.9	V

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		•C
2	С	collector		
3	Е	emitter		
mb	С	mounting base; connected to collector	TO247	G E sym200

6. Ordering information

Table 3. Ordering information

Type nun	nber	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WG75S6	5HFW1	TO247	WG75S65HFW1Q	Tube	30	TO247P	09-Mar-2023

7. Marking

Table 4. Marking codes

Type number	Marking codes
WG75S65HFW1	G75S65 HFW1

8. Limiting values

Table 5. Limiting values

Symbol	Parameter	Notes	Value	Unit
V _{CE}	Collector-emitter voltage, T _j ≥ 25 °C		650	V
I _C	DC collector current, limited by $T_{j(max)}$ T_{c} = 25 °C T_{c} = 100 °C		150 75	А
I _{C(puls)}	Pulsed collector current, t _p limited by T _{j(max)}		225	Α
-	Turn off safe operating area $V_{CE} \le 650 \text{ V}, T_j \le 175 ^{\circ}\text{C}, t_p = 1 \mu\text{s}$		225	А
I _F	Diode forward current, limited by $T_{j(max)}$ T_{C} = 25 °C T_{C} = 100 °C		150 75	А
I _{Fpuls}	Diode pulsed current, t _p limited by T _{j(max)}		225	Α
V_{GE}	Gate-emitter voltage		±20	V
P _{tot}	Power dissipation $T_C = 25 ^{\circ}\text{C}$ Power dissipation $T_C = 100 ^{\circ}\text{C}$		454 227	W
T _{stg}	Storage temperature		-55 to +150	°C
T _{jmax}	Maximum operating junction temperature		175	°C
-	Peak soldering temperture		260	°C
M	Mounting Torque with washer		0.55	Nm

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-c)}	IGBT thermal resistance from junction to case			-	0.33	-	K/W
R _{th(j-c)}	Diode thermal resistance from junction to case			-	0.5	-	K/W
R _{th(j-a)}	thermal resistance from junction to ambient			-	40	-	K/W

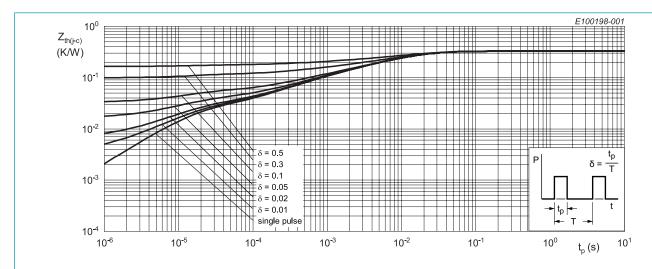


Fig. 1. Transient thermal impedance from junction to case as a function of pulse duration; IGBT

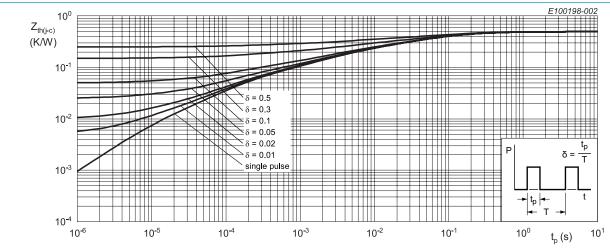


Fig. 2. Transient thermal impedance from junction to case as a function of pulse duration; Diode

10. Characteristics

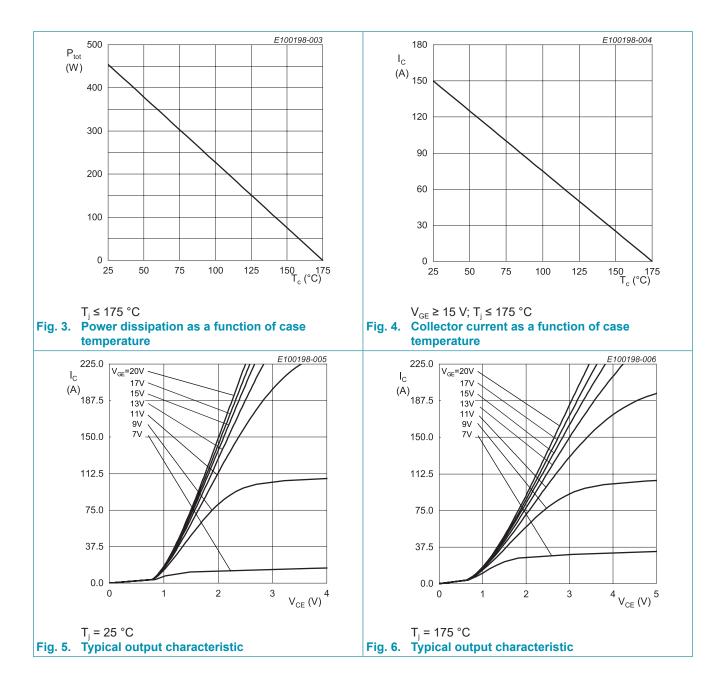
Table 7. Characteristics

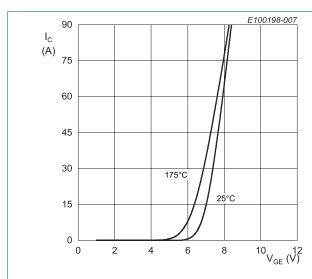
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
BV_CES	Collector-emitter breakdown voltage	$V_{GE} = 0 \text{ V}; I_{C} = 50 \mu\text{A}$		650	-	-	V
$V_{\text{CE(sat)}}$	Collector-emitter saturation	$V_{GE} = 15 \text{ V}; I_{C} = 75 \text{ A}; T_{j} = 25 \text{ °C}$		-	1.5	1.9	V
	voltage	V_{GE} = 15 V; I_{C} = 75 A; T_{j} = 175 °C		-	1.9	-	V
V _F Diode forward voltage		$V_{GE} = 0 \text{ V}; I_F = 75 \text{ A}; T_j = 25 \text{ °C}$		-	1.9	-	V
		V _{GE} = 0 V; I _F = 75 A; T _j = 175 °C		-	1.6	-	V
$V_{\text{GE(th)}}$	Gate-emitter threhold voltage	$I_{\rm C}$ = 0.5 mA; $V_{\rm CE}$ = $V_{\rm GE}$		4	5	6	V
I _{CES}	Zero gate voltage collector current	$V_{CE} = 650 \text{ V}; V_{GE} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	-	100	μA
		$V_{CE} = 650 \text{ V}; V_{GE} = 0 \text{ V}; T_j = 175 ^{\circ}\text{C}$		-	-	1	mA
g _{fs}	Transconductance	V _{CE} = 20 V; I _C = 75 A		-	63	-	S
Dynamic	characteristics						
C _{ies}	Input capacitance	$V_{CE} = 30 \text{ V}; V_{GE} = 0 \text{ V}; f = 1 \text{ MHz};$		-	5571	-	pF
C _{oes}	Output capacitance T _j = 25 °C	I _j = 25 °C		-	92	-	pF
C _{res}	Reverse transfer capacitance			-	65	-	pF
Q_{G}	Gate charge	V_{CC} = 520 V; I_{C} = 75 A; V_{GE} = 15 V; T_{j} = 25 °C		-	237	-	nC

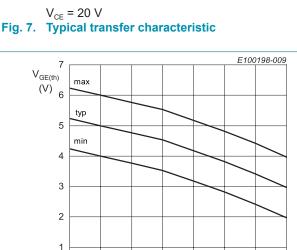
11. Switching Characteristics

Table 8. Switching Characteristics, Inductive Load

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
IGBT cha	racteristics						
$t_{d(on)}$	Turn-on delay time	T _j = 25 °C;		-	59	-	nS
t _r	Rise time	$V_{CC} = 400 \text{ V}; I_C = 75 \text{ A};$ $V_{GE} = 15 \text{ V} / 0 \text{ V};$		-	72	-	nS
$t_{\text{d(off)}}$	Turn-off delay time	$R_{\rm G} = 10 \Omega$		-	324	-	nS
t _f	Fall time			-	68	-	nS
E _{on}	Turn-on energy			-	3.15	-	mJ
E _{off}	Turn-off energy			-	1.95	-	mJ
E _{ts}	Total switching energy			-	5.1	-	mJ
t _{d(on)}	Turn-on delay time	T _j = 175 °C;		-	55	-	nS
t _r	Rise time	$V_{CC} = 400 \text{ V}; I_{C} = 75 \text{ A};$ $V_{GE} = 15 \text{V} / 0 \text{V};$		-	69	-	nS
$t_{d(off)}$	Turn-off delay time	$R_G = 10 \Omega$		-	362	-	nS
t _f	Fall time			-	112	-	nS
E _{on}	Turn-on energy			-	4.5	-	mJ
E _{off}	Turn-off energy			-	2.7	-	mJ
E _{ts}	Total switching energy			-	7.2	-	mJ
Diode cha	aracteristics						
t _{rr}	Reverse recovery time	T _j = 25 °C;		-	44	-	nS
Q _r	Reverse recovery charge	$V_R = 400 \text{ V}; I_F = 75 \text{ A};$ $dI_F/dt = 500 \text{A/us}$		-	221	-	nC
I _{RM}	Reverse recovery peak current			-	9	-	А
t _{rr}	Reverse recovery time	T _j = 175 °C;		-	100	-	nS
Q _r	Reverse recovery charge	$V_R = 400 \text{ V}; I_F = 75 \text{ A};$ $dI_F/dt = 500 \text{A/us}$		-	990	-	nC
I _{RM}	Reverse recovery peak current	F		-	17	-	А



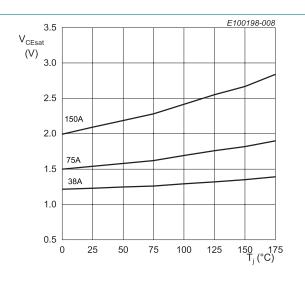




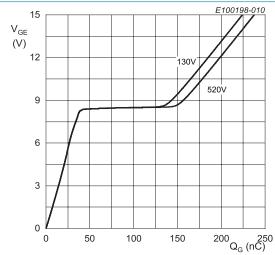
 $I_{\rm C} = 500 \ \mu A$ Fig. 9. Gate-emitter threshold voltage as a function of junction temperature

75

100



 $V_{GE} = 15 \text{ V}$ Fig. 8. Typical collector-emitter saturation voltage as a function of junction temperature



 $I_{c} = 75 A$ Fig. 10. Typical gate charge

150 175 T_j (°C)

125

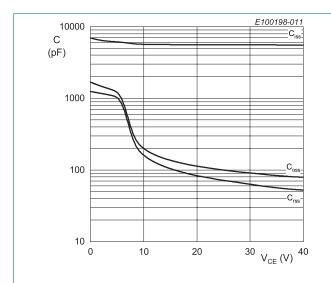
0

0

25

50

E100198-012



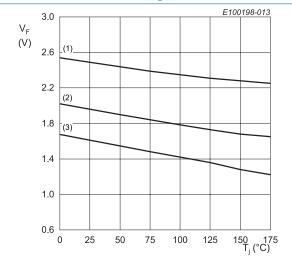
150.0 112.5 175°C 25°C 75.0 37.5 0.0 $^{4}V_{F}\left(V\right) ^{5}$

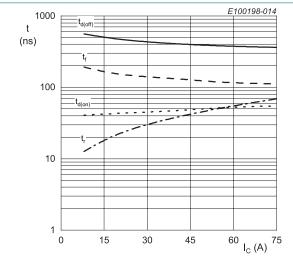
225.0

(A) 187.5

Fig. 12. Typical diode forward current as a function of forward voltage

 $\label{eq:VGE} V_{GE} = 0 \ V; \ f = 1 \ MHz$ Fig. 11. Typical capacitance as a function of collector-emitter voltage



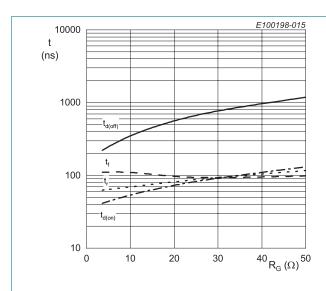


(1) $I_F = 150 A$ (2) $I_F = 75 A$

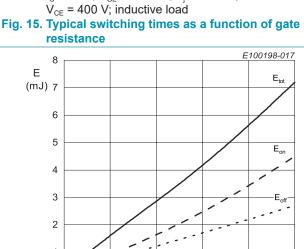
 R_{g} = 10 $\Omega;$ V_{GE} = 15V/0V; T_{j} = 175 °C; V_{CE} = 400 V; inductive load

(3) $I_F = 38 A$ Fig. 13. Typical diode forward voltage as a function of junction temperature

Fig. 14. Typical switching times as a function of collector current

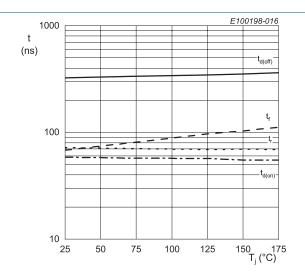


 $I_C = 75 \text{ A}; V_{GE} = 15 \text{V/0V}; T_i = 175 °C;$



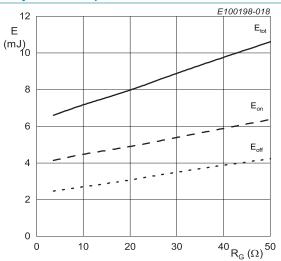
 R_{g} = 10 $\Omega;$ V_{GE} = 15V/0V; T_{j} = 175 °C; V_{CE} = 400 V; inductive load

Fig. 17. Typical switching energy losses as a function of collector current



 I_{C} = 75 A; V_{GE} = 15V/0V; R_{g} = 10 Ω; V_{CE} = 400 V; inductive load

Fig. 16. Typical switching times as a function of junction temperature



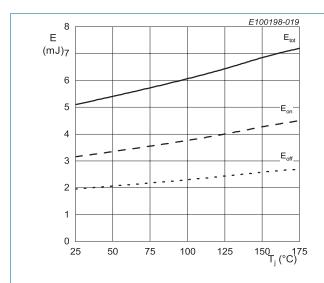
 I_{C} = 75 A; V_{GE} = 15V/0V; T_{j} = 175 °C; V_{CE} = 400 V; inductive load

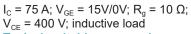
Fig. 18. Typical switching energy losses as a function of gate resistance

 60 I_{C} (A) 75

0

0





1000 I_C (A) 100 10 10 t_p = 0.001ms 0.2ms 0.5ms 1 ms 1

Fig. 20. Forward bias safe operating area



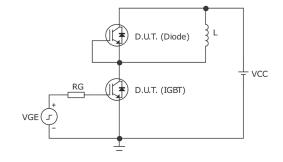


Fig. 21. Test circuit for inductive load switching

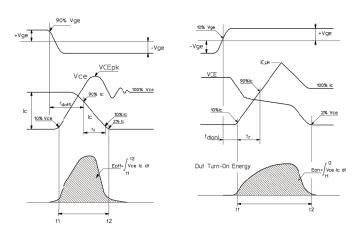
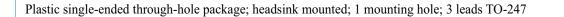
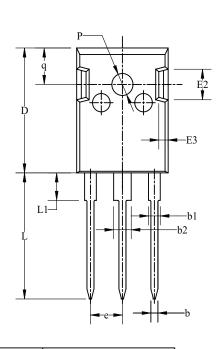


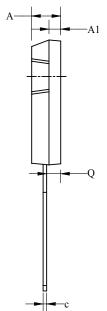
Fig. 22. Definition of switching times and losses

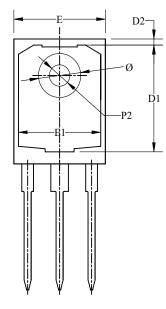
12. Package outline



TO247







Dim	All Dimensions in Millimeters			
D.III	Min	Тур	Max	
A	4.70	4.95	5.20	
A1	1.90	2.00	2.10	
b	1.00	1.20	1.40	
b1	1.80	2.00	2.20	
b2	2.80	3.00	3.20	
с	0.50	0.60	0.70	
D	20.30	20.45	20.60	
D1	17.28	17.48	17.68	
D2	0.80	1.00	1.20	
Е	15.45	15.60	15.75	
E1	13.82	14.02	14.22	
E2	4.80	5.00	5.20	
E3	1.40	1.60	1.80	
e		5.45 BSC		
L	20.40	20.65	20.90	
L1	4.25	4.50	4.75	
P2	3.40	3.50	3.60	
P	3.50	3.60	3.70	
Q	2.20	2.40	2.60	
q	5.78	5.98	6.18	
Ø	7.10	7.19	7.30	

IGRT

13. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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