

## 1. General description

W2MS100N028W is a low voltage N-channel MOSFET in TO247 package, which utilizes the split gate technology to provide superior FOM  $R_{DS(on)} \cdot Q_g$  among silicon based MOSFETs. It is particularly suitable for applications require extreme high efficiency and power density.



## 2. Features and benefits

- Very low on-resistance  $R_{DS(on)}$
- Excellent gate charge x  $R_{DS(on)}$  product (FOM)
- 100% avalanche tested
- Qualified according to JEDEC criteria

## 3. Applications

- BLDC Motor drive applications
- Battery powered circuits
- Synchronous rectifier applications
- Resonant mode power supplies

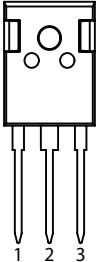
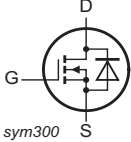
## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Notes	Values			Unit
Absolute maximum rating							
V <sub>DS</sub>	drain-source voltage			100			V
V <sub>GS</sub>	gate-source voltage	static		±20			V
I <sub>D</sub>	continuous drain current	T <sub>C</sub> = 25 °C		175			A
P <sub>tot</sub>	power dissipation	T <sub>C</sub> = 25 °C		297			W
T <sub>j</sub>	junction temperature			-55 to 150			°C
Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
R <sub>DS(on)</sub>	drain-source on-state resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 60 A		-	2.0	2.8	mΩ
Dynamic characteristics							
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 60 A; V <sub>DS</sub> = 50 V; V <sub>GS</sub> = 10 V		-	190	-	nC

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	drain		
3	S	source		
mb	D	mounting base; connected to drain		

6. Ordering information

Table 3. Ordering information

Type number	Package name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
W2MS100N028W	TO247	W2MS100N028WQ	Tube	30	TO247N	20-July-2016

7. Marking

Table 4. Marking codes

Type number	Marking codes
W2MS100N028W	W2MS 100N028W

8. Limiting values

Table 5. Limiting values  
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Notes	Values	Unit
$V_{DS}$	drain-source voltage			100	V
$V_{GS}$	gate-source voltage	static		$\pm 20$	V
$I_D$	continuous drain current	$T_C = 25\text{ }^{\circ}\text{C}$		175	A
		$T_C = 100\text{ }^{\circ}\text{C}$		157	A
$I_{DM}$	pulsed drain current	$T_C = 25\text{ }^{\circ}\text{C}$		760	A
$P_{tot}$	power dissipation	$T_C = 25\text{ }^{\circ}\text{C}$		297	W
$E_{AS}$	single pulse drain-to-source avalanche	$I_{AS} = 44\text{ A}$ ; $R_{GS} = 25\text{ }\Omega$ ; $V_{DD} = 50\text{ V}$ ; $T_J = 25\text{ }^{\circ}\text{C}$		484	mJ
$I_{AS}$	avalanche current, single pulse			44	A
$T_{stg}$	storage temperature			-55 to 150	$^{\circ}\text{C}$
$T_J$	junction temperature			-55 to 150	$^{\circ}\text{C}$

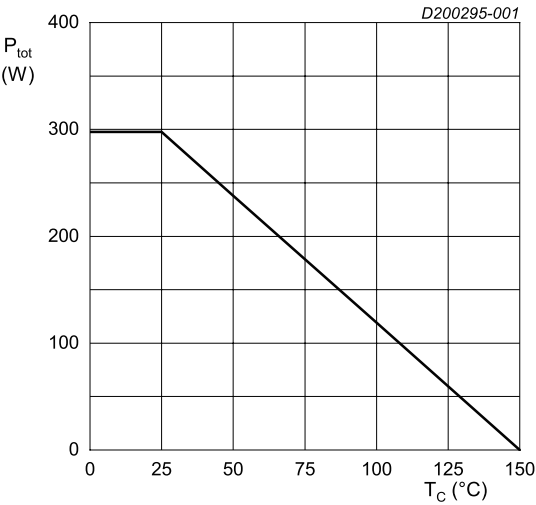


Fig. 1. Total power dissipation as a function of mounting base temperature

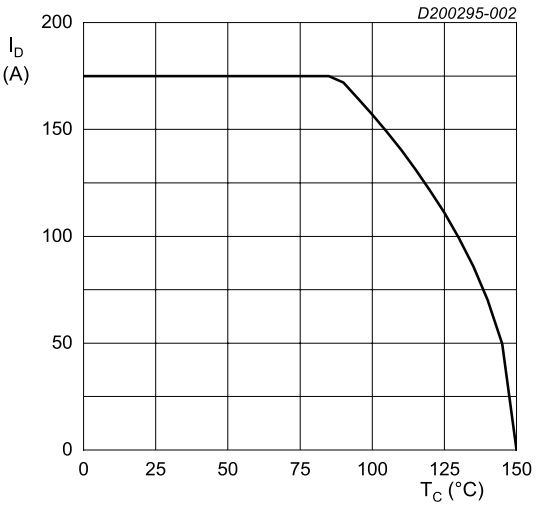
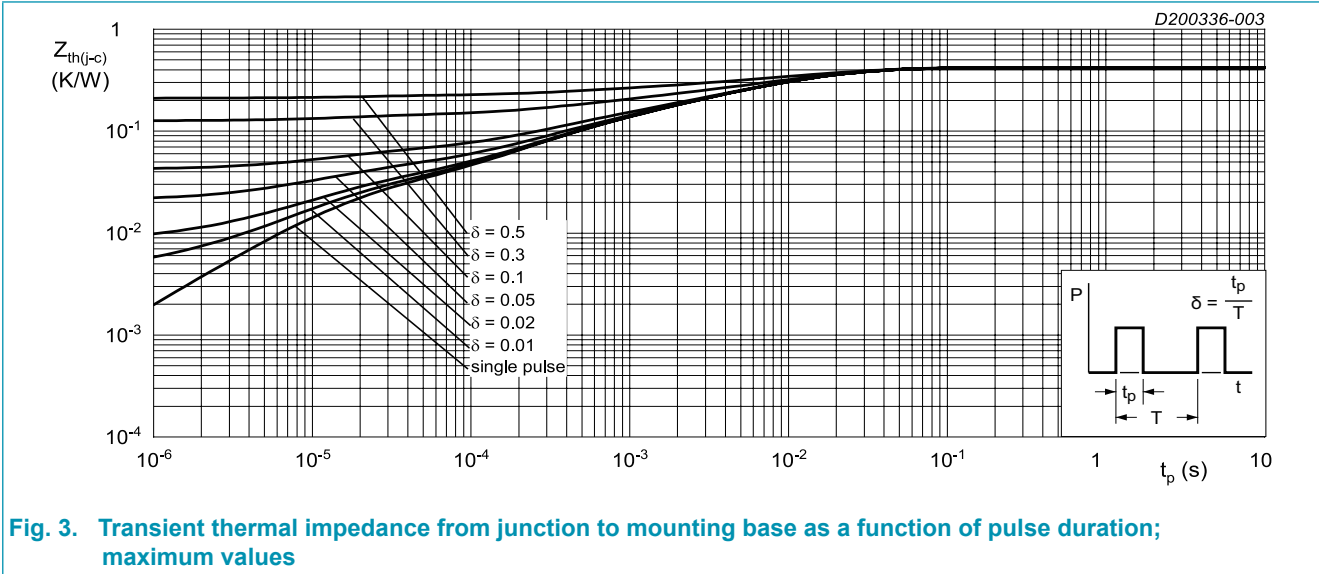


Fig. 2. Continuous Drain Current as a function of mounting base temperature

9. Thermal & Mechanical characteristics

Table 6. Thermal & Mechanical characteristics

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
$R_{th(j-c)}$	thermal resistance from junction to case			-	0.3	0.42	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air		-	60	-	K/W



## 10. Characteristics

**Table 7. Characteristics**

$T_j = 25\text{ }^{\circ}\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Notes	Min	Typ	Max	Unit
Static characteristics							
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V		100	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	I <sub>D</sub> = 250 μA; V <sub>DS</sub> = V <sub>GS</sub>		2.0	-	4.0	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 100 V; V <sub>GS</sub> = 0 V		-	-	1	μA
		V <sub>DS</sub> = 100 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 150 °C		-	-	100	μA
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = ±20 V; V <sub>DS</sub> = 0 V		-	-	±100	nA
R <sub>DS(on)</sub>	drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 60 A		-	2.0	2.8	mΩ
R <sub>G</sub>	gate resistance	f = 1 MHz		-	1.6	-	Ω
g <sub>fs</sub>	transconductance	V <sub>DS</sub> = 5 V; I <sub>D</sub> = 60 A; T <sub>j</sub> = 25 °C		-	141	-	S
Dynamic characteristics							
Q <sub>G(tot)</sub>	total gate charge	I <sub>D</sub> = 60 A; V <sub>DS</sub> = 50 V; V <sub>GS</sub> = 10 V		-	190	-	nC
Q <sub>GS</sub>	gate-source charge			-	56	-	nC
Q <sub>GD</sub>	gate-drain charge			-	44	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 50 V; V <sub>GS</sub> = 0 V; f = 1 MHz		-	12716	-	pF
C <sub>oss</sub>	output capacitance			-	2986	-	pF
C <sub>rss</sub>	reverse transfer capacitance			-	441	-	pF
t <sub>d(on)</sub>	turn-on delay time	V <sub>DS</sub> = 50 V; V <sub>GS</sub> = 10 V; R <sub>G</sub> = 1.5 Ω; I <sub>D</sub> = 60 A		-	29	-	ns
t <sub>r</sub>	rise time			-	33	-	ns
t <sub>d(off)</sub>	turn-off delay time			-	59	-	ns
t <sub>f</sub>	fall time			-	22	-	ns
Source-drain diode							
V <sub>SD</sub>	source-drain voltage	V <sub>GS</sub> = 0 V; I <sub>S</sub> = 60 A		-	0.89	-	V
I <sub>S</sub>	body-diode continuous current	T <sub>C</sub> = 25 °C		-	-	175	A
t <sub>rr</sub>	reverse recovery time	V <sub>R</sub> = 50 V; I <sub>F</sub> = 60 A; dI <sub>F</sub> /dt = 100 A/μs		-	67	-	ns
Q <sub>rr</sub>	reverse recovered charge			-	145	-	nC
I <sub>rrm</sub>	reverse recovery current			-	3.3	-	A

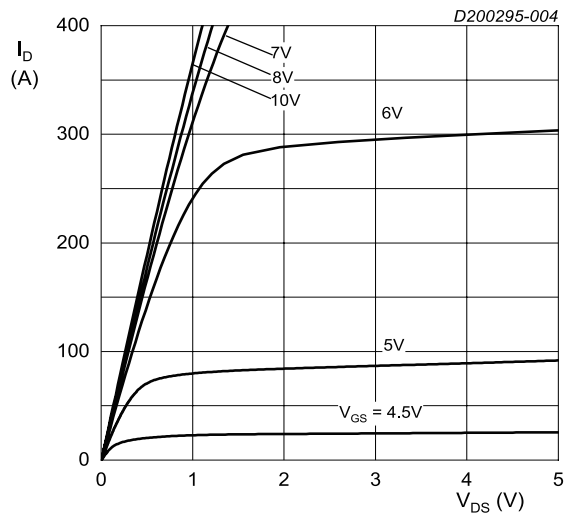


Fig. 4. Drain current as a function of drain-source voltage; typical values

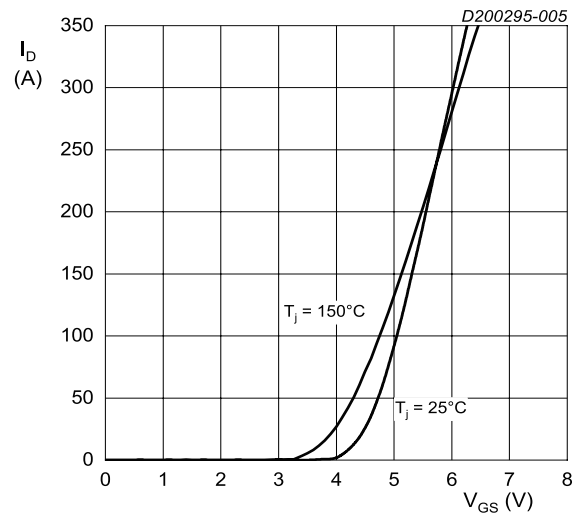


Fig. 5. Drain current as a function of gate-source voltage; typical values  
 $V_{DS} = 5$  V

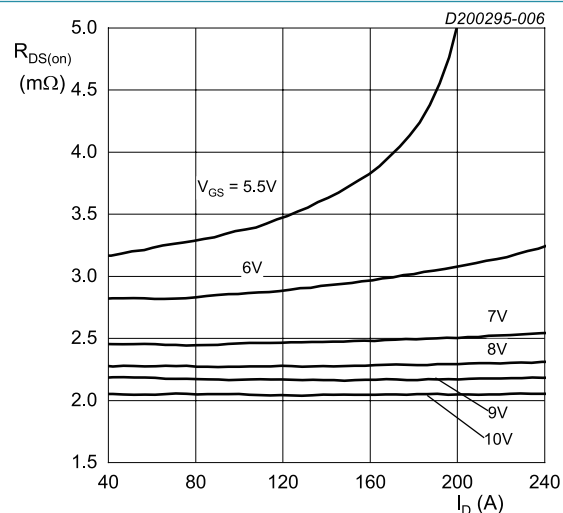


Fig. 6. Drain-source on-state resistance as a function of drain current; typical values

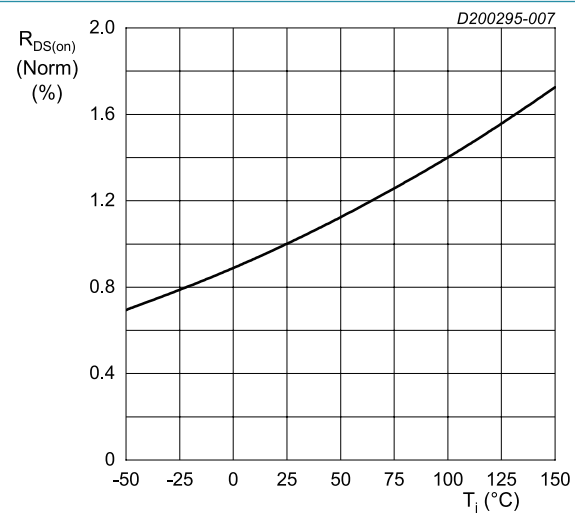
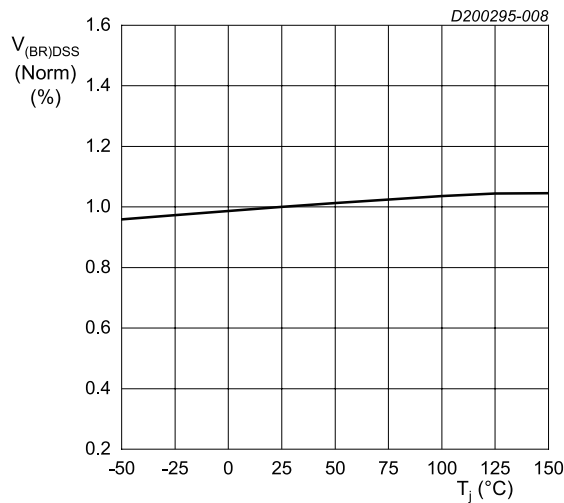
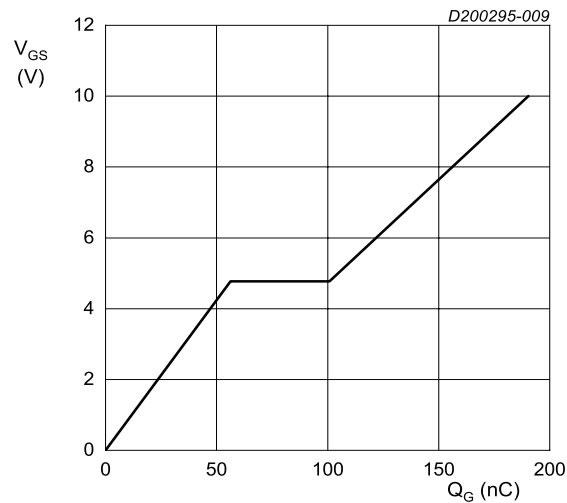


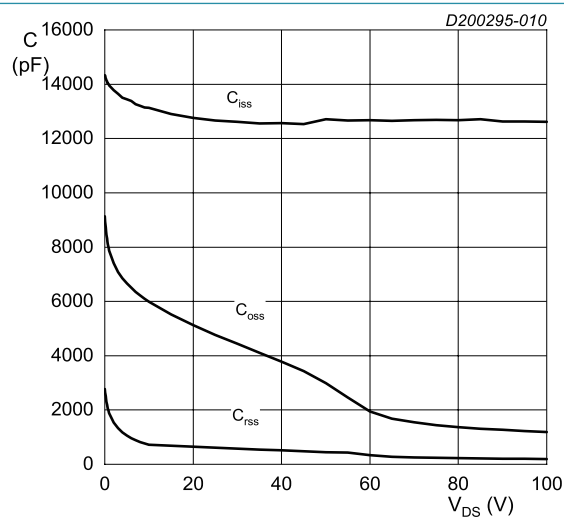
Fig. 7. Normalized drain-source on-state resistance as a function of junction temperature  
 $V_{GS} = 10$  V;  $I_D = 60$  A



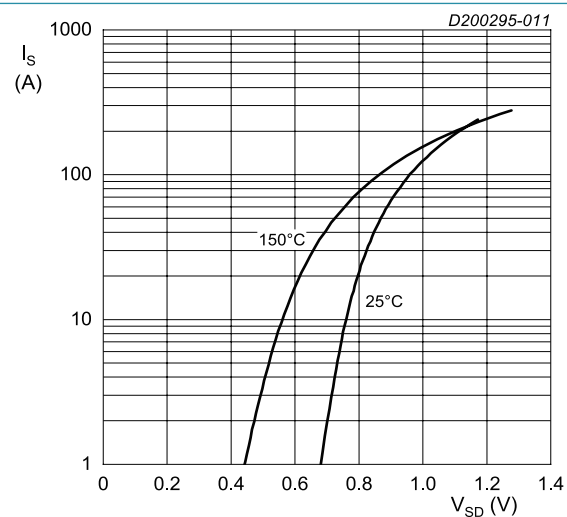
$I_D = 250 \mu A$   
**Fig. 8. Normalized drain-source breakdown voltage as a function of junction temperature**



$I_D = 60 A; V_{DS} = 50 V$   
**Fig. 9. Gate-source voltage as a function of gate charge; typical values**



$V_{GS} = 0 V; f = 1 MHz$   
**Fig 10. Capacitances as a function of drain-source voltage; typical values**



$V_{GS} = 0 V$   
**Fig 11. Source current as a function of source-drain voltage; typical values**

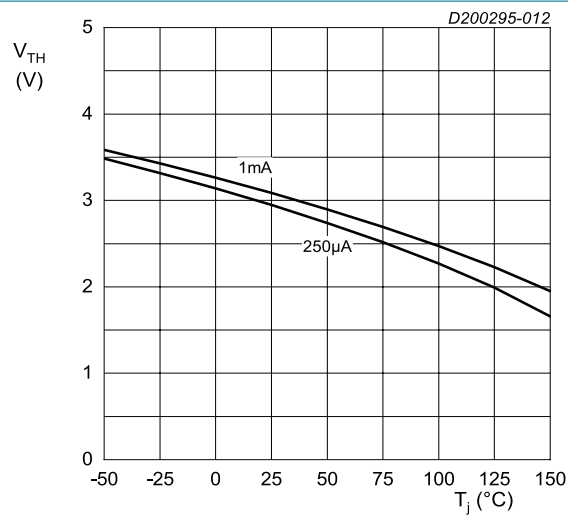
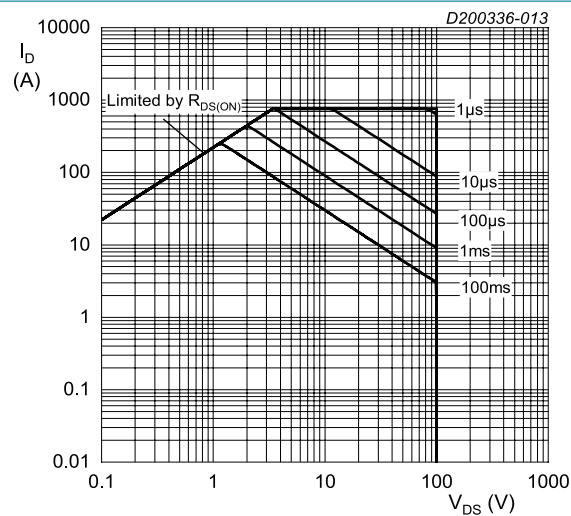


Fig. 12. Gate threshold voltage as a function of junction temperature



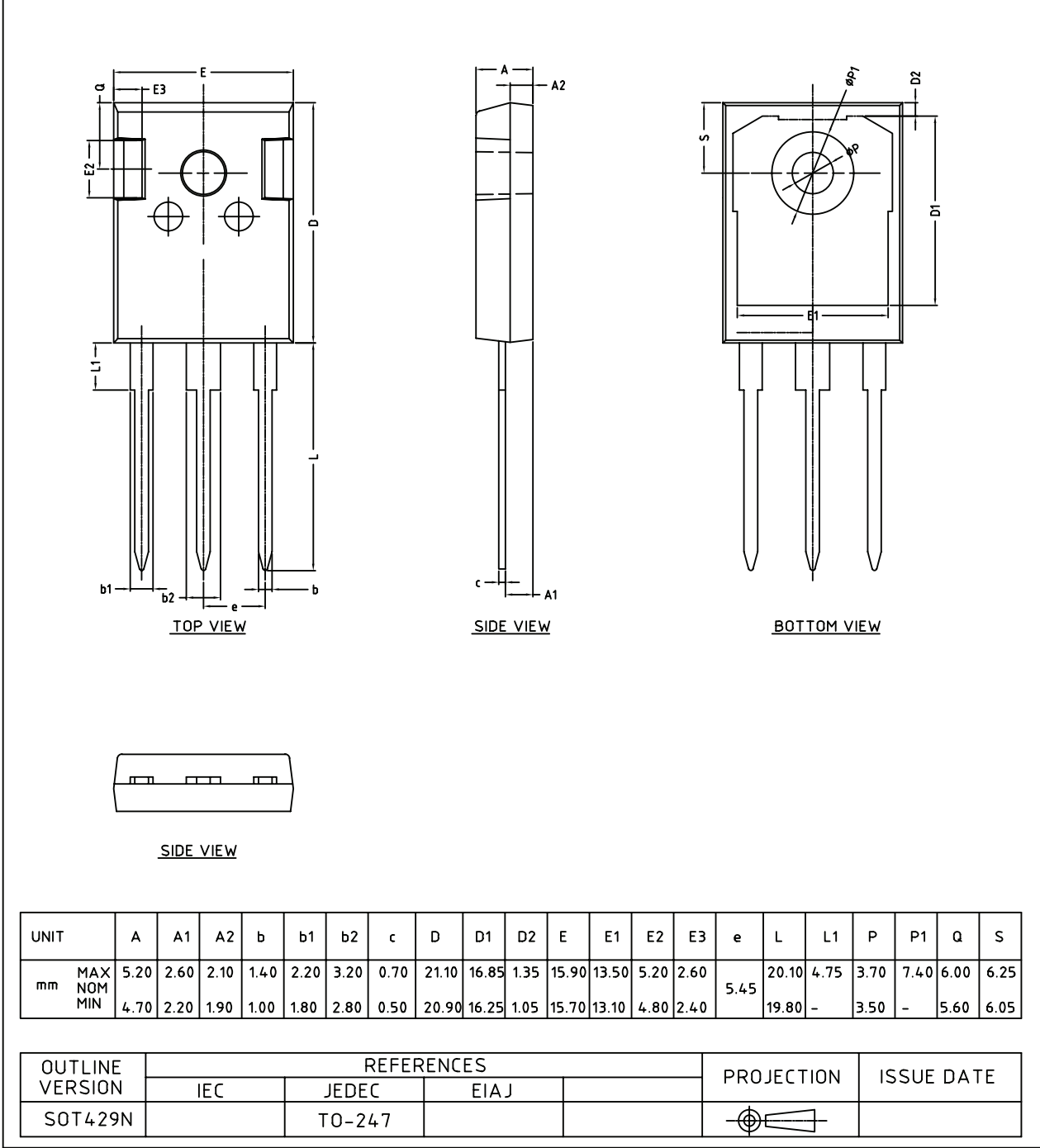
$T_C = 25^{\circ}C$   
Fig. 13. Safe operating area



11. Package outline

Plastic single-ended through-hole package; heatsink mounted; 1 mounting hole; 3-lead TO-247

SOT429N



## 12. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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