

WG40N120UFW1

Rev.01 - 12 June 2024

IGBT

Preliminary data sheet

1. General description

WG40N120UFW1 uses advanced Fine Trench Field-stop IGBT technology with anti-parallel diode in TO-247 package. This device is part of the Ultra-Fast series of IGBTs, which represents an optimum compromise between conduction and switching losses to maximize the efficiency of high switching frequency converter.



2. Features and benefits

- Maximum junction temperature 175 °C
- Ultra-Fast switching series
- · Positive Temperature efficient for Easy Parallel Operating
- · Very soft, fast recovery anti-parallel diode
- EMI Improved Design

3. Applications

- Solar inverter
- PFC
- · Welding converters
- UPS
- Mid to high switching frequency applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter		Notes	Value			Unit
V_{CE}	Collector-emitter voltage, $T_j \ge 25 \text{ °C}$			1200		V	
I _c	DC collector current, limited by $T_{j(max)}$ T _c = 100 °C				40		A
Symbol	Parameter Conditions		Notes	Min	Тур	Max	Unit
Static cha	racteristics						
$V_{\text{CE(sat)}}$	Collector-emitter saturation voltage	V _{GE} = 15 V; I _C = 40 A; T _j = 25 °C		-	1.75	2.15	V

5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		۹C
2	С	collector		
3	E	emitter		
mb	C	mounting base; connected to collector		G E sym200

6. Ordering information

Table 3. Ordering information								
Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date		
WG40N120UFW1	TO247	WG40N120UFW1Q	Tube	30	TO247P	09-Mar-2023		

7. Marking

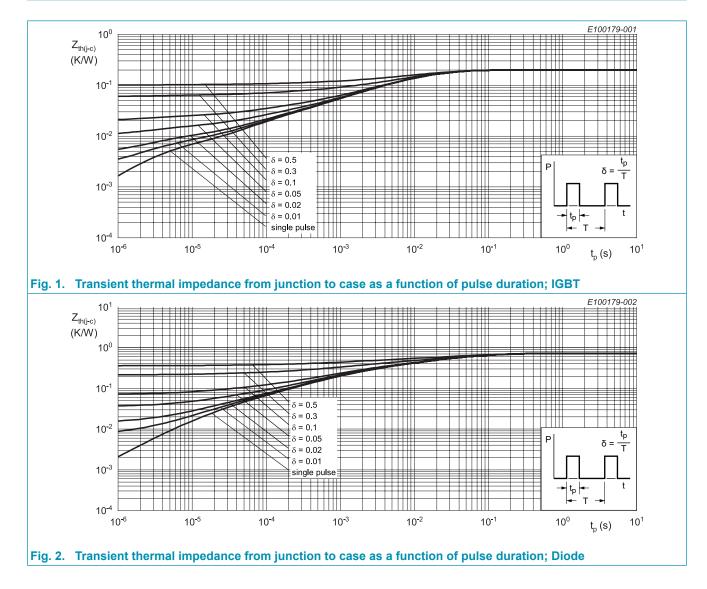
Table 4. Marking codes	
Type number	Marking codes
WG40N120UFW1	G40N120 UFW1

8. Limiting values

able 5. Li	miting values			
Symbol	Parameter	Notes	Value	Unit
V_{CE}	Collector-emitter voltage, $T_j \ge 25 \text{ °C}$		1200	V
I _c	DC collector current, limited by $T_{j(max)}$ T _c = 25 °C T _c = 100 °C		80 40	A
$I_{C(puls)}$	Pulsed collector current, $t_{\rm p}$ limited by $T_{j(max)}$		120	А
-	Turn off safe operating area $V_{CE} \le 1200 \text{ V}, \text{ T}_{j} \le 125 \text{ °C}, \text{ t}_{p} = 1 \mu\text{s}$		120	A
I _F	Diode forward current, limited by $T_{j(max)}$ T _c = 25 °C T _c = 100 °C		80 40	A
I _{Fpuls}	Diode pulsed current, t_p limited by $T_{j(max)}$		120	А
V_{GE}	Gate-emitter voltage		±20	V
P _{tot}	Power dissipation $T_c = 25 \degree C$ Power dissipation $T_c = 100 \degree C$		750 375	W
T _{stg}	Storage temperature		-55 to +150	°C
T _{jmax}	Maximum operating junction temperature		175	°C
-	Peak soldering temperture		260	°C
М	Mounting Torque with washer		0.55	Nm

9. Thermal characteristics

Table 6. Thermal characteristics							
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-c)}	IGBT thermal resistance from junction to case			-	0.20	-	K/W
$R_{th(j-c)}$	Diode thermal resistance from junction to case			-	0.72	-	K/W
R _{th(j-a)}	thermal resistance from junction to ambient			-	40	-	K/W

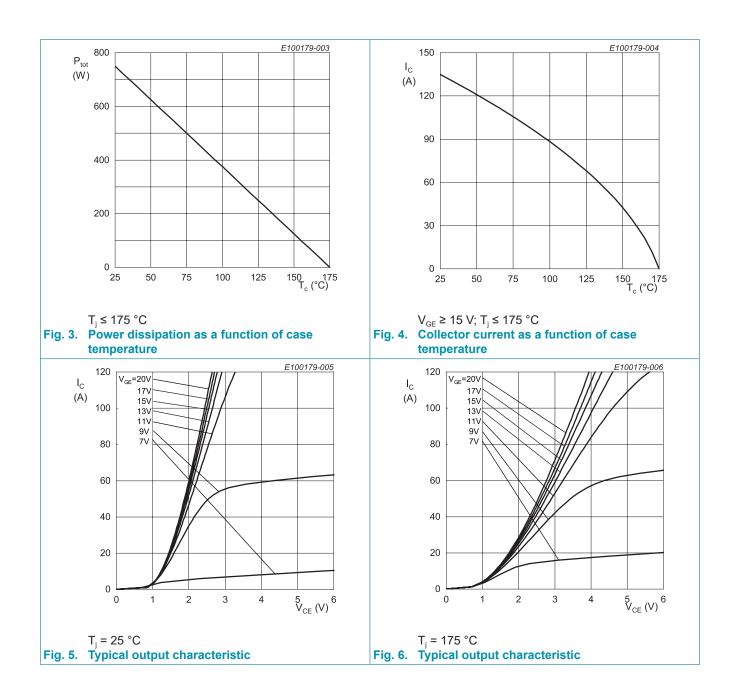


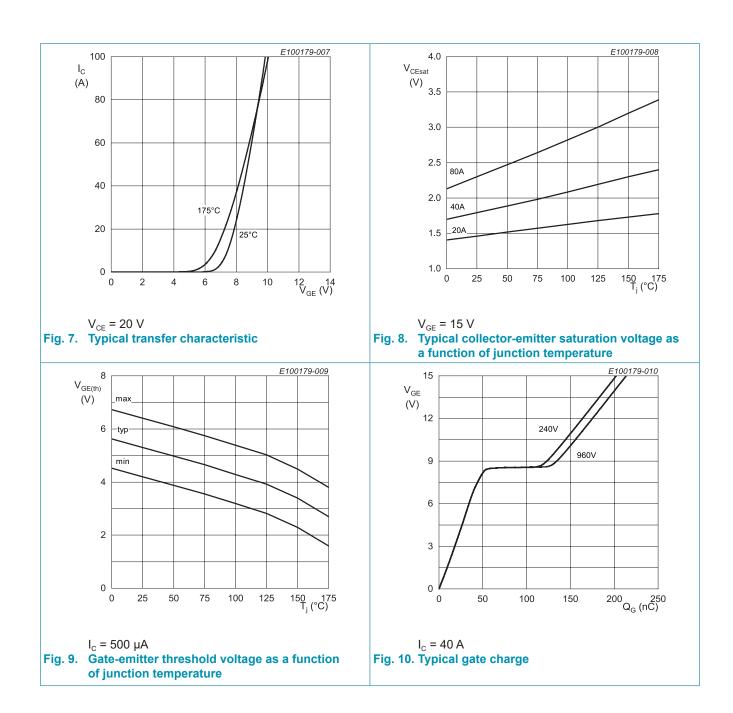
10. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	aracteristics						
BV _{CES}	Collector-emitter breakdown voltage	V_{GE} = 0 V; I _C = 1.0 mA		1200	-	-	V
V _{CE(sat)}	Collector-emitter saturation	V _{GE} = 15 V; I _C = 40 A; T _j = 25 °C		-	1.75	2.15	V
	voltage	V _{GE} = 15 V; I _C = 40 A; T _j = 175 °C		-	2.4	- V - V - V 6.4 V	
V _F Dic	Diode forward voltage	V _{GE} = 0 V; I _F = 40 A; T _j = 25 °C		-	2.3	-	V
		V _{GE} = 0 V; I _F = 40 A; T _j = 175 °C		-	2.1	-	V
$V_{GE(th)}$	Gate-emitter threhold voltage	I_{c} = 0.5 mA; V_{ce} = V_{ge}		4.2	5.3	6.4	V
I _{CES}	Zero gate voltage collector	V_{CE} = 1200 V; V_{GE} = 0 V; T_{j} = 25 °C		-	-	250	μA
	current	V _{CE} = 1200 V; V _{GE} = 0 V; T _j = 175 °C		-	-	10	mA
g _{fs}	Transconductance	V _{CE} = 20 V; I _C = 40 A		-	30	-	S
Dynamic	characteristics						
C _{ies}	Input capacitance	V _{CE} = 30 V; V _{GE} = 0 V; f = 1 MHz;		-	7372	-	pF
C _{oes}	Output capacitance	T _j = 25 °C		-	91	-	pF
C _{res}	Reverse transfer capacitance			-	27	-	pF
Q _G	Gate charge	V _{CC} = 960 V; I _C = 40 A; V _{GE} = 15 V; T _i = 25 °C		-	213	-	nC

11. Switching Characteristics

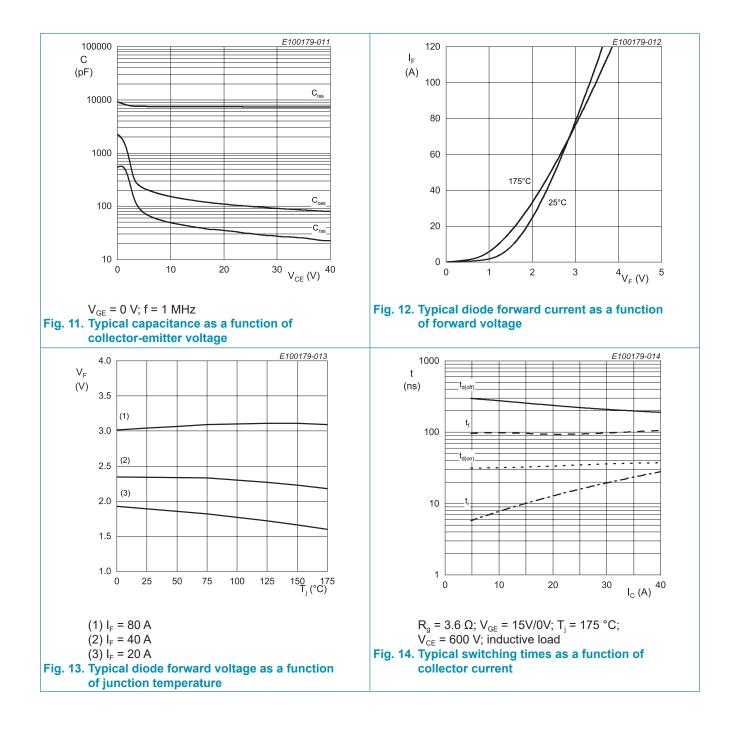
Table 8. Sv	witching Characteristics, Ir	nductive Load					
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
IGBT cha	racteristics			1		1	
t _{d(on)}	Turn-on delay time	T _j = 25 °C; V _{cc} = 600 V; I _c = 40 A;		-	39	-	nS
t _r	Rise time	$V_{CC} = 500 \text{ V}, 1_{C} = 40 \text{ A},$ $V_{GE} = 15 \text{ V} / 0 \text{ V};$		-	28	-	nS
$t_{\rm d(off)}$	Turn-off delay time	$R_{G} = 3.6 \Omega$		-	148	-	nS
t _f	Fall time			-	45	-	nS
E _{on}	Turn-on energy			-	2.0	-	mJ
E _{off}	Turn-off energy			-	1.05	-	mJ
E _{ts}	Total switching energy			-	3.05	-	mJ
t _{d(on)}	Turn-on delay time	T _j = 175 °C;		-	37	-	nS
t _r	Rise time	V _{cc} = 600 V; I _c = 40 A; V _{GE} = 15V / 0V;		-	28	-	nS
$t_{d(off)}$	Turn-off delay time	$R_{\rm g} = 3.6 \ \Omega$		-	190	-	nS
t _f	Fall time			-	106	-	nS
E _{on}	Turn-on energy			-	3.1	-	mJ
E _{off}	Turn-off energy			-	1.9	-	mJ
E _{ts}	Total switching energy			-	5.0	-	mJ
Diode cha	aracteristics					1	
t _{rr}	Reverse recovery time	T _j = 25 °C;		-	200	-	nS
Q _r	Reverse recovery charge	$V_{R} = 600 \text{ V}; I_{F} = 40 \text{ A};$ dI _F /dt = 500A/us		-	1660	-	nC
I _{RM}	Reverse recovery peak current			-	16	-	A
t _{rr}	Reverse recovery time	T _j = 175 °C;		-	453	-	nS
Q _r	Reverse recovery charge	$V_{R} = 600 \text{ V}; I_{F} = 40 \text{ A};$ dI _F /dt = 500A/us		-	5566	-	nC
I _{RM}	Reverse recovery peak current			-	26	-	А





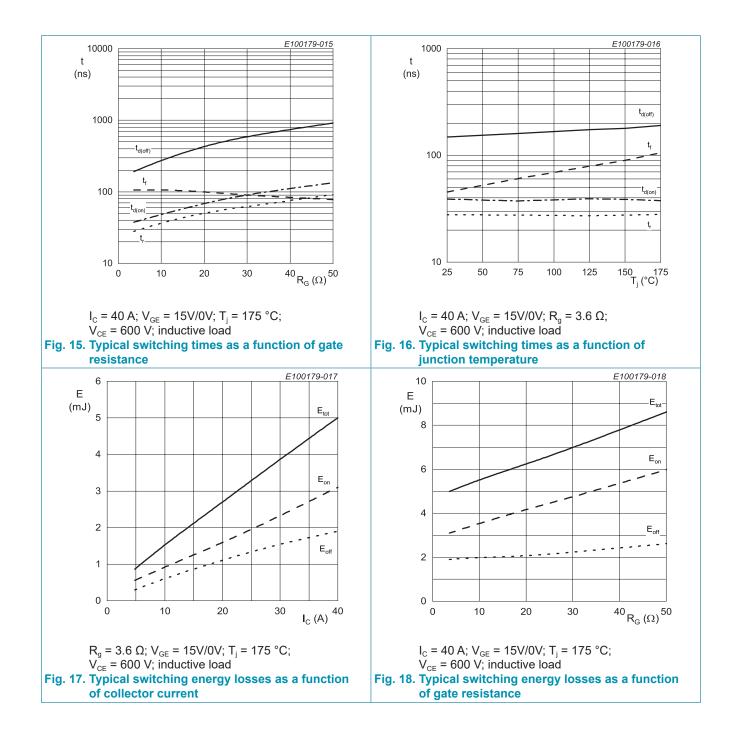
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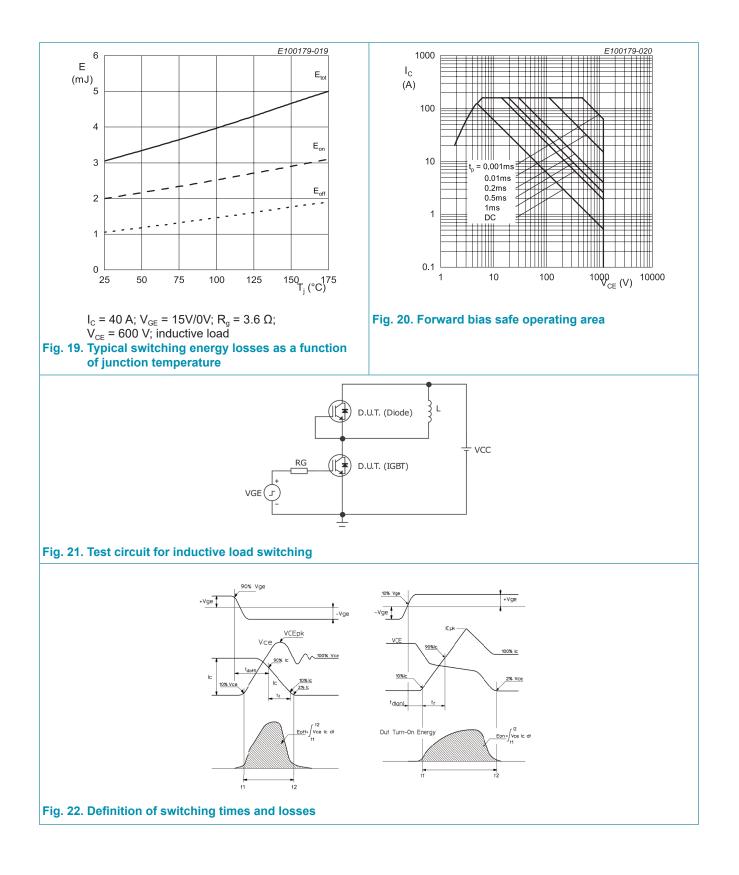
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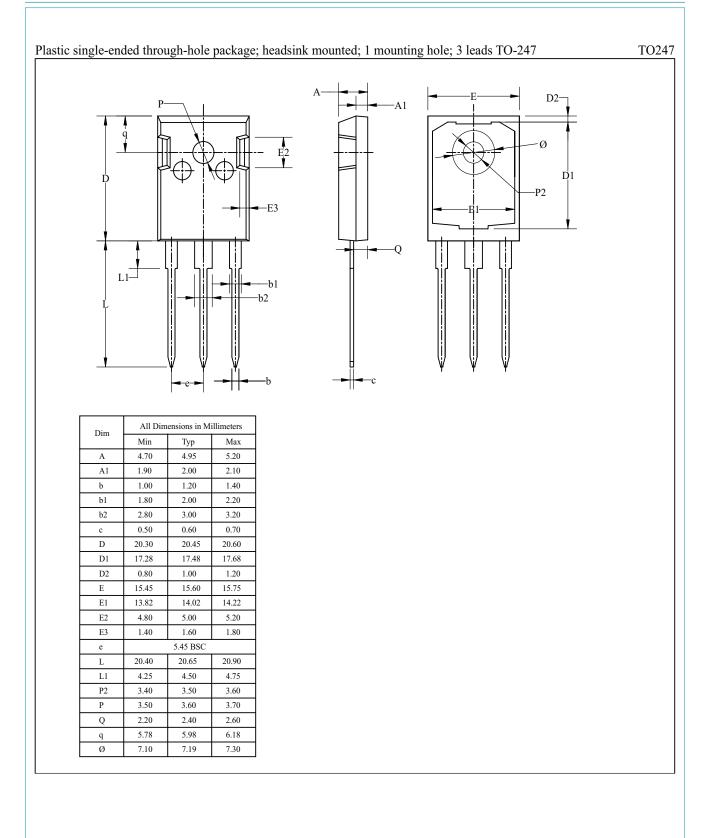
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12. Package outline



WG40N120UFW1 Preliminary data sheet

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13. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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