Product data sheet

1. General description

WG50N65HSW1 uses advanced Fine Trench Field-stop IGBT technology with anti-parallel SiC diode in TO247 package to provide extremely low Vce(sat), and excellent switching performance. This device offers Best-in-Class efficiency in hard switching and resonant topology.





2. Features and benefits

- · Maximum junction temperature 175 °C
- · Positive Temperature efficient for easy paralleling
- · Anti-parallel SiC diode
- · High switching speed
- · EMI Improved Design

3. Applications

- PFC
- Solar converters
- UPS
- Welding Converters
- · Mid to high range switching frequency converters

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter		Notes	Value			Unit
V _{CE}	Collector-emitter voltage, T _j ≥ 25 °C			650		V	
I _C	DC collector current, limited by $T_{j(max)}$ $T_C = 100~^{\circ}C$				50		А
Symbol	Parameter Conditions		Notes	Min	Тур	Max	Unit
Static cha	racteristics						
V _{CE(sat)}	Collector-emitter saturation voltage	$V_{GE} = 15 \text{ V}; I_{C} = 50 \text{ A}; T_{j} = 25 \text{ °C}$		-	1.55	1.95	V

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		•C
2	С	collector		
3	E	emitter		
mb	С	mounting base; connected to collector	TO247	G E sym200

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WG50N65HSW1	TO247	WG50N65HSW1Q	Tube	30	SOT429	25-Mar-2013

7. Marking

Table 4. Marking codes

Type number	Marking codes
WG50N65HSW1	G50N65 HSW1

8. Limiting values

Table 5. Limiting values

Symbol	Parameter	Notes	Value	Unit
V_{CE}	Collector-emitter voltage, T _j ≥ 25 °C		650	V
I _C	DC collector current, limited by $T_{j(max)}$ T_{c} = 25 °C T_{c} = 100 °C		100 50	А
I _{C(puls)}	Pulsed collector current, t _p limited by T _{j(max)}		150	Α
-	Turn off safe operating area $V_{CE} \le 650 \text{ V}, T_j \le 175 ^{\circ}\text{C}, t_p = 1 \mu\text{s}$		150	А
I _F	Diode forward current, limited by $T_{j(max)}$ T_{c} = 25 °C T_{c} = 100 °C		50 25	А
I _{Fpuls}	Diode pulsed current, t _p limited by T _{j(max)}		150	А
V_{GE}	Gate-emitter voltage		±20	V
P _{tot}	Power dissipation $T_C = 25 ^{\circ}\text{C}$ Power dissipation $T_C = 100 ^{\circ}\text{C}$		454 227	W
T _{stg}	Storage temperature		-55 to +150	°C
T _{jmax}	Maximum operating junction temperature		175	°C
-	Peak soldering temperture		260	°C
M	Mounting Torque with washer		0.55	Nm

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-c)}	IGBT thermal resistance from junction to case			-	0.33	-	K/W
R _{th(j-c)}	Diode thermal resistance from junction to case			-	1.0	-	K/W
R _{th(j-a)}	thermal resistance from junction to ambient			-	40	-	K/W

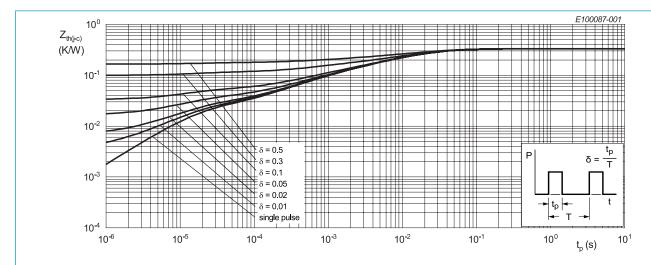


Fig. 1. Transient thermal impedance from junction to case as a function of pulse duration; IGBT

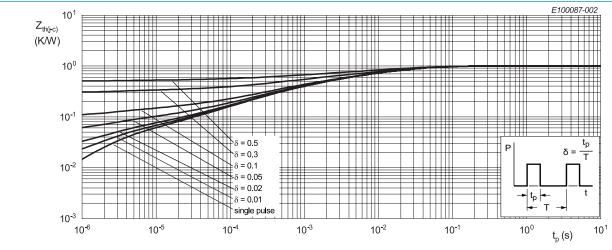


Fig. 2. Transient thermal impedance from junction to case as a function of pulse duration; Diode

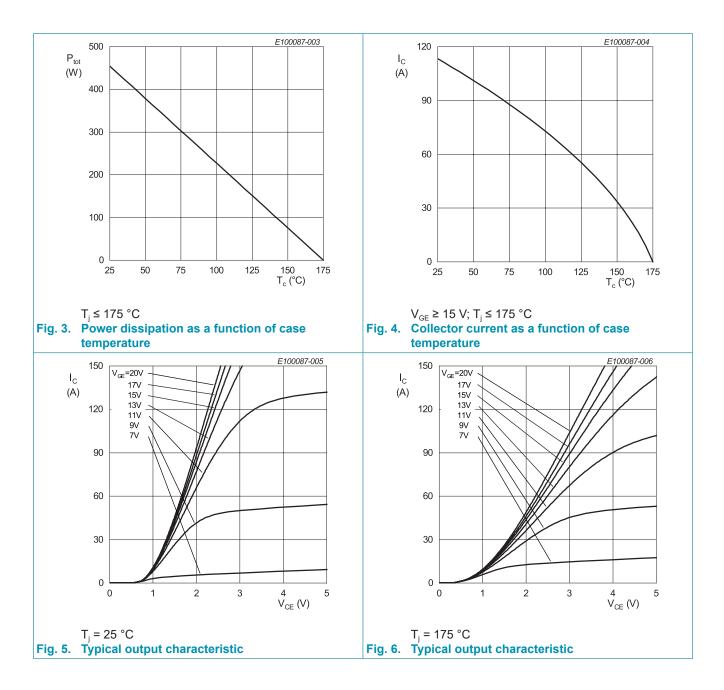
10. Characteristics

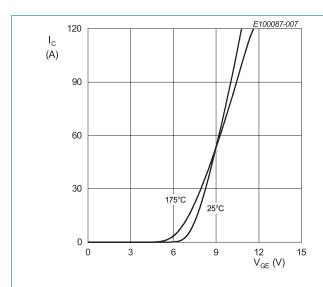
Table 7. Characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
BV _{CES}	Collector-emitter breakdown voltage	$V_{GE} = 0 \text{ V}; I_{C} = 1.0 \text{ mA}$		650	-	-	V
OL(Sat)	Collector-emitter saturation	$V_{GE} = 15 \text{ V}; I_C = 50 \text{ A}; T_j = 25 ^{\circ}\text{C}$		-	1.55	1.95	V
	voltage	V_{GE} = 15 V; I_{C} = 50 A; T_{j} = 175 °C		-	2.1	-	V
V _F	V _F Diode forward voltage	$V_{GE} = 0 \text{ V}; I_F = 25 \text{ A}; T_j = 25 \text{ °C}$		-	1.35	-	V
		V _{GE} = 0 V; I _F = 25 A; T _j = 175 °C		-	1.65	-	V
$V_{\text{GE(th)}}$	Gate-emitter threhold voltage	$I_{\rm C}$ = 0.5 mA; $V_{\rm CE}$ = $V_{\rm GE}$		4.2	5.2	6.2	V
I _{CES}	Zero gate voltage collector	$V_{CE} = 650 \text{ V}; V_{GE} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	-	100	μA
	current	$V_{CE} = 650 \text{ V}; V_{GE} = 0 \text{ V}; T_j = 175 ^{\circ}\text{C}$		-	-	1	mA
g _{fs}	Transconductance	V _{CE} = 20 V; I _C = 50 A		-	34	-	S
Dynamic	characteristics						
C _{ies}	Input capacitance	$V_{CE} = 30 \text{ V}; V_{GE} = 0 \text{ V}; f = 1 \text{ MHz};$		-	2955	-	pF
C _{oes}	Output capacitance	T _j = 25 °C		-	105	-	pF
C _{res}	Reverse transfer capacitance			-	41	-	pF
Q_{G}	Gate charge	V_{CC} = 520 V; I_{C} = 50 A; V_{GE} = 15 V; T_{i} = 25 °C		-	138	-	nC

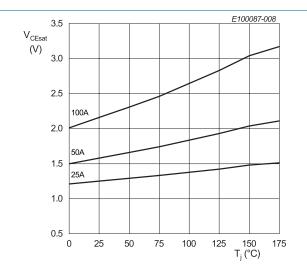
11. Switching Characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
IGBT cha	racteristics				'		
$t_{d(on)}$	Turn-on delay time	T _j = 25 °C;		-	44	-	nS
t _r	Rise time	$V_{CC} = 400 \text{ V}; I_C = 50 \text{ A}; V_{GE} = 15 \text{V} / 0 \text{V};$ $R_G = 10 \Omega$		-	50	-	nS
$t_{d(off)}$	Turn-off delay time			-	190	-	nS
t _f	Fall time			-	33	-	nS
E _{on}	Turn-on energy			-	1.05	-	mJ
E _{off}	Turn-off energy			-	0.65	-	mJ
E _{ts}	Total switching energy			-	1.7	-	mJ
t _{d(on)}	Turn-on delay time	T _j = 175 °C;		-	40	-	nS
t _r	Rise time	$V_{CC} = 400 \text{ V}; I_{C} = 50 \text{ A}; V_{GE} = 15 \text{V} / 0 \text{V};$ $R_{G} = 10 \Omega$		-	54	-	nS
$t_{d(off)}$	Turn-off delay time			-	211	-	nS
t _f	Fall time			-	42	-	nS
E _{on}	Turn-on energy			-	1.25	-	mJ
E _{off}	Turn-off energy			-	0.85	-	mJ
E _{ts}	Total switching energy			-	2.1	-	mJ
Diode cha	aracteristics				'		
t _{rr}	Reverse recovery time	T _j = 25 °C;		-	22	-	nS
Q _r	Reverse recovery charge	$V_R = 50 \text{ V}; I_F = 25 \text{ A}; dI_F/dt = 200 \text{A/us}$		-	59	-	nC
I _{RM}	Reverse recovery peak current			-	3	-	А
t _{rr}	Reverse recovery time	T _j = 175 °C;		-	24	-	nS
Q _r	Reverse recovery charge	$V_R = 50 \text{ V}; I_F = 25 \text{ A}; dI_F/dt = 200 \text{A/us}$		-	68	-	nC
I _{RM}	Reverse recovery peak current			-	3	-	А

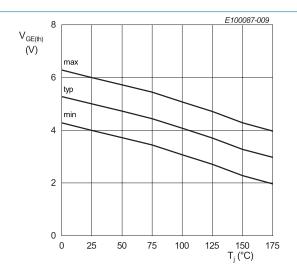




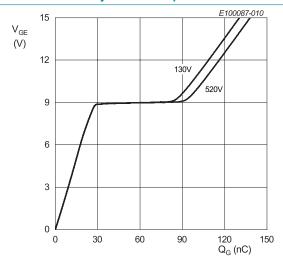
 V_{CE} = 20 V Fig. 7. Typical transfer characteristic



V_{GE} = 15 V
Fig. 8. Typical collector-emitter saturation voltage as a function of junction temperature

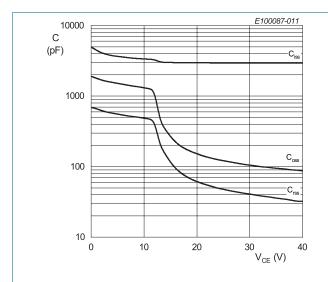


 I_c = 500 μA Fig. 9. Gate-emitter threshold voltage as a function of junction temperature



 $I_{\rm C}$ = 50 A Fig. 10. Typical gate charge

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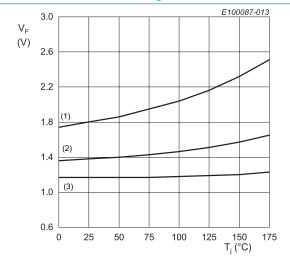
(A) 80 60 25°C 40 175°C 20 0 $^{4}V_{F}(V)$

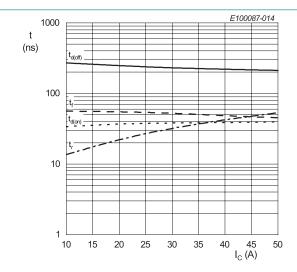
100

 I_{F}

Fig. 12. Typical diode forward current as a function of forward voltage

 $\label{eq:VGE} V_{GE} = 0 \ V; \ f = 1 \ MHz$ Fig. 11. Typical capacitance as a function of collector-emitter voltage



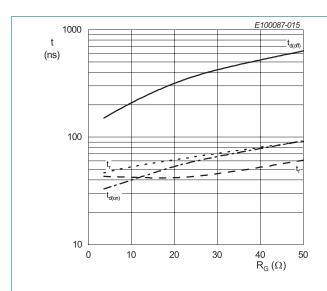


(1) $I_F = 50 A$ (2) $I_F = 25 A$ (3) $I_F = 12.5 A$

 R_{g} = 10 $\Omega;$ V_{GE} = 15V/0V; T_{j} = 175 °C; V_{CE} = 400 V; inductive load

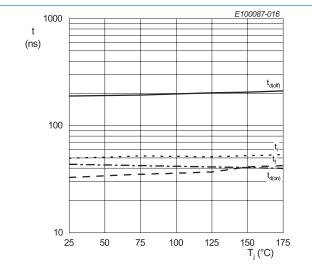
Fig. 13. Typical diode forward voltage as a function of junction temperature

Fig. 14. Typical switching times as a function of collector current



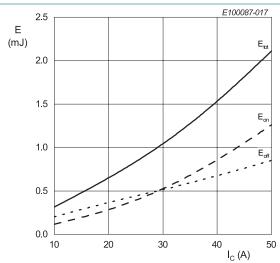
 I_C = 50 A; V_{GE} = 15V/0V; T_j = 175 °C; V_{CE} = 400 V; inductive load

Fig. 15. Typical switching times as a function of gate resistance



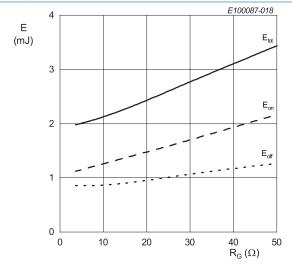
 I_{C} = 50 A; V_{GE} = 15V/0V; R_{g} = 10 Ω ; V_{CE} = 400 V; inductive load

Fig. 16. Typical switching times as a function of junction temperature



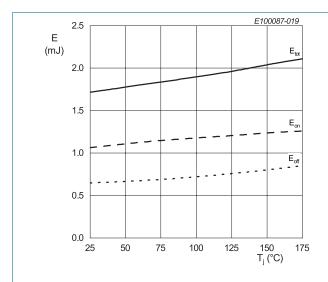
 R_g = 10 Ω ; V_{GE} = 15V/0V; T_j = 175 °C; V_{CE} = 400 V; inductive load

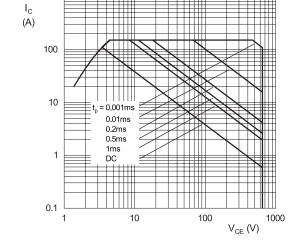
Fig. 17. Typical switching energy losses as a function of collector current



 I_{C} = 50 A; V_{GE} = 15V/0V; T_{j} = 175 °C; V_{CE} = 400 V; inductive load

Fig. 18. Typical switching energy losses as a function of gate resistance





 I_{C} = 50 A; V_{GE} = 15V/0V; R_{g} = 10 $\Omega;$ V_{CE} = 400 V; inductive load

Fig. 20. Forward bias safe operating area

Fig. 19. Typical switching energy losses as a function of junction temperature

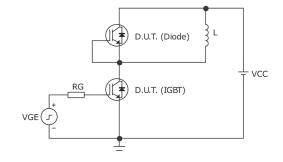


Fig. 21. Test circuit for inductive load switching

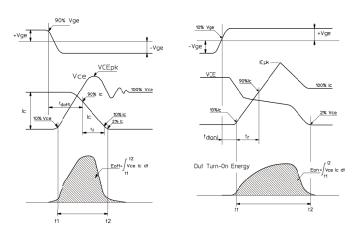
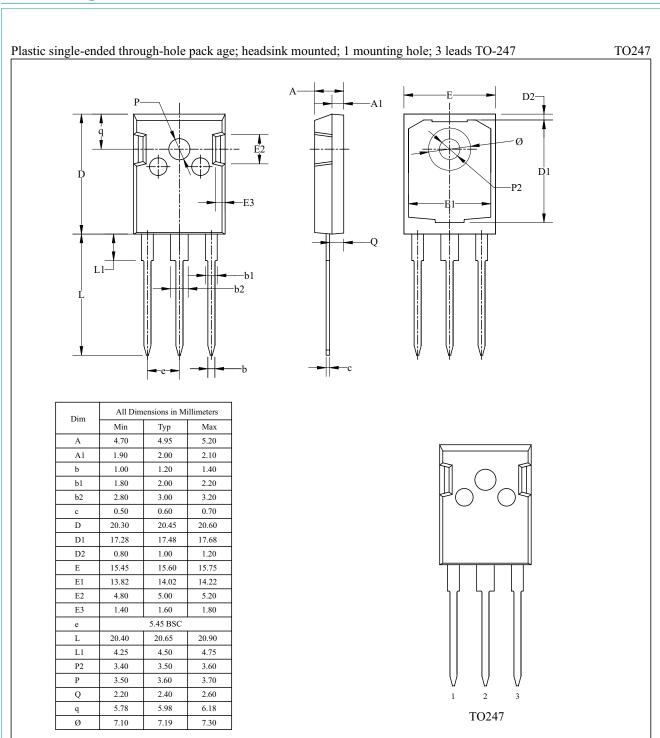


Fig. 22. Definition of switching times and losses

12. Package outline



13. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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