Notification about the transfer of the semiconductor business

The semiconductor business of Panasonic Corporation was transferred on September 1, 2020 to Nuvoton Technology Corporation (hereinafter referred to as "Nuvoton"). Accordingly, Panasonic Semiconductor Solutions Co., Ltd. became under the umbrella of the Nuvoton Group, with the new name of Nuvoton Technology Corporation Japan (hereinafter referred to as "NTCJ").

In accordance with this transfer, semiconductor products will be handled as NTCJ-made products after September 1, 2020. However, such products will be continuously sold through Panasonic Corporation.

Publisher of this Document is NTCJ.
If you would find description “Panasonic” or “Panasonic semiconductor solutions”, please replace it with NTCJ.

※ Except below description page
“Request for your special attention and precautions in using the technical information and semiconductors described in this book”

Nuvoton Technology Corporation Japan

MOS FET

Panasonic

MTM761110LBF

MTM761110LBF

Silicon P-channel MOSFET

For Switching

■ Features

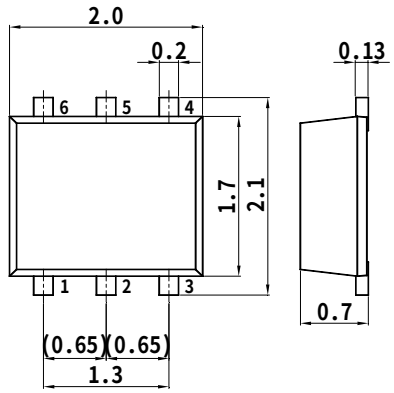
- Low Drain-source On-state Resistance : RDS(on) typ. = 26 mΩ (VGS = -4.5 V)
- Low Drive Voltage : 1.8 V Drive
- Halogen-free / RoHS compliant
  (EU RoHS / UL-94 V-0 / MSL : Level 1 compliant)

■ Marking Symbol : GS

■ Packaging

Embossed type (Thermo-compression sealing) : 3 000 pcs / reel (standard)

Unit : mm



1. Drain
2. Drain
3. Gate
4. Source
5. Drain
6. Drain

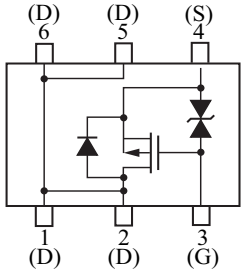
Panasonic	WSMini6-F1-B
JEITA	SC-113DA
Code	—

■ Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Drain to Source Voltage	VDS	-12	V
Gate to Source Voltage	VGS	±8	V
Drain Current	ID	-4.0	A
Drain Current (Pulsed)	IDp	-20	A
Total Power Dissipation *1	PD	700	mW
Channel Temperature	Tch	150	°C
Operating ambient temperature	Topr	-40 to + 85	°C
Storage Temperature Range	Tstg	-55 to +150	°C

Note: *1 Measuring on ceramic board at 40 mm × 38 mm × 0.2 mm.
Absolute maximum rating PD Non-heat sink shall be made 150 mW.

Internal Connection



Pin Name

1. Drain
2. Drain
3. Gate
4. Source
5. Drain
6. Drain

Established : 2008-02-01
Revised : 2013-10-18

■ Electrical Characteristics Ta = 25 °C ± 3 °C

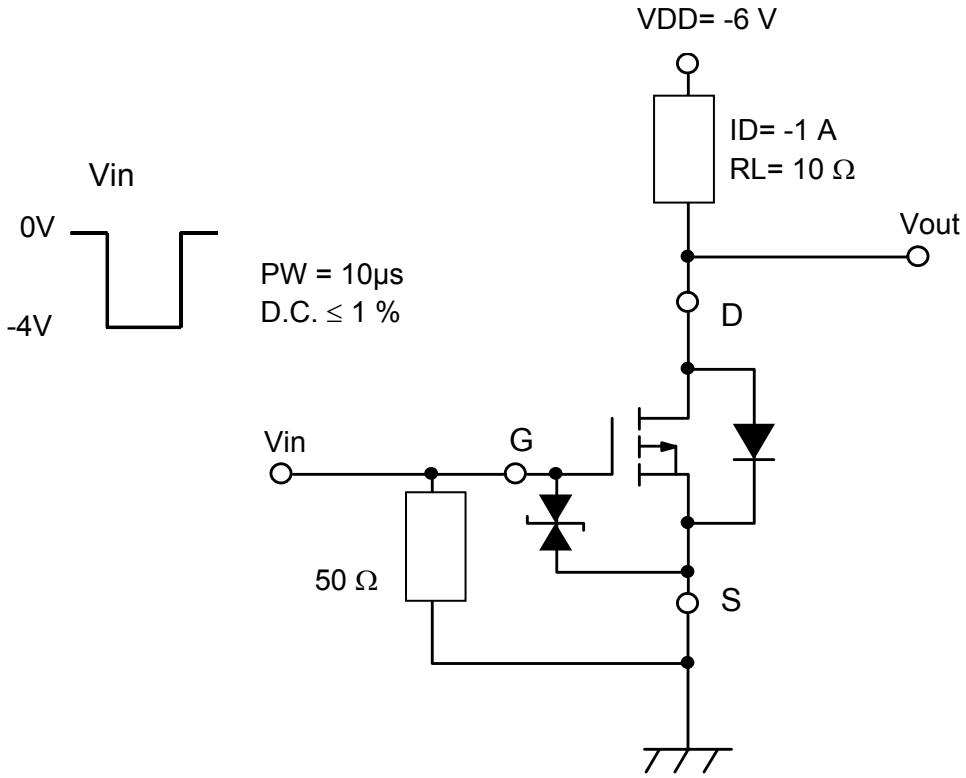
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-source surrender voltage	VDSS	ID = -1 mA, VGS = 0	-12			V
Drain-source cutoff current	IDSS	VDS = -10 V, VGS = 0			-0.1	μA
Gate-source cutoff current	IGSS	VGS = ±8 V, VDS = 0			±10	μA
Gate threshold voltage	Vth	ID = -1.0 mA, VDS = -6.0 V	-0.3	-0.65	-1.0	V
Drain-source ON resistance	RDS(ON)1	ID = -1.0 A, VGS = -4.5 V		26	34	mΩ
	RDS(ON)2	ID = -0.5 A, VGS = -2.5 V		30	41	
	RDS(ON)3	ID = -0.5 A, VGS = -1.8 V		36	54	
Forward transfer admittance	|Yfs|	ID = -1.0 A, VDS = -10 V	4.0			S
Short-circuit input capacitance (Common source)	Ciss	VDS = -10 V, VGS = 0, f = 1 MHz		1400		pF
Short-circuit output capacitance (Common source)	Coss			135		
Reverse transfer capacitance (Common source)	Crss			150		
Turn-on delay time *1	td(on)	VDD = -6 V, VGS = 0 to -4 V		9		ns
Rise time *1	tr	ID = -1.0 A		11		
Turn-off delay time *1	td(off)	VDD = -6 V, VGS = -4 to 0 V		270		ns
Fall time *1	tf	ID = -1.0 A		160		

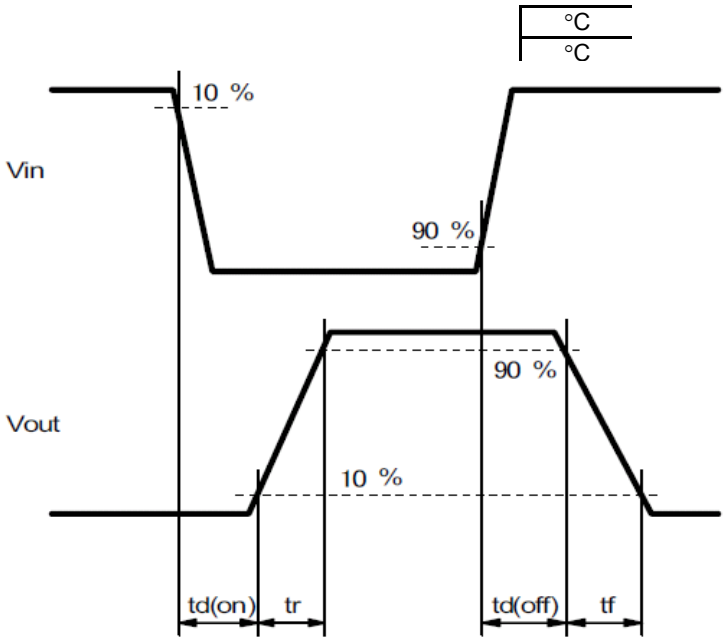
Note : Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

*1 Measurement circuit for Turn-on Delay Time / Turn-off Delay Time

Established : 2008-02-01
Revised : 2013-10-18

*1 Measurement circuit for Turn-on Delay Time / Turn-off Delay Time

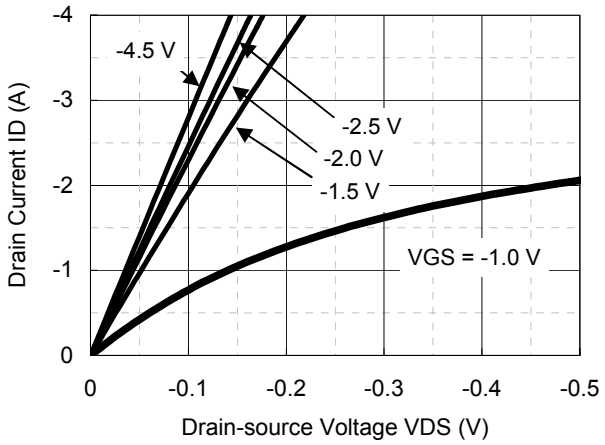




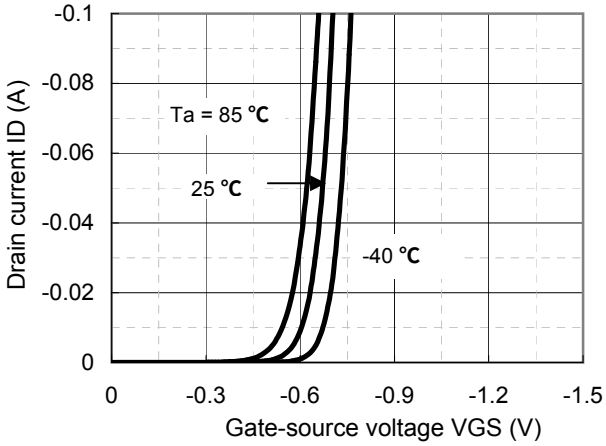
Established : 2008-02-01
Revised : 2013-10-18

Technical Data (reference)

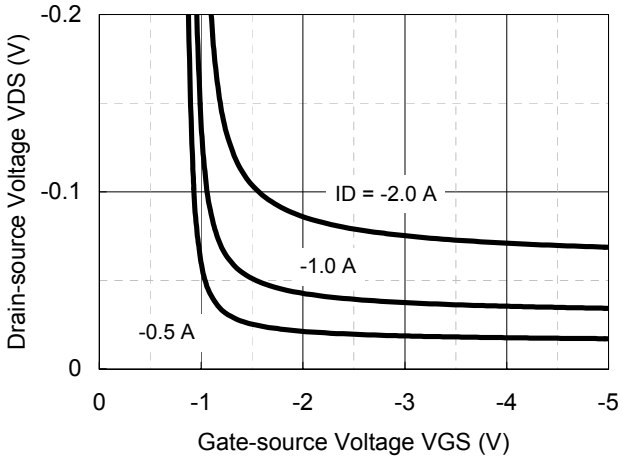
ID - VDS



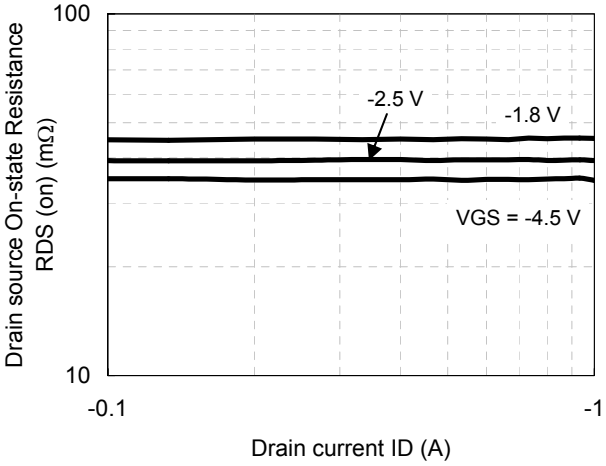
ID - VGS



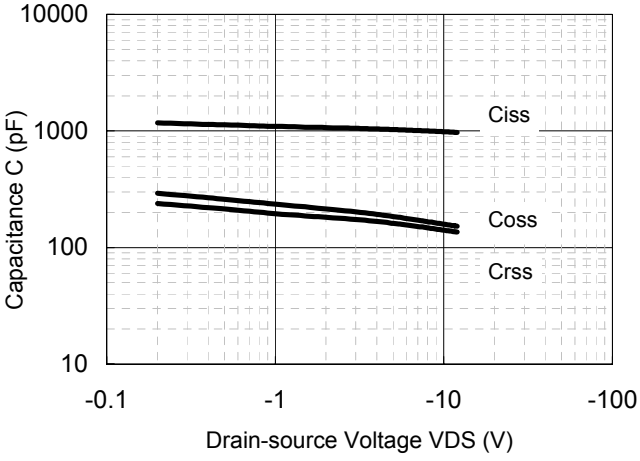
VDS - VGS



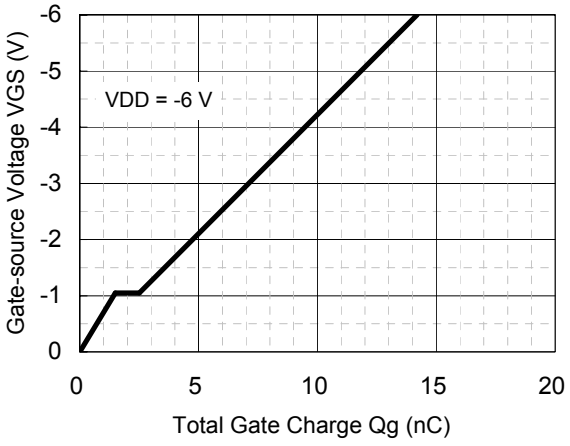
RDS(on) - ID



Capacitance - VDS



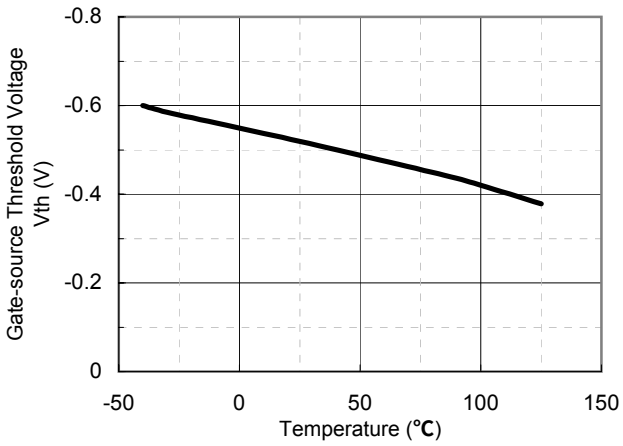
Dynamic Input/Output Characteristics



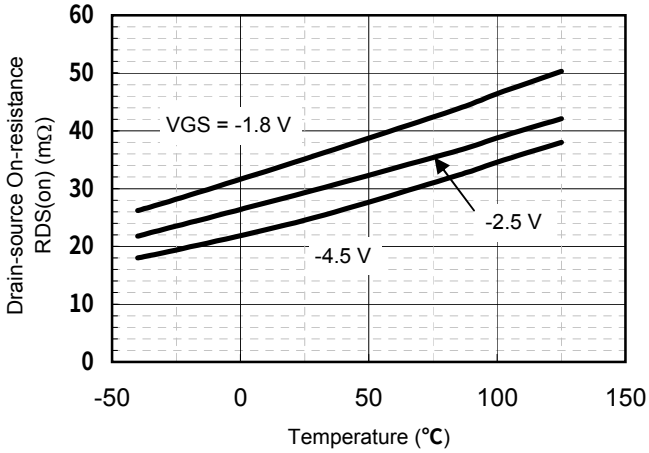
Established : 2008-02-01
Revised : 2013-10-18

Technical Data (reference)

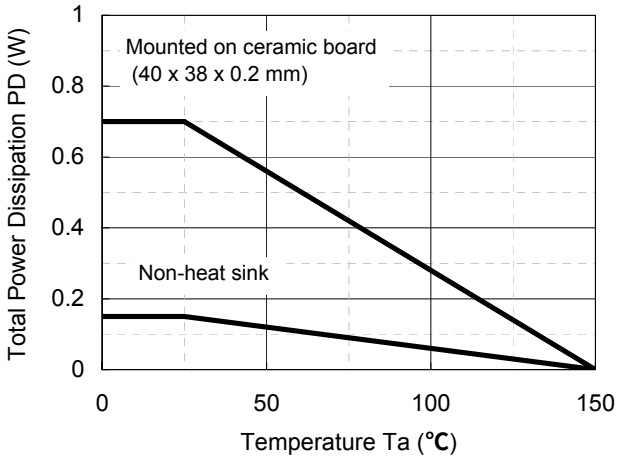
Vth - Ta



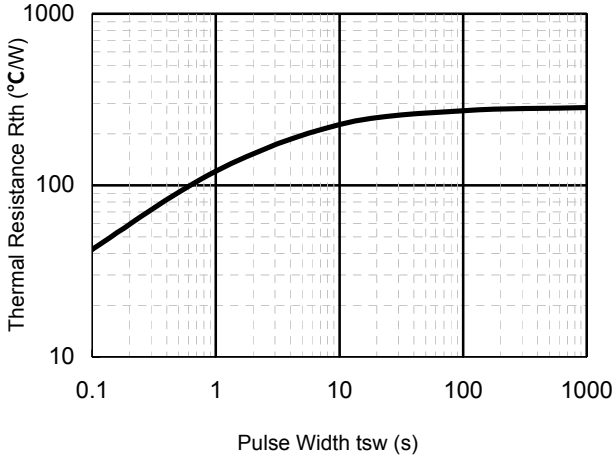
RDS(on) - Ta



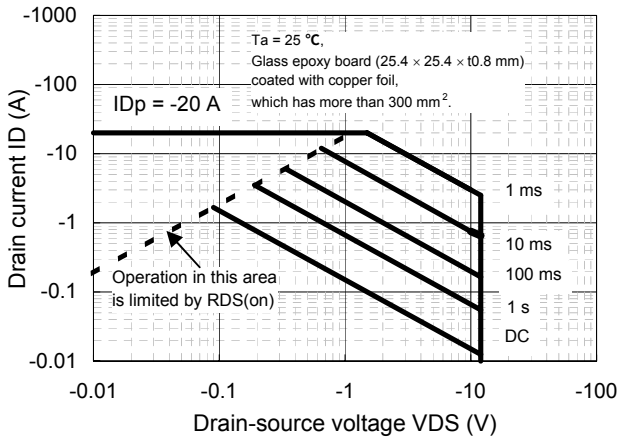
PD - Ta



Rth - tsw



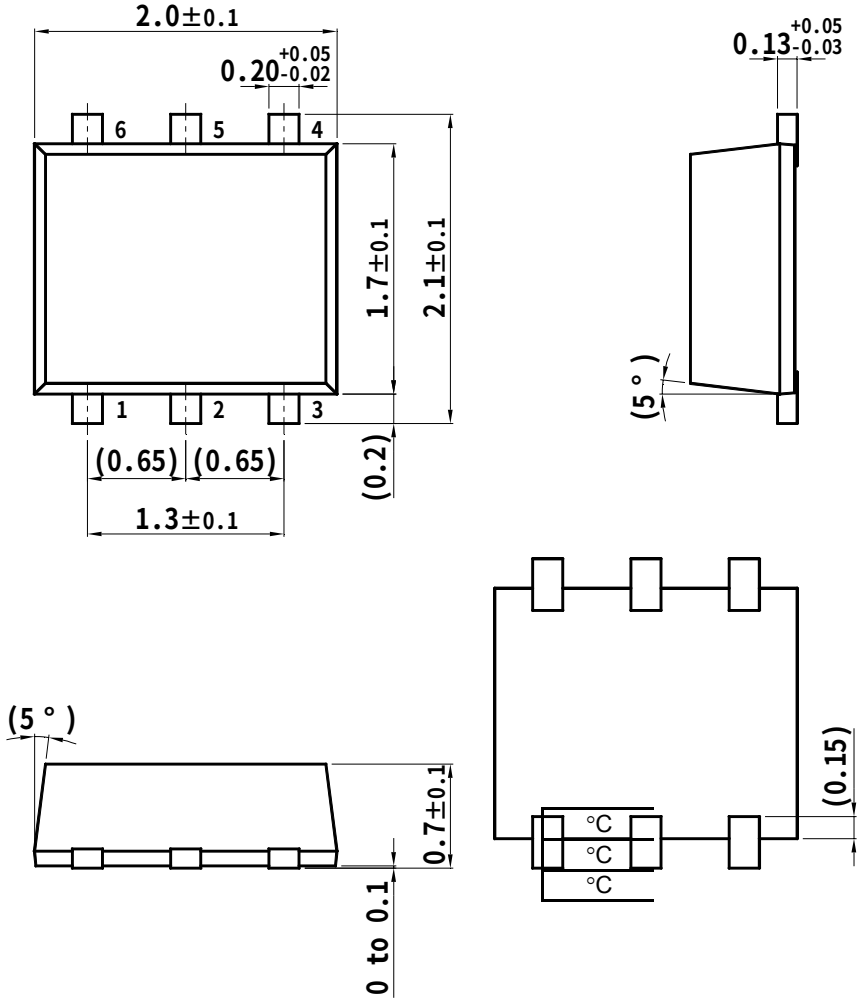
Safe Operating Area



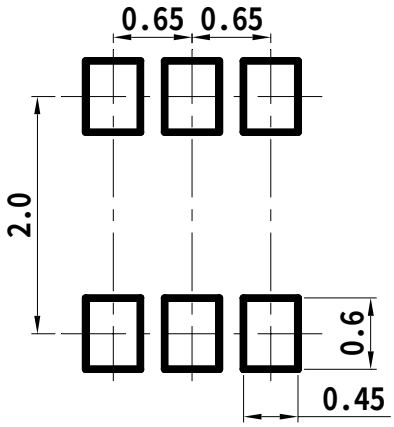
Established : 2008-02-01
Revised : 2013-10-18

WSMini6-F1-B

Unit : mm



■ Land Pattern (Reference) (Unit : mm)



Established : 2008-02-01
Revised : 2013-10-18

Request for your special attention and precautions in using the technical information and semiconductors described in this book

(1) If any of the products or technical information described in this book is to be exported or provided to non-residents, the laws and regulations of the exporting country, especially, those with regard to security export control, must be observed.

(2) The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products. No license is granted in and to any intellectual property right or other right owned by Panasonic Corporation, Nuvoton Technology Corporation Japan or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information de-scribed in this book.

(3) The products described in this book are intended to be used for general applications (such as office equipment, communications equipment, measuring instruments and household appliances), or for specific applications as expressly stated in this book.
Please consult with our sales staff in advance for information on the following applications, moreover please exchange documents separately on terms of use etc.: Special applications (such as for in-vehicle equipment, airplanes, aerospace, automotive equipment, traffic signaling equipment, combustion equipment, medical equipment and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
Unless exchanging documents on terms of use etc. in advance, it is to be understood that our company shall not be held responsible for any damage incurred as a result of or in connection with your using the products described in this book for any special application.

(4) The products and product specifications described in this book are subject to change without notice for modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.

(5) When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Other-wise, we will not be liable for any defect which may arise later in your equipment.
Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.

(6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. We do not guarantee quality for disassembled products or the product re-mounted after removing from the mounting board.
When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.

(7) When reselling products described in this book to other companies without our permission and receiving any claim of request from the resale destination, please understand that customers will bear the burden.

No.070920