



30V N-Channel Enhancement Mode MOSFET

Voltage 30 V Current

8 A

Features

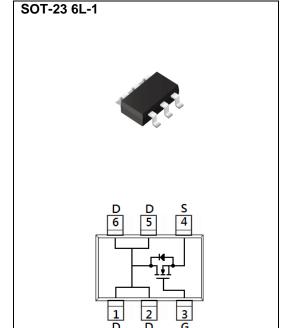
- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@8A<23m\Omega$
- R_{DS(ON)}, V_{GS}@4.5V, I_D@6A<32mΩ
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- · Advanced trench process technology
- Specially designed for switch load, PWM application, etc
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

• Case: SOT-23 6L-1 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0005 ounces, 0.014 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS		
Drain-Source Voltage		V _{DS}	30	,	
Gate-Source Voltage		V _G s	<u>+</u> 20	V	
Continuous Drain Current (Note 4)		I _D	8	A	
Pulsed Drain Current (Note 1)		I _{DM}	32		
Power Dissipation	T _a =25°C	P _D	2	W	
	Derate above 25°C		16	mW/°C	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance					
- Junction to Ambient (Note 3)		$R_{ heta JA}$	62.5	°C/W	





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30	-	-	- 2.5
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =250uA	1	1.7	2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =8A	-	18.5	23	mΩ
		V _{GS} =4.5V, I _D =6A	-	24	32	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 5)						
Total Gate Charge	Qg	V _{DS} =15V, I _D =8A, V _{GS} =4.5V (Note 2)	-	4.3	-	nC
Gate-Source Charge	Qgs		-	1.3	-	
Gate-Drain Charge	Q_{gd}		-	1.6	-	
Input Capacitance	Ciss	V _{DS} =25V, V _{GS} =0V, f=1MHZ	-	392	-	pF
Output Capacitance	Coss		-	76	-	
Reverse Transfer Capacitance	Crss		-	54	-	
Turn-On Delay Time	td _(on)	V_{DS} =15V, I_{D} =1A, V_{GS} =10V, R_{G} =6 Ω (Note 2)	-	5.9	-	
Turn-On Rise Time	tr		-	11	-	
Turn-Off Delay Time	td _(off)		-	17	-	
Turn-Off Fall Time	tf		-	3.8	-	
Drain-Source Diode						
Maximum Continuous Drain-Source			-	-	1.5	А
Diode Forward Current	ls					
Diode Forward Voltage	V _{SD}	Is=1A, V _{GS} =0V	-	0.73	1	V

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

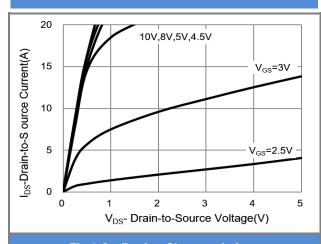


Fig.1 On-Region Characteristics

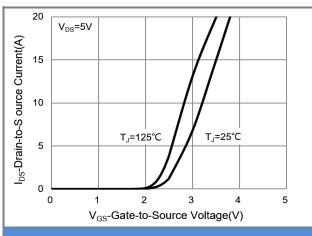


Fig.2 Transfer Characteristics

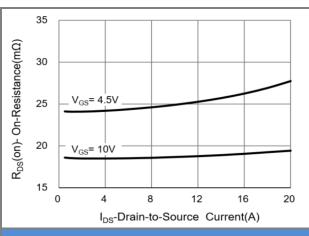


Fig.3 On-Resistance vs. Drain Current

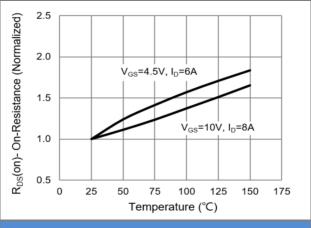


Fig.4 On-Resistance vs. Junction temperature

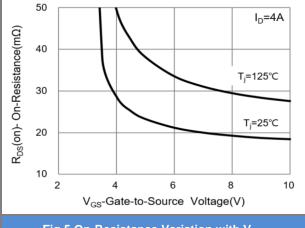
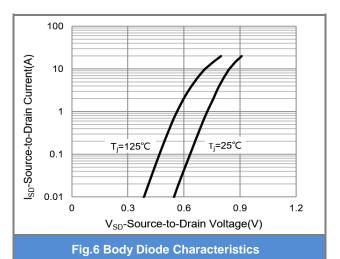


Fig.5 On-Resistance Variation with V_{GS}







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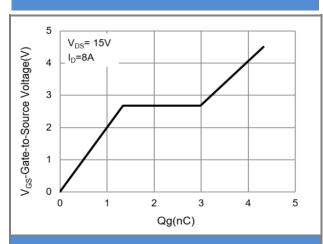


Fig.7 Gate-Charge Characteristics

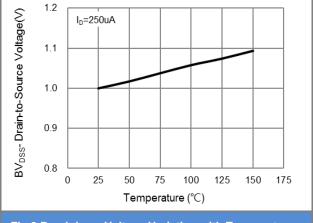


Fig.8 Breakdown Voltage Variation with Temperature

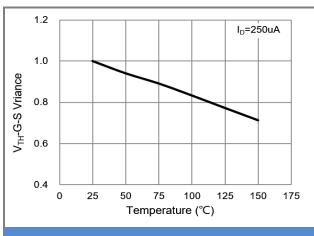


Fig.9 Threshold Voltage Variation with Temperature

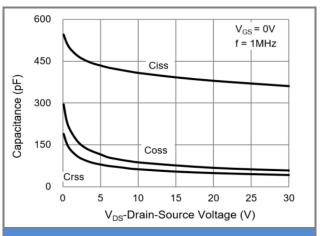


Fig.10 Capacitance vs. Drain-Source Voltage

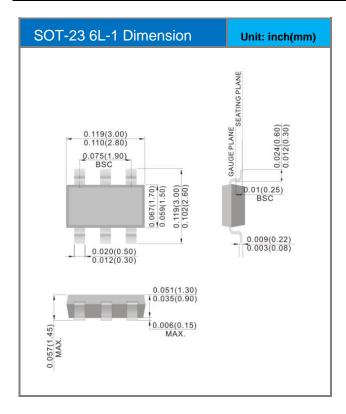


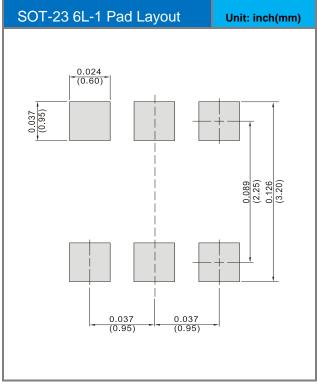


Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6412_S1_00001	SOT-23 6L-1	3K pcs / 7" reel	S12	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout









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