



40V N-Channel Enhancement Mode MOSFET

Voltage 40 V Current 3.3A

Features

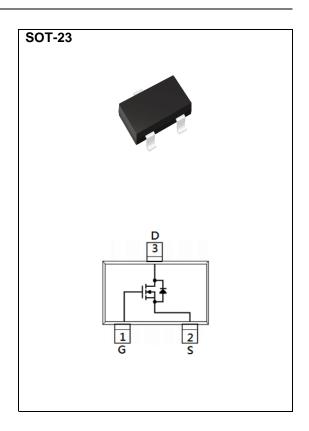
- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@3.3A<71m\Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_{D}@2.2A<96m\Omega$
- Advanced Trench Process Technology
- Specially Designed for switch Load, PWM applications, and solid-state relays relay
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

• Case: SOT-23 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0003 ounces, 0.0084 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	40	V	
Gate-Source Voltage		V _{GS}	<u>+</u> 20		
Continuous Drain Current(Note 4)		I _D	3.3	- А	
Pulsed Drain Current ^(Note 1)		I _{DM}	13.2		
Power Dissipation	T _a =25°C	P _D	1.25	W	
	Derate above 25°C		10	mW/°C	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient ^(Note 3,4)		R _{θJA}	100	°C/W	





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA 40 V _{DS} =V _{GS} , I _D =250uA 1	40	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$		1	1.3	2.1	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =3.3A	-	52	71	mΩ
		V _{GS} =4.5V, I _D =2.2A	-	70	96	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =40V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic ^(Note 5)			_			
Total Gate Charge	Qg	V _{DS} =20V, I _D =3.3A, V _{GS} =10V ^(Note 1,2)	_	6.1	-	nC
Gate-Source Charge	Qgs		-	0.9	-	
Gate-Drain Charge	Q_{gd}		-	1.2	-	
Input Capacitance	Ciss	V _{DS} =20V, V _{GS} =0V, f=1MHZ	-	241	-	pF
Output Capacitance	Coss		-	28	-	
Reverse Transfer Capacitance	Crss		-	24	-	
Turn-On Delay Time	td _(on)	$\begin{array}{c} V_{DD} \!\!=\!\! 20 V, \ I_D \!\!=\!\! 3.3 A, \\ V_{GS} \!\!=\!\! 10 V, \\ R_G \!\!=\!\! 1\Omega^{(Note\ 1,2)} \end{array}$	-	3.3	-	ns
Turn-On Rise Time	tr		-	28	-	
Turn-Off Delay Time	td _(off)		-	13	-	
Turn-Off Fall Time	tf		-	8.7	-	
Drain-Source Diode			_			
Maximum Continuous Drain-Source	Is		-	-	1	А
Diode Forward Current	IS					
Diode Forward Voltage	V _{SD}	Is=1A, V _{GS} =0V	-	0.8	1.2	V

NOTES:

- 1. Pulse width < 300us, Duty cycle < 2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

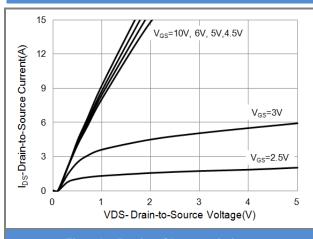


Fig.1 On-Region Characteristics

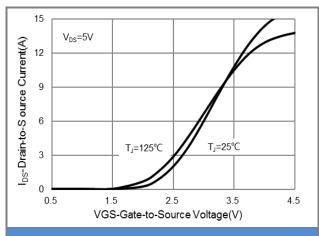


Fig.2 Transfer Characteristics

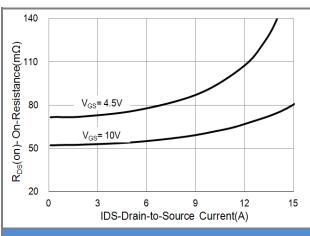


Fig.3 On-Resistance vs. Drain Current

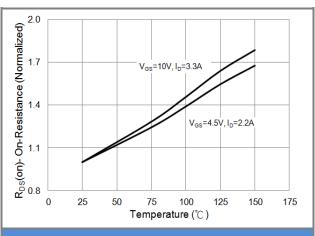
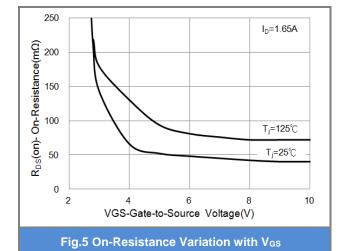
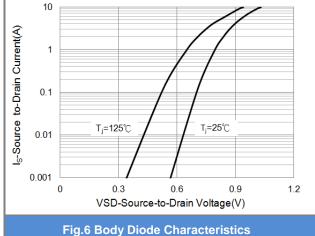


Fig.4 On-Resistance vs. Junction temperature









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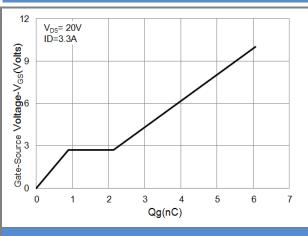


Fig.7 Gate-Charge Characteristics

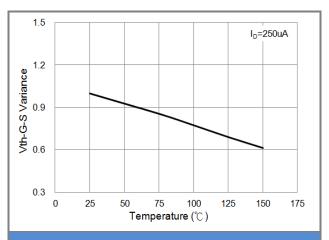


Fig.8 Threshold Voltage Variation with Temperature

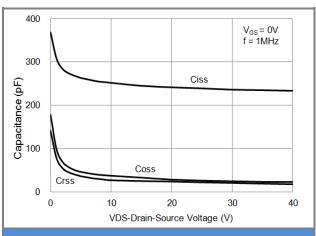


Fig.9 Capacitance vs. Drain-Source Voltage

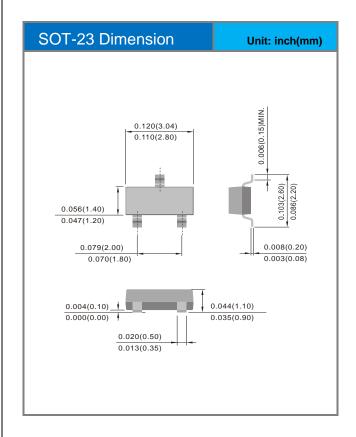


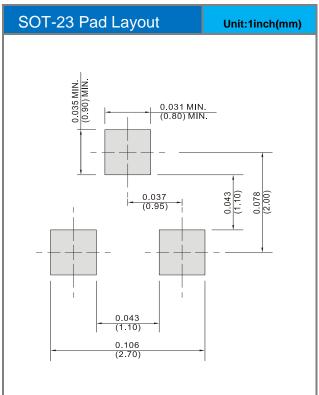


Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJA3448-AU_R1_000A1	SOT-23	3K pcs / 7" reel	A48	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout









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