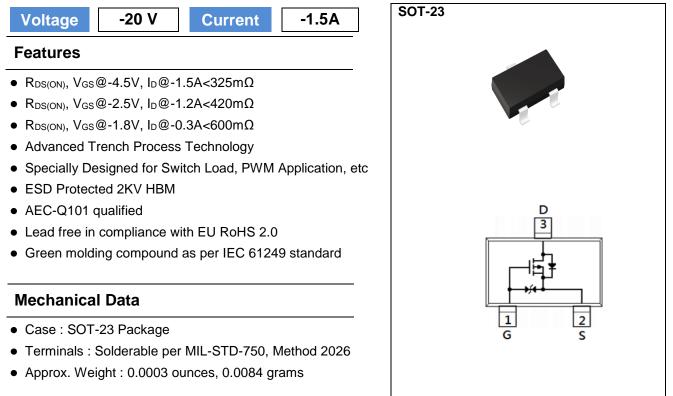
PAN	JIT
	SEMI
	CONDUCTOR

20V P-Channel Enhancement Mode MOSFET – ESD Protected



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-20		
Gate-Source Voltage	V _{GS}	<u>+</u> 8	V		
Continuous Drain Current (Note 4)		Ι _D	-1.5	•	
Pulsed Drain Current (Note 1)		I _{DM}	-4	A	
Power Dissipation	T _a =25°C	PD	1.25	W	
	Derate above 25°C		10	mW/°C	
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient ^(Note 3,4)		Reja	100	°C/W	



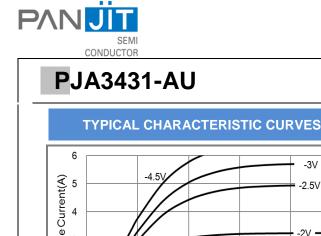
Electrical Characteristics (T_A=25°C unless otherwise noted)

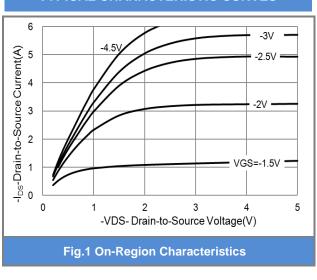
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-		v
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-0.5	-0.64	-1	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-1.5A	-	240	325	mΩ
		V _{GS} =-2.5V, I _D =-1.2A	-	295	420	
		V _{GS} =-1.8V, I _D =-0.3A	-	405	600	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-20V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	lgss	V _{GS=<u>+</u>8V, V_{DS}=0V}	-	-	<u>+</u> 10	
Dynamic (Note 5)						
Total Gate Charge	Qg		-	1.7	-	nC
Gate-Source Charge	Qgs	V _{DS} =-10V, I _D =-1.5A, V _{GS} =-4.5V ^(Note 1,2)	-	0.35	-	
Gate-Drain Charge	Q_{gd}		-	0.43	-	
Input Capacitance	Ciss	V _{DS} =-10V, V _{GS} =0V,	-	165	-	pF
Output Capacitance	Coss		-	25	-	
Reverse Transfer Capacitance	Crss	f=1MHZ	-	14.7	-	
Turn-On Delay Time	td _(on)		-	11	-	
Turn-On Rise Time	tr	V _{DD} =-10V, I _D =-1.5A, V _{GS} =-4.5V,	-	38	-	
Turn-Off Delay Time	td _(off)		-	130	-	ns
Turn-Off Fall Time	tf	$R_G=6\Omega$ (Note 1,2)	-	75	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	-1.6	А
Diode Forward Voltage	V _{SD}	Is=-1.6A, V _{GS} =0V	-	-1.03	-1.2	V

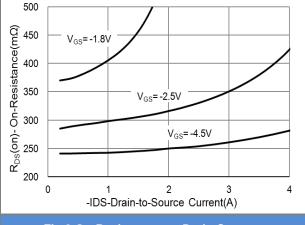
NOTES :

1. Pulse width</br>

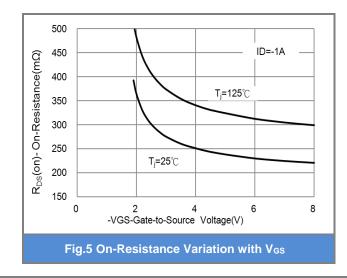
- 2. Essentially independent of operating temperature typical characteristics.
- 3. ReJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.

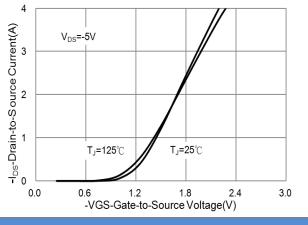




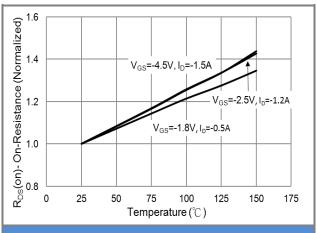




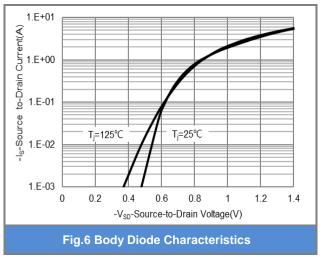


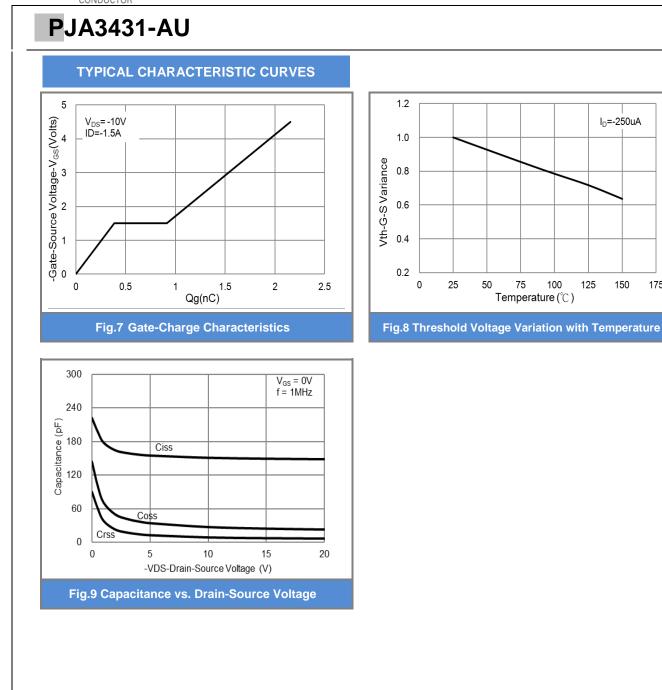




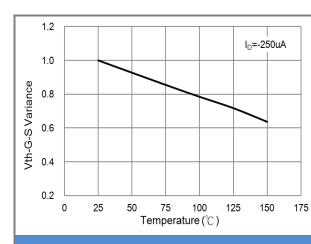










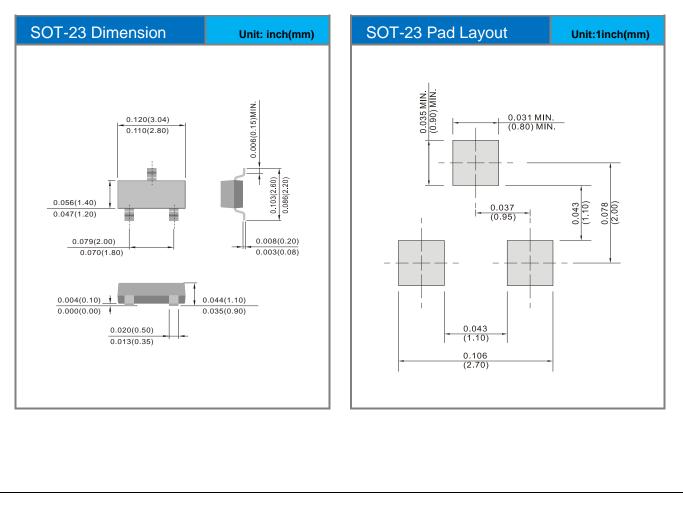




Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJA3431-AU_R1_000A1	SOT-23	3K pcs / 7" reel	A31	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout





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