



30V N-Channel Enhancement Mode MOSFET- ESD Protected

Voltage

30 V

Current

4.2A

Features

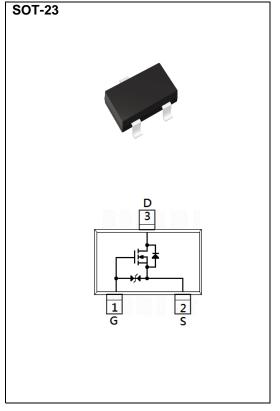
- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@4.2A<42m\Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_D@3.5A<48m\Omega$
- $R_{DS(ON)}$, $V_{GS}@2.5V$, $I_D@2.8A<55m\Omega$
- Advanced Trench Process Technology
- ESD Protected
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 Standard

Mechanical Data

• Case: SOT-23 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0003 ounces, 0.0084 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETE	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage		V _G s	<u>+</u> 12	V
Continuous Drain Current		I _D	4.2	Α
Pulsed Drain Current		I _{DM}	16.8	Α
Power Dissipation	T _a =25°C	P_{D}	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient ^(Note 3)		R _θ JA	100	°C/W





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =250uA	0.5	0.8	1.3	V	
	R _{DS(on)}	V _{GS} =10V, I _D =4.2A	-	32	42	mΩ	
Drain-Source On-State Resistance		V _{GS} =4.5V, I _D =3.5A	-	35	48		
		V _{GS} =2.5V, I _D =2.8A	-	44	55		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1	uA	
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 10V, V _{DS} =0V	-	-	<u>+</u> 10	uA	
Dynamic ^(Note 5)							
Total Gate Charge	Q_g	V _{DS} =15V, I _D =4.2A, V _{GS} =4.5V ^(Note 1,2)	-	6	-	nC	
Gate-Source Charge	Q_{gs}		-	1	-		
Gate-Drain Charge	Q_gd		-	1.5	-		
Input Capacitance	Ciss	V _{DS} =15V, V _{GS} =0V, f=1.0MHZ	-	430	-	pF	
Output Capacitance	Coss		-	45	-		
Reverse Transfer Capacitance	Crss		-	40	-		
Turn-On Delay Time	td _(on)	$V_{DD}{=}15\text{V}, \ I_{D}{=}1\text{A},$ $V_{GS}{=}10\text{V},$ $R_{G}{=}3\Omega^{(Note\ 1,2)}$	-	3	-		
Turn-On Rise Time	tr		-	25	-	ns	
Turn-Off Delay Time	td _(off)		-	26	-		
Turn-Off Fall Time	tf		-	18	-		
Drain-Source Diode							
Maximum Continuous Drain-Source Diode Forward Current	Is		-	-	1.5	Α	
Diode Forward Voltage	V _{SD}	I _S =1.0A, V _{GS} =0V	-	0.77	1.2	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited
- 5. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

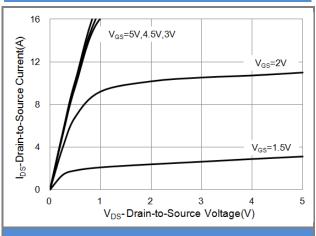


Fig.1 On-Region Characteristics

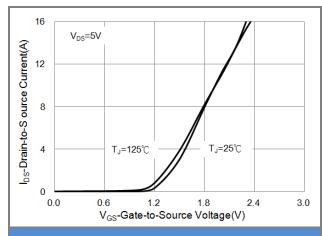


Fig.2 Transfer Characteristics

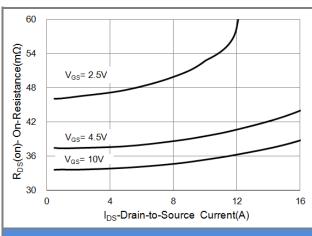


Fig.3 On-Resistance vs. Drain Current

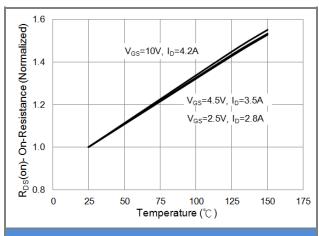
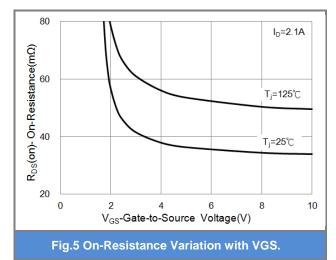
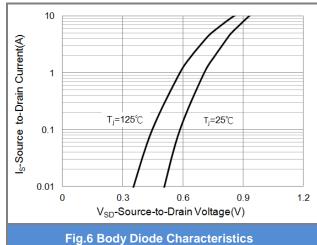


Fig.4 On-Resistance vs. Junction temperature









TYPICAL CHARACTERISTIC CURVES

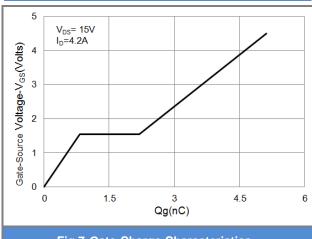


Fig.7 Gate-Charge Characteristics

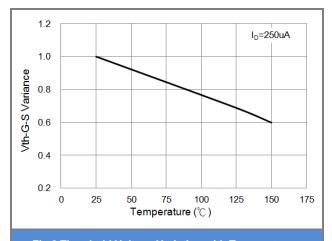


Fig.8 Threshold Voltage Variation with Temperature

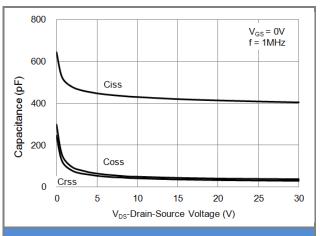


Fig.9 Capacitance vs. Drain-Source Voltage.

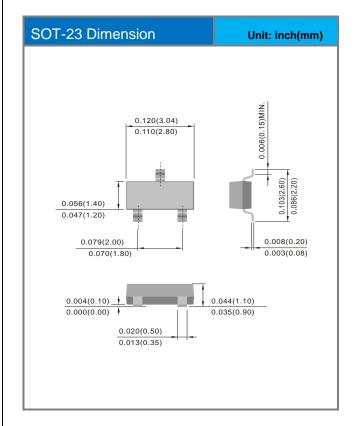


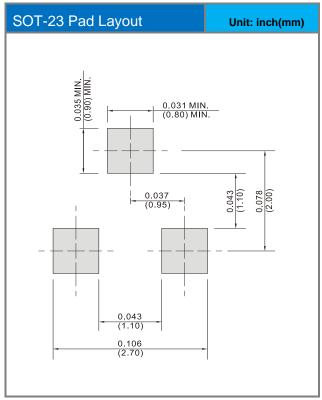


Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJA3424E_R1_00001	SOT-23	3K pcs / 7" reel	24E	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout









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