



30V P-Channel Enhancement Mode MOSFET

Voltage

-30 V

Current

-3.6A

Features

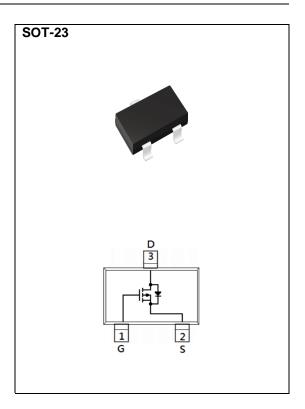
- $R_{DS(ON)}$, $V_{GS}@-10V$, $I_D@-3.6A<72m\Omega$
- $\bullet \ \ R_{DS(ON)}, \ V_{GS}@\text{-}4.5V, \ I_{D}@\text{-}2.3A\text{<}82m\Omega$
- $R_{DS(ON)}$, $V_{GS}@-2.5V$, $I_D@-1.4A<115m\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

• Case: SOT-23 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0003 ounces, 0.0084 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMET	SYMBOL	LIMIT	UNITS		
Drain-Source Voltage		V _{DS}	-30	V	
Gate-Source Voltage		V _{GS}	<u>+</u> 12		
Continuous Drain Current(Note 4)		I _D	-3.6	A	
Pulsed Drain Current ^(Note 1)		I _{DM}	-14.4		
Power Dissipation	T _a =25°C	P _D	1.25	W	
	Derate above 25°C		10	mW/°C	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient ^(Note 3,4)		Reja	100	°C/W	





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-30	_V	W		
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA	-0.5	-0.97	-1.3	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-3.6A	-	60	72	mΩ	
		V _{GS} =-4.5V, I _D =-2.3A	-	67	82		
		V _{GS} =-2.5V, I _D =-1.4A	-	84	115		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V	-	-	-1	uA	
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	-	<u>+</u> 100	nA	
Dynamic ^(Note 5)							
Total Gate Charge	Q_g	\/ 45\/ 2.6\	-	15	-	nC	
Gate-Source Charge	Q_gs	V_{DS} =-15V, I_{D} =-3.6A, V_{GS} =-10V ^(Note 1,2)	-	1.3	-		
Gate-Drain Charge	Q_gd		-	2	-		
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V,	-	633	-	pF	
Output Capacitance	Coss		-	50	-		
Reverse Transfer Capacitance	Crss	I= IIVITZ	-	35	-		
Turn-On Delay Time	td _(on)	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	-	2.9	-		
Turn-On Rise Time	tr	V_{DD} =-15V, I_{D} =-3.6A, V_{GS} =-10V, R_{G} =6 $\Omega^{(Note 1,2)}$	-	43	-	ns	
Turn-Off Delay Time	td _(off)		-	224	-		
Turn-Off Fall Time	tf	KG=012(1000 1,2)	-	100	-		
Drain-Source Diode							
Maximum Continuous Drain-Source	Is		_	_	-1.5	Α	
Diode Forward Current	IS		_	_	-1.0	A	
Diode Forward Voltage	V_{SD}	Is=-1A, V _G s=0V	-	-0.77	-1.2	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

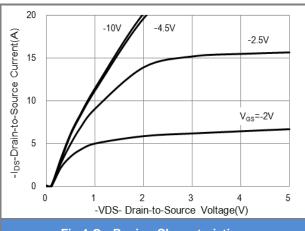


Fig.1 On-Region Characteristics

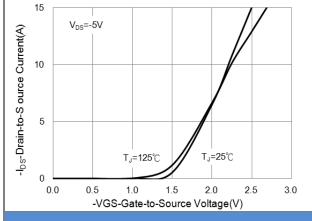


Fig.2 Transfer Characteristics

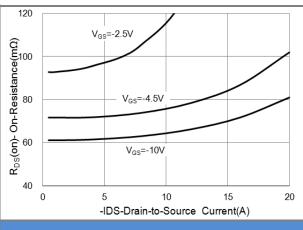


Fig.3 On-Resistance vs. Drain Current

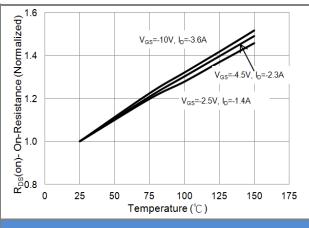
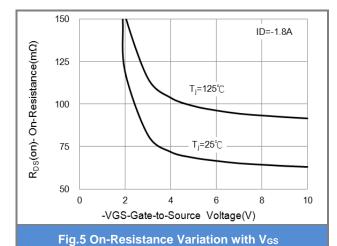
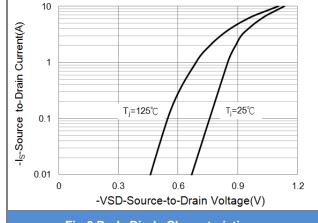


Fig.4 On-Resistance vs. Junction temperature









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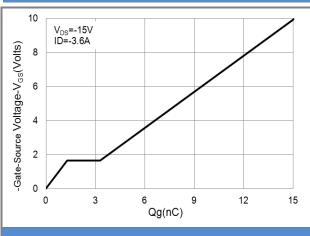


Fig.7 Gate-Charge Characteristics

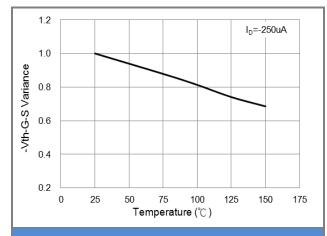


Fig.8 Threshold Voltage Variation with Temperature

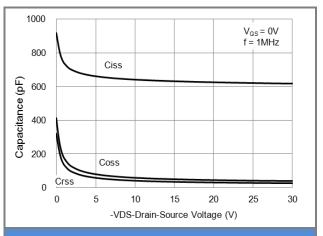


Fig.9 Capacitance vs. Drain-Source Voltage

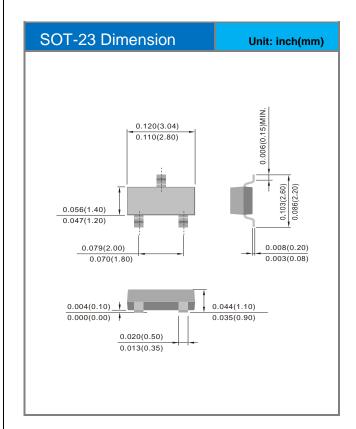


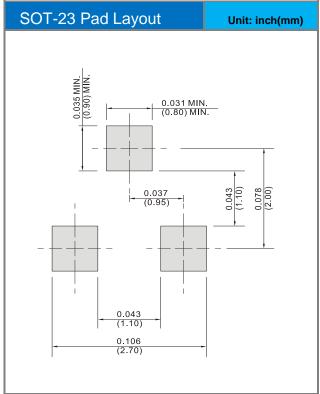


Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJA3401-AU_R1_000A1	SOT-23	3K pcs / 7" reel	A01	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout









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