

30V N-Channel Enhancement Mode MOSFET

Voltage

30 V

Current

141 A

Features

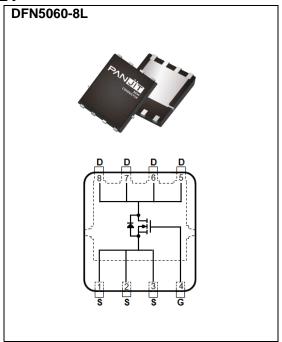
- RDS(ON), VGS@10V, ID@20A<2m Ω
- RDS(ON), VGS@4.5V, ID@20A<3.1m Ω
- Excellent FOM
- Logic Level Drive
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

• Case: DFN5060-8L Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.08 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

| PARAMETER | | SYMBOL | LIMIT | UNITS | |
|--|-----------------------|----------------------------------|---------|-------|--|
| Drain-Source Voltage | | V _{DS} | 30 | V | |
| Gate-Source Voltage | | V _{GS} | ±20 | V | |
| Continuous Drain Current(Note 3) | T _C =25°C | | 141 | | |
| | T _C =100°C | l _D | 99 | Α | |
| Pulsed Drain Current(Note 1) | Tc=25°C | I _{DM} | 564 | | |
| Power Dissipation | T _C =25°C | | 75 | 14/ | |
| | T _C =100°C | PD | 37.5 | W | |
| Continuous Drain Current(Note 4) | T _A =25°C | | 29.6 | | |
| | T _A =70°C | I _D | 24.8 | A | |
| Power Dissipation | T _A =25°C | D- | 3.3 | 14/ | |
| | T _A =70°C | PD | 2.3 | W | |
| Single Pulse Avalanche Energy(Note 5) | | Eas | 150 | mJ | |
| Operating Junction and Storage Temperature Range | | T _J ,T _{STG} | -55~175 | °C | |
| Thermal Resistance ^(Note 4) | Junction to Case | R _{0JC} | 2 | °C/W | |
| | Junction to Ambient | R _{0JA} | 45 | | |



Electrical Characteristics (T_A=25°C unless otherwise noted)

| PARAMETER | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNITS | |
|----------------------------------|---------------------|--|------|------|------|-------|--|
| Static | | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V, I _D =250uA | 30 | - | - | - | |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} =V _{GS} , I _D =250uA | 1.3 | 1.8 | 2.5 | V | |
| Drain-Source On-State Resistance | R _{DS(on)} | V _{GS} =10V, I _D =20A | - | 1.63 | 2 | mΩ | |
| | | V _{GS} =4.5V, I _D =20A | - | 2.4 | 3.1 | | |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =30V, V _{GS} =0V | - | - | 1 | uA | |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} =±20V, V _{DS} =0V | - | - | ±100 | nA | |
| Dynamic ^(Note 6) | | | | • | | • | |
| Total Gate Charge | Q_g | V _{DS} =24V, I _D =20A, | - | 43 | - | nC | |
| Gate-Source Charge | Qgs | | - | 8.4 | - | | |
| Gate-Drain Charge | Q_{gd} | V _{GS} =10V | - | 5.5 | - | | |
| Input Capacitance | Ciss | | - | 2930 | - | pF | |
| Output Capacitance | Coss | V _{DS} =25V, V _{GS} =0V, | - | 1275 | - | | |
| Reverse Transfer Capacitance | Crss | f=1MHz | - | 72 | - | | |
| Gate resistance | Rg | f=1MHz | - | 0.3 | - | Ω | |
| Turn-On Delay Time | td _(on) | ., .,,, | - | 16 | - | ns | |
| Turn-On Rise Time | tr | V _{DS} =24V, I _D =20A, | - | 11 | - | | |
| Turn-Off Delay Time | td _(off) | $V_{GS}=10V, R_{G}=3\Omega$ (Note 2) | - | 35 | - | | |
| Turn-Off Fall Time | tf | (Note 2) | - | 40 | - | | |
| Drain-Source Diode | | | | | | | |
| Diode Forward Current | Is | T 05 ⁰ 0 | - | - | 137 | Α | |
| Pulsed Diode Forward Current | I _{SM} | T _C =25°C | - | - | 548 | | |
| Diode Forward Voltage | V _{SD} | Is=20A, Vgs=0V | - | 0.79 | 1.1 | V | |
| Reverse Recovery Time | Trr | V _{GS} =0V, I _S =20A | - | 50 | - | ns | |
| Reverse Recovery Charge | Qrr | dls/dt=100A/us | - | 45 | - | nC | |

NOTES:

- 1. Pulse width<100us, Duty cycle<2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Chip capability with an R_{eJC}=2°C/W, Package limited 100A.
- 4. R_{BJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 5. The test condition is L=0.5mH, I_{AS} =25A, V_{DD} =30V, V_{GS} =10V, Starting T_J =25°C.
- 6. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES

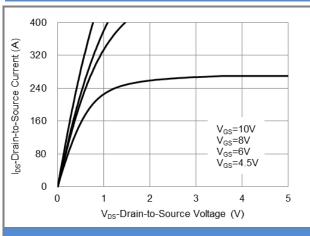


Fig.1 On-Region Characteristics

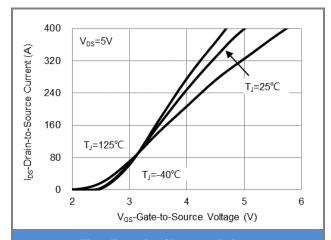


Fig.2 Transfer Characteristics

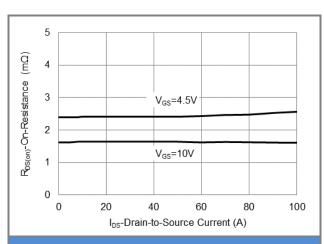


Fig.3 On-Resistance vs. Drain Current

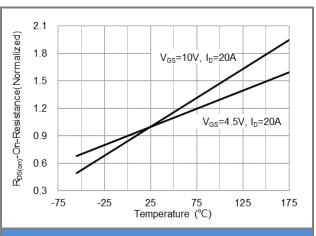
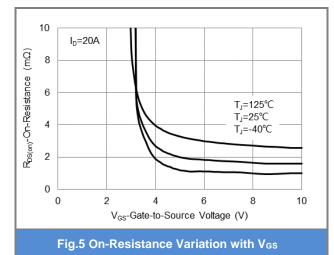


Fig.4 On-Resistance vs. Junction temperature



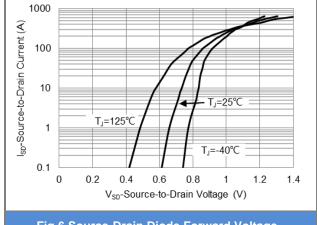


Fig.6 Source-Drain Diode Forward Voltage



TYPICAL CHARACTERISTIC CURVES

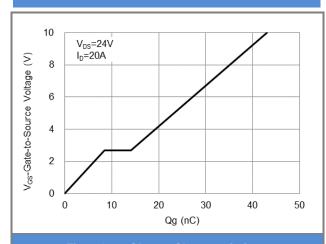


Fig.7 Gate-Charge Characteristics

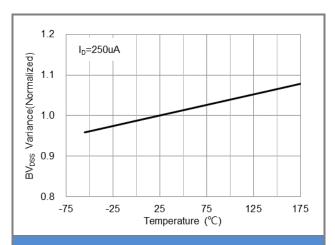


Fig.8 Breakdown Voltage Variation vs. Temperature

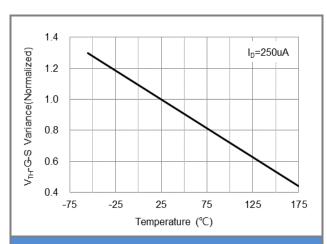


Fig.9 Threshold Voltage Variation with Temperature

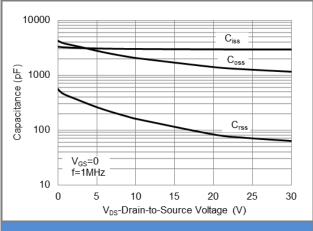
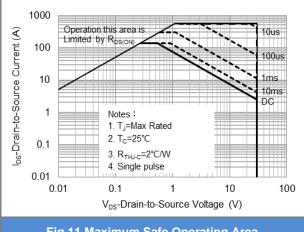


Fig.10 Capacitance vs. Drain-Source Voltage





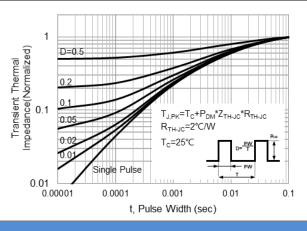


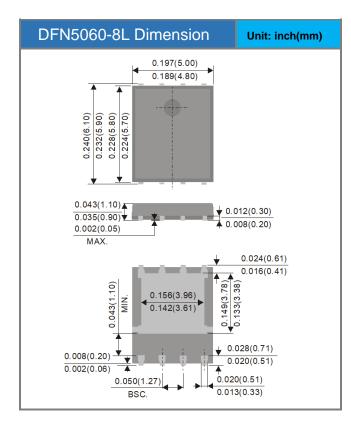
Fig.12 Normalized Transient Thermal Impedance

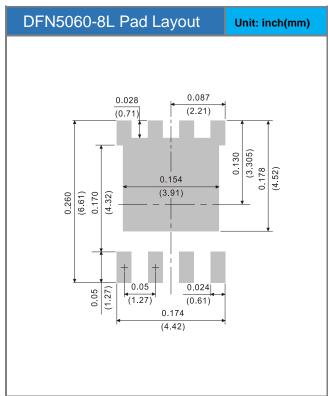


Product and Packing Information

| Part No. | Package Type | Packing Type | Marking | |
|------------|--------------|-------------------|---------|--|
| PJQ5522-AU | DFN5060-8L | 3K pcs / 13" reel | Q5522 | |

Packaging Information & Mounting Pad Layout





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