Advance Technical Information

## PolarHV ${ }^{\text {TM }}$ HiPerFET Power MOSFET ISOPLUS220 ${ }^{\text {TM }}$

## (Electrically Isolated Back Surface)

N-Channel Enhancement Mode
Fast Intrinsic Diode
Avalanche Rated

| Symbol | Test Conditions | Maximum Ratings |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {DSs }}$ | $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}$ | 800 |  |  | V |
| $\mathrm{V}_{\text {DGR }}$ | $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C} ; \mathrm{R}_{\mathrm{GS}}=1 \mathrm{M} \Omega$ | 800 |  |  | V |
| $\mathrm{V}_{\text {Gs }}$ | Continuous | $\pm 30$ |  |  | V |
| $\mathrm{V}_{\text {GSM }}$ | Transient | $\pm 40$ |  |  | V |
| $\mathrm{I}_{\mathrm{D} 25}$ | $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ | 5 |  |  | A |
| $\mathrm{I}_{\mathrm{DM}}$ | $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$, pulse width limited by $\mathrm{T}_{\mathrm{JM}}$ | 30 |  |  | A |
| $\mathrm{I}_{\text {AR }}$ | $\mathrm{T}_{\mathrm{c}}=25^{\circ} \mathrm{C}$ | 5 |  |  | A |
| $\mathrm{E}_{\text {AR }}$ | $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ | 30 |  |  | mJ |
| $\mathrm{E}_{\mathrm{AS}}$ | $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ | 750 |  |  | mJ |
| dv/dt | $\begin{aligned} & \mathrm{I}_{\mathrm{S}} \leq \mathrm{I}_{\mathrm{DM},}, \mathrm{di} / \mathrm{dt} \leq 100 \mathrm{~A} / \mu \mathrm{S}, \mathrm{~V}_{\mathrm{DD}} \leq \mathrm{V}_{\mathrm{DSS}}, \\ & \mathrm{~T}_{\mathrm{J}} \leq 150^{\circ} \mathrm{C}, \mathrm{R}_{\mathrm{G}}=10 \Omega \end{aligned}$ | 10 |  |  | V/ns |
| $\mathrm{P}_{\mathrm{D}}$ | $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ | 100 |  |  | W |
| $\mathrm{T}_{\text {J }}$ |  | $-55 \ldots+150$ |  |  | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {J }}$ |  | 150 |  |  | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {stg }}$ |  | $-55 \ldots+150$ |  |  | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\mathrm{L}}$ | 1.6 mm (0.062 in.) from case for 10 s | 300 |  |  | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {soLD }}$ | Plastic body for 10 s | 260 |  |  | ${ }^{\circ} \mathrm{C}$ |
| $\overline{\mathrm{V}}_{\text {ISOL }}$ | $50 / 60 \mathrm{~Hz}, \mathrm{RMS}$, t = 1, leads-to-tab | 2500 |  |  | V~ |
| $\mathrm{F}_{\mathrm{c}}$ | Mounting Force | 11..65/2.5.. 15 |  |  | N/lb |
| Weight |  | 2 |  |  | g |
| Symbol Test Conditions$\left(T_{J}=25^{\circ} \mathrm{C}\right.$ unless otherwise specified) |  | Characteristic Values |  |  |  |
|  |  | Min. | Typ. | Max |  |
| $\mathrm{BV}_{\text {Dss }}$ | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=250 \mu \mathrm{~A}$ | 800 |  |  | V |
| $\mathrm{V}_{\text {GS(th) }}$ | $\mathrm{V}_{\mathrm{DS}}=\mathrm{V}_{\mathrm{GS}}, \mathrm{I}_{\mathrm{D}}=2.5 \mathrm{~mA}$ | 3.0 |  | 5.5 | V |
| $\mathrm{I}_{\text {GSS }}$ | $\mathrm{V}_{\mathrm{GS}}= \pm 30 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0 \mathrm{~V}$ |  |  | $\pm 100$ | nA |
| $\mathrm{I}_{\text {DSs }}$ | $\begin{array}{ll} V_{D S}=V_{D S S} \\ V_{G S}=0 \mathrm{~V} & T_{J}=125^{\circ} \mathrm{C} \end{array}$ |  |  |  | $\mu \mathrm{A}$ $\mu \mathrm{A}$ |
| $\mathrm{R}_{\mathrm{DS}(\text { on) }}$ | $\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=\mathrm{I}_{\mathrm{T},}$ (Note 1) <br> Pulse test, $\mathrm{t} \leq 300 \mu \mathrm{~s}$, duty cycle $\mathrm{d} \leq 2 \%$ |  |  | 1.2 | $\mathrm{m} \Omega$ |


| $\mathrm{V}_{\mathrm{DSS}}$ | $=800 \mathrm{~V}$ |  |
| :--- | ---: | ---: | ---: |
| $\mathrm{I}_{\mathrm{D} 25}$ | $=$ | 5 A |
| $\mathrm{R}_{\mathrm{DS}(\text { on) })}$ | $\leq$ | $1.2 \mathrm{~m} \Omega$ |
| $\mathrm{t}_{\mathrm{rr}}$ | $\leq 250 \mathrm{~ns}$ |  |

ISOPLUS220 ${ }^{\text {TM }}$ (IXFC)

$$
\begin{array}{ll}
G=\text { Gate } & D=\text { Drain } \\
S=\text { Source } &
\end{array}
$$

## Features

- Silicon chip on Direct-Copper-Bond substrate
- High power dissipation
- Isolated mounting surface
- 2500V electrical isolation
- Low drain to tab capacitance(<35pF)
- Low R DS (on) $\mathrm{HDMOS}^{\text {TM }}$ process
- Rugged polysilicon gate cell structure
- Unclamped Inductive Switching (UIS)
rated
- Fast intrinsic Rectifier


## Applications

- DC-DC converters
- Battery chargers
- Switched-mode and resonant-mode power supplies
- DC choppers
- AC motor control


## Advantages

- Easy assembly: no screws, or isolation foils required
- Space savings
- High power density
- Low collector capacitance to ground (low EMI)

Symbol
Test Conditions
Characteristic Values

( $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ unless otherwise specified) | Min. | Typ. | Max. |
| :--- | :--- | :--- |



## Source-Drain Diode

Characteristic Values
( $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ unless otherwise specified)

| Symbol | Test Conditions | Min. ${ }^{\text {Typ. }}$ | Max. |  |
| :---: | :---: | :---: | :---: | :---: |
| $\mathrm{I}_{\text {s }}$ | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}$ |  | 10 | A |
| $\mathrm{I}_{\text {SM }}$ | Repetitive |  | 30 | A |
| $\mathrm{V}_{\text {sD }}$ | $I_{F}=I_{S}, V_{G S}=0 \mathrm{~V},$ <br> Pulse test, $\mathrm{t} \leq 300 \mu \mathrm{~s}$, duty cycle $\mathrm{d} \leq 2 \%$ |  | 1.5 | V |
| $\left.\begin{array}{l} \mathrm{t}_{\mathrm{rr}} \\ \mathrm{I}_{\mathrm{RM}} \\ \mathrm{Q}_{\mathrm{RM}} \end{array}\right\}$ | $\begin{aligned} & \mathrm{I}_{\mathrm{F}}=10 \mathrm{~A},-\mathrm{di} / \mathrm{dt}=100 \mathrm{~A} / \mu \mathrm{s} \\ & \mathrm{~V}_{\mathrm{R}}=100 \mathrm{~V}, \quad \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V} \end{aligned}$ | $\begin{array}{r} 5 \\ 0.6 \end{array}$ | 250 | ns A $\mu \mathrm{C}$ |

ISOPLUS220 ${ }^{\text {TM }}$ (IXFC) Outline


Note:
Bottom heatsink (Pin 4) is electrically isolated from Pin 1,2 , or 3.

| SYM | INCHES |  | MILLIMETERS |  |
| :---: | :---: | :---: | :---: | :---: |
|  | MIN | MAX | MIN | MAX |
| A | . 157 | . 197 | 4.00 | 5.00 |
| A2 | . 098 | . 118 | 2.50 | 3,00 |
| b | . 035 | . 051 | 0.90 | 1.30 |
| b2 | . 049 | . 065 | 1.25 | 1.65 |
| b4 | . 093 | . 100 | 2.35 | 2.55 |
| C | . 028 | . 039 | 0.70 | 1.00 |
| D | . 591 | . 630 | 15.00 | 16.00 |
| D1 | 472 | . 512 | 12.00 | 13.00 |
| E | 394 | . 433 | 10.00 | 11.00 |
| E1 | . 295 | . 335 | 7.50 | 8.50 |
| e | . 100 BASIC |  | 2.55 BASIC |  |
| L | . 512 | . 571 | 13.00 | 14.50 |
| L1 | . 118 | . 138 | 3.00 | 3.50 |
| T* |  |  | $42.5{ }^{\circ}$ | 47.5* |

Ref: IXYS CO 0177 R0

Note 1: Test Current $\mathrm{I}_{\mathrm{T}}=5 \mathrm{~A}$

## ADVANCE TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated objective result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

