



一众显示科技有限公司

TEAM SOURCE DISPLAY TECH. CO, LTD.

# TFT-LCD Module Specification

**Module NO.: TST035HVQI-54BC**

**Version: V1.0**

APPROVAL FOR SPECIFICATION

APPROVAL FOR SAMPLE

For Customer's Acceptance:	
Approved by	Comment

Team Source Display:		
Presented by	Reviewed by	Approved by
CJL	Bill	Aron

## Revision history

Version No.	Date	Page	Content	Remark
V1.0	2024-01-15	All	The initial release	

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## 1 General Characteristics

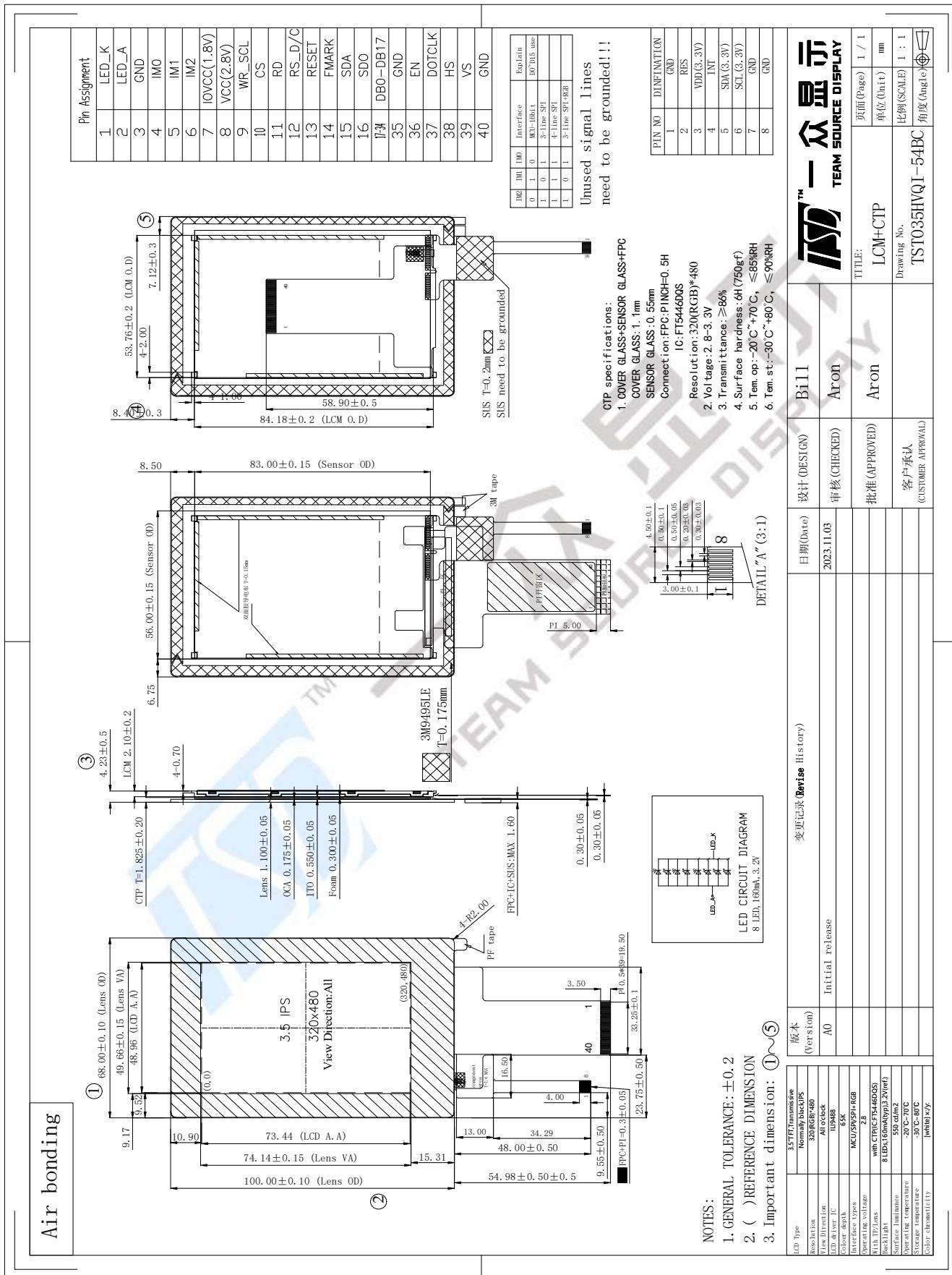
### 1.1 Introduction

**TST035HVQI-54BC** is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a Backlight and a Capacitive touch screen. This TFT LCD has a 3.5 (9:15) inch diagonally measured active display area with HVGA (320 horizontal by 480 vertical pixel) resolution.

### 1.2 General Information

ITEM	Specification	Unit
LCD Type	a-Si TFT,Transmissive,Normally black	-
LCD Size	3.5	inch
Resolution (W x H)	320x (RGB) × 480	pixel
Outline size	68(W) x 100(H) x 4.23(D)	mm
Active Area	48.96 (W) x 73.44(H)	mm
Pixel Pitch	0.153 x 0.153	mm
Viewing Direction	ALL o'clock	-
Color Depth	65/262K	-
Pixel Arrangement	RGB-stripe	-
Surface Luminance	550	cd/m <sup>2</sup>
Lens Surface Treatment	-	-
LCD Interface Type	MCU/SPI+RGB	-
With/Without TP or Lens	With CTP(IC:FT5446DQS)	-
Weight	TBD	g

## 2 Mechanical drawings



Air bonding

### 3 Interface description

#### 3.1 LCM interface description

PIN NO.	Symbol	description
1	LED_K	Backlight K Cathode input pin.
2	LED_A	Backlight A Anode input pin.
3	GND	System Ground. (0V)
4~6	M0~IM2	Interface setting pin
7	IOVCC(1.8V)	Voltage input pin for logic I/O +1.8V
8	VCC(2.8V)	Power supply +2.8V~3.3V
9	WR_SCL	write signal
10	CS	Chip select signal.
11	RD	read signal
12	RS_D/C	Data/Command Selection pin
13	RESET	Reset input signal
14	FMARK	Tearing Effect output signal
15	SDA	Serial data input pin
16	SDO	Serial data output pin
17~34	DB0~DB17	Data BUS
35	GND	System Ground. (0V)
36	EN	Data enable
37	DOTCLK	Pixel clock
38	HS	Horizontal sync signal
39	VS	Vertical sync signal
40	GND	System Ground. (0V)

IM2	IM1	IMO	Interface	Explain
0	1	0	MCU-16bit	D0~D15 use
1	0	1	3-line SPI	
1	1	1	4-line SPI	
1	0	1	3-line SPI+RGB	

## 4 Timing Characteristics

### 4.1 Reset timing

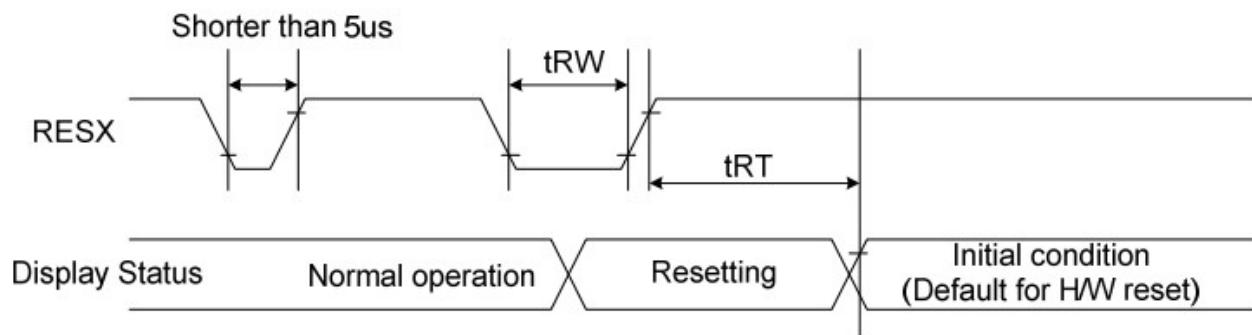
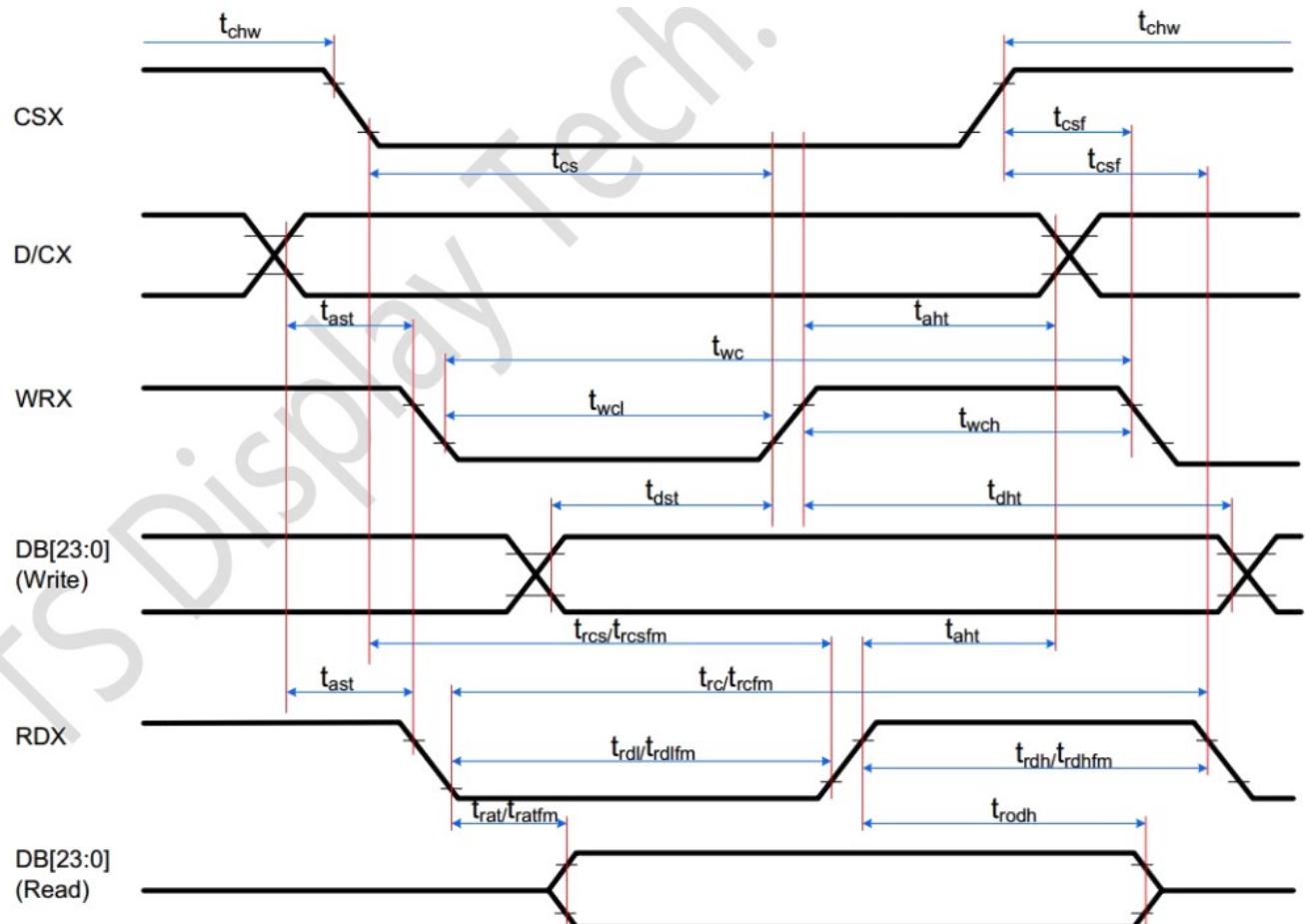


Table 39: Reset Timing

Signal	Symbol	Parameter	Min	Max	Unit
RESX	tRW	Reset pulse duration	10		uS
	tRT	Reset cancel		5 (note 1,5) 120 (note 1,6,7)	mS

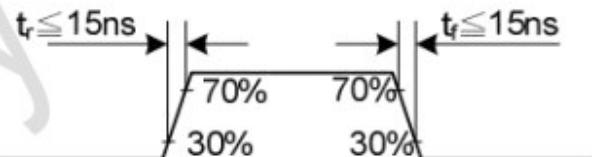
## 4.2 MCU write/read timing



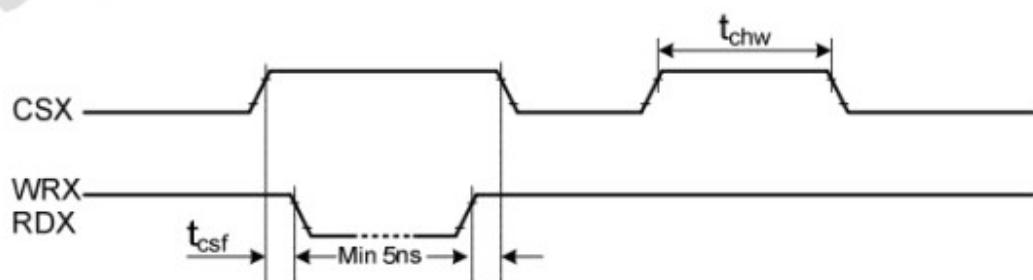
Signal	Symbol	Parameter	min	max	Unit	Description
DCX	tast	Address setup time	0	-	ns	-
	that	Address hold time (Write/Read)	0	-	ns	-
CSX	tchw	CSX "H" pulse width	0	-	ns	-
	tcs	Chip Select setup time (Write)	15	-	ns	-
	trcs	Chip Select setup time (Read ID)	45	-	ns	-
	trcsm	Chip Select setup time (Read FM)	355	-	ns	-
	tcsf	Chip Select Wait time (Write/Read)	0	-	ns	-
WRX	twc	Write cycle	40	-	ns	-
	twrh	Write Control pulse H duration	15	-	ns	-
	twrl	Write Control pulse L duration	15	-	ns	-
RDX (FM)	trcfm	Read Cycle (FM)	450	-	ns	When read from Frame Memory
	trdhfm	Read Control H duration (FM)	90	-	ns	
	trdlfm	Read Control L duration (FM)	355	-	ns	
RDX (ID)	trc	Read cycle (ID)	160	-	ns	When read ID data
	trdh	Read Control pulse H duration	90	-	ns	
	trdl	Read Control pulse L duration	45	-	ns	
DB [23:0], DB [17:0], DB [15:0], DB [8:0], DB [7:0]	tdst	Write data setup time	10	-	ns	For maximum, CL=30pF For minimum, CL=8pF
	tdht	Write data hold time	10	-	ns	
	trat	Read access time	-	40	ns	
	tratfm	Read access time	-	340	ns	
	trod	Read output disable time	20	80	ns	

**Notes:**

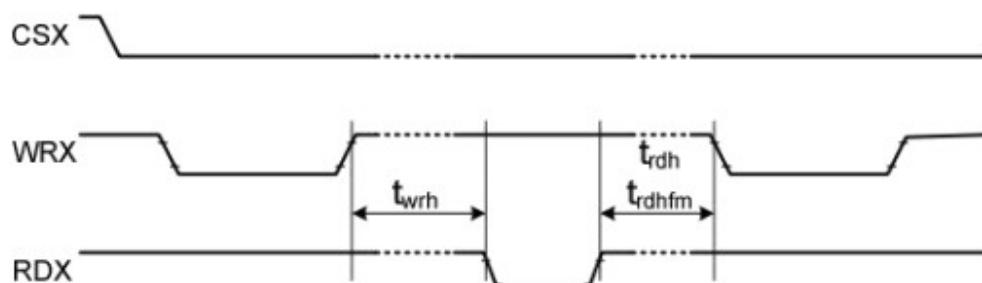
1.  $T_a = -30$  to  $70$  °C,  $IOVCC = 1.65V$  to  $3.3V$ ,  $VCI = 2.5V$  to  $3.3V$ ,  $AGND = DGND = 0V$
2. Logic high and low levels are specified as 30% and 70% of  $IOVCC$  for input signals.
3. Input signal rising time and falling time:



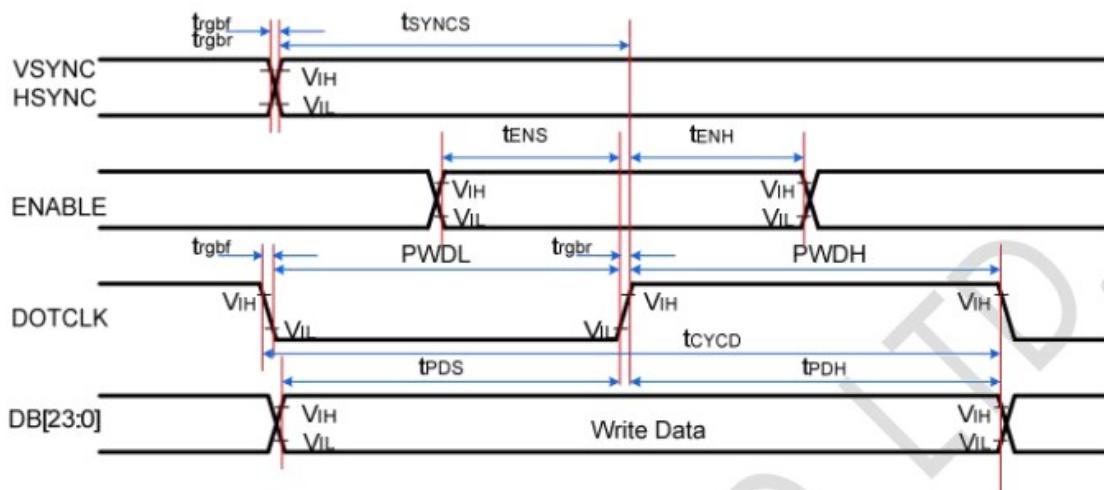
4. The CSX timing:



5. The Write to Read or the Read to Write timing:

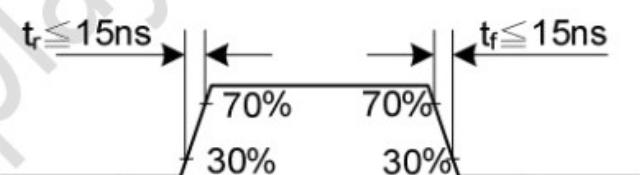


## 4.3 RGB Timing characteristics

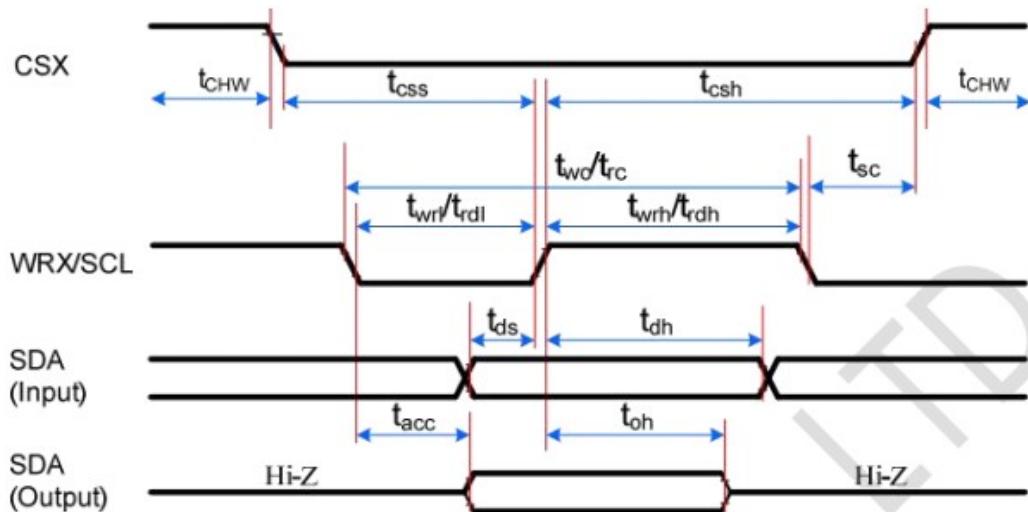


Signal	Symbol	Parameter	min	max	Unit	Description
VSYNC/ HSYNC	t <sub>SYNCS</sub>	VSYNC/HSYNC setup time	15	-	ns	16-/18-/24-bit bus RGB interface mode
	t <sub>SYNCH</sub>	VSYNC/HSYNC hold time	15	-	ns	
ENABLE	t <sub>E<sub>S</sub>N</sub>	ENABLE setup time	15	-	ns	16-/18-/24-bit bus RGB interface mode
	t <sub>E<sub>N</sub>H</sub>	ENABLE hold time	15	-	ns	
DB [23:0]	t <sub>P<sub>O</sub>S</sub>	Data setup time	15	-	ns	16-/18-/24-bit bus RGB interface mode
	t <sub>P<sub>D</sub>H</sub>	Data hold time	15	-	ns	
DOTCLK	PWDH	DOTCLK high-level period	20	-	ns	16-/18-/24-bit bus RGB interface mode
	PWDL	DOTCLK low-level period	20	-	ns	
	t <sub>CYCD</sub>	DOTCLK cycle time	50	-	ns	
	t <sub>r<sub>gb</sub>f</sub> , t <sub>f<sub>gb</sub>r</sub>	DOTCLK,HSYNC,VSYNC rise/fall time	-	15	ns	

Note: Ta = -30 to 70 °C, IOVCC = 1.65V to 3.3V, VCI = 2.5V to 3.3V, AGND = DGND = 0V

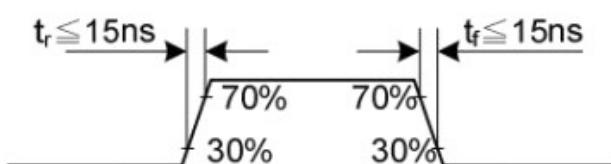


## 4.4 3-SPI Timing characteristics

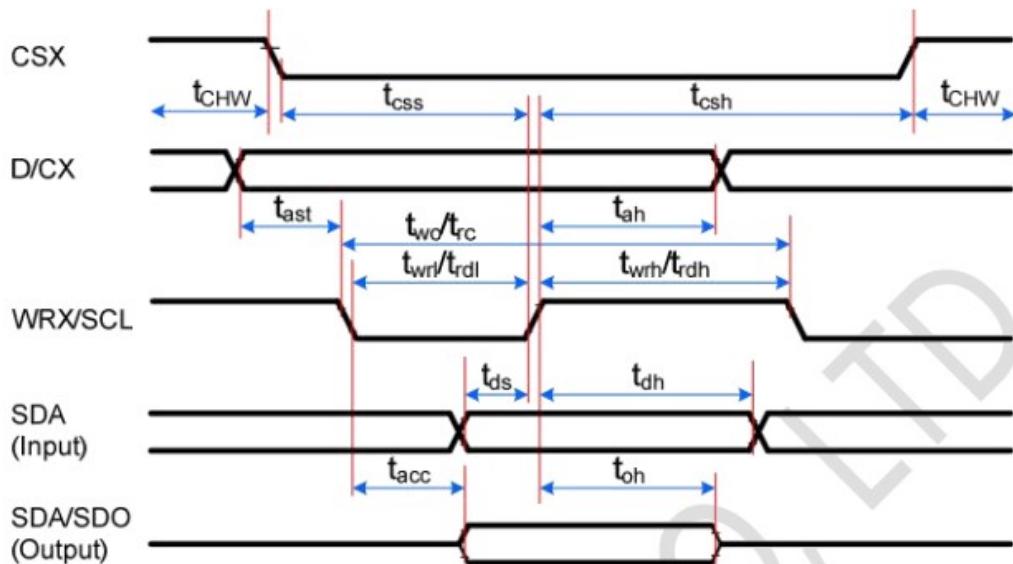


Signal	Symbol	Parameter	min	max	Unit	Description
CSX	tsc	SCL-CSX	15	-	ns	
	tchw	CSX H Pulse Width	40	-	ns	
	tcss	Chip select time (Write)	60	-	ns	
	tcs	Chip select hold time (Read)	65	-	ns	
SCL	t <sub>wc</sub>	Serial Clock Cycle (Write)	66	-	ns	
	t <sub>wrh</sub>	SCL H Pulse Width (Write)	15	-	ns	
	t <sub>wrl</sub>	SCL L Pulse Width (Write)	15	-	ns	
	t <sub>rc</sub>	Serial Clock Cycle (Read)	150	-	ns	
	t <sub>rdh</sub>	SCL H Pulse Width (Read)	60	-	ns	
	t <sub>rdl</sub>	SCL L Pulse Width (Read)	60	-	ns	
SDA (Input)	t <sub>ds</sub>	Data setup time (Write)	10	-	ns	
	t <sub>dh</sub>	Data hold time (Write)	10	-	ns	
SDA/SDO (Output)	t <sub>acc</sub>	Access time (Read)	10	50	ns	For maximum CL=30pF
	t <sub>oh</sub>	Output disable time (Read)	15	50	ns	For minimum CL=8pF

Note: Ta = -30 to 70 °C, IOVCC = 1.65V to 3.6V, VCI = 2.5V to 3.6V, AGND = DGND = 0V, T = 10+/-0.5ns



## 4.5 4-SPI Timing characteristics



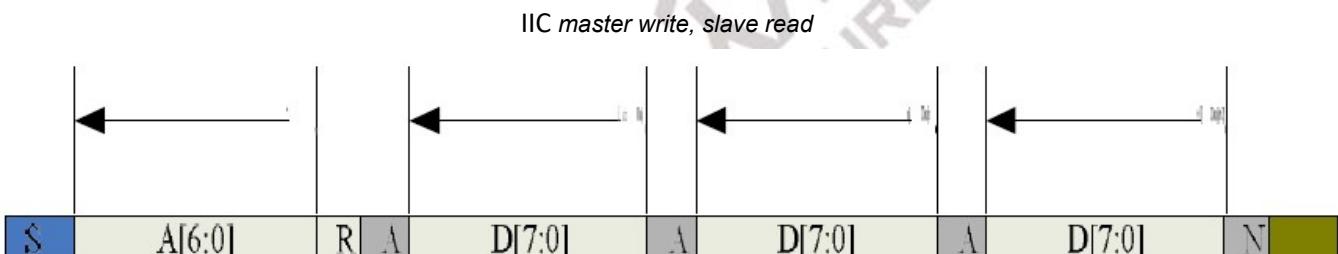
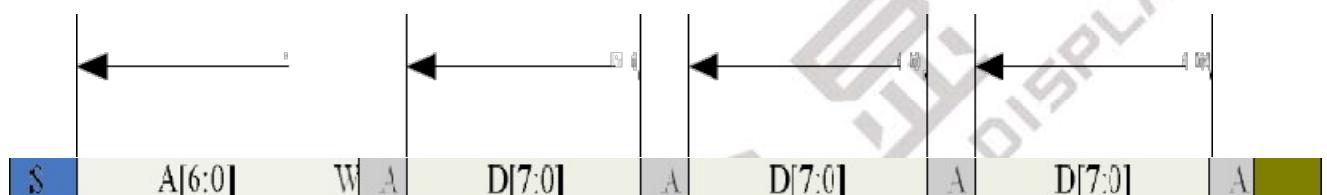
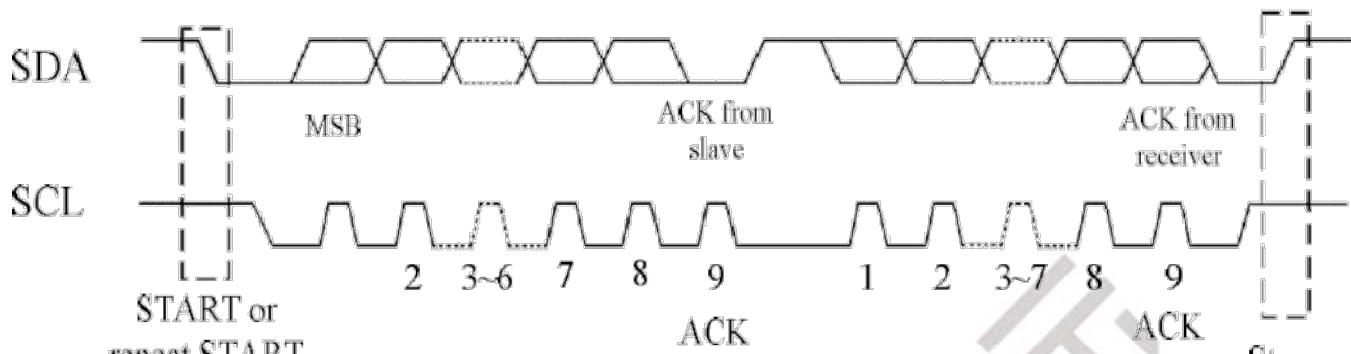
Signal	Symbol	Parameter	min	max	Unit	Description
CSX	$t_{css}$	Chip select time (Write)	15	-	ns	
	$t_{csh}$	Chip select hold time (Read)	15	-	ns	
	$t_{CHW}$	CS H pulse width	40	-	ns	
SCL	$t_{wc}$	Serial clock cycle (Write)	50	-	ns	
	$t_{wrh}$	SCL H pulse width (Write)	10	-	ns	
	$t_{wrh}$	SCL L pulse width (Write)	10	-	ns	
	$t_{rc}$	Serial clock cycle (Read)	150	-	ns	
	$t_{rdh}$	SCL H pulse width (Read)	60	-	ns	
	$t_{rdl}$	SCL L pulse width (Read)	60	-	ns	
D/CX	$t_{as}$	D/CX setup time	10	-	ns	
	$t_{ah}$	D/CX hold time (Write/Read)	10	-	ns	
SDA (Input)	$t_{ds}$	Data setup time (Write)	10	-	ns	
	$t_{dh}$	Data hold time (Write)	10	-	ns	
SDA/SDO (Output)	$t_{acc}$	Access time (Read)	10	50	ns	For maximum CL=30pF
	$t_{od}$	Output disable time (Read)	15	50	ns	For minimum CL=8pF

### Notes:

1.  $T_a = -30$  to  $70$  °C,  $IOVCC = 1.65V$  to  $3.3V$ ,  $VCI = 2.5V$  to  $3.3V$ ,  $AGND = DGND = 0V$ ,  $T = 10\pm 0.5$  ns.
2. Does not include signal rising and falling times.

## 4.6 Touch Screen IIC interface

The IIC is always configured in the Slave mode. The data transfer format is showed in the following figure.



Mnemonics Description Table

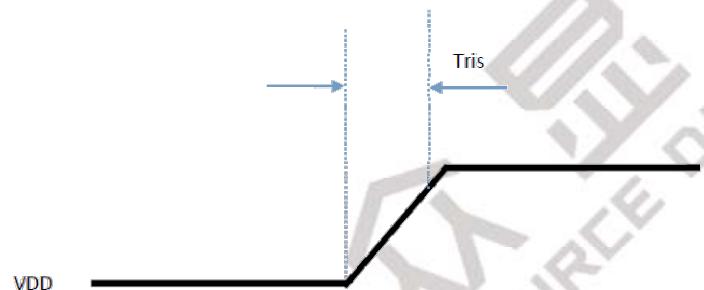
Mnemonics	Description
S	IIC Start or IIC Restart
A	Slave address: A[7:1]: Slave address A[0]: read or write bit
W	Bit0 = 0 : Write
R	Bit0 = 1: Read
A(N)	ACK(NACK)
P	STOP: the indication of the end of a packet (if this bit is missing, S will indicate the end of the current packet and the beginning of the next packet)

IIC Timing Characteristics Table

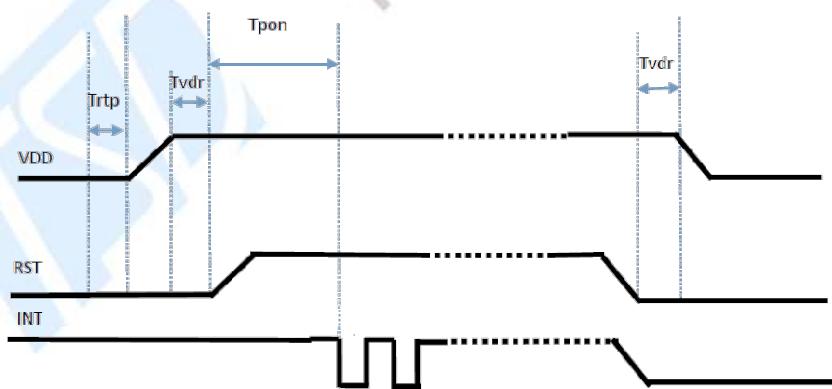
PARAMETER	MIN	MAX	UNIT
SCL Frequency	-	400K	Hz
Bus Free Time Between a STOP and START Condition	1.3	-	uS
Hold Time (repeated) START Condition	0.6	-	uS
Data Setup Time	100	-	nS
Setup Time for Repeated START Condition	0.6	-	uS
Setup Time for STOP Condition	0.6	-	uS

## 4.7 Touch Screen Power Sequence

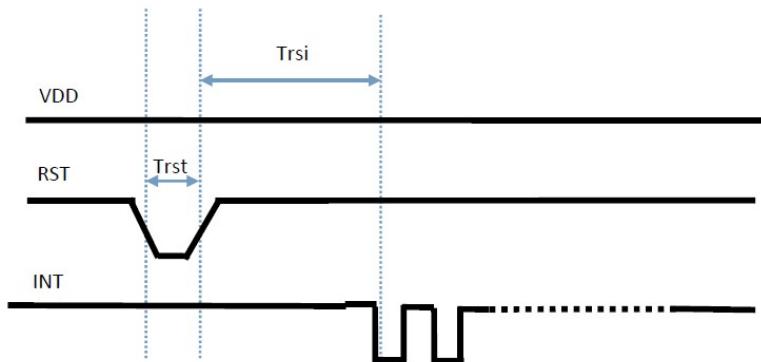
Power Rise Timing:



Power On / Off Timing:



Reset Sequence:



Parameter	Description	Min	Max	Units
Tris	Rise time from 0.1VDD to 0.9VDD		5	ms
Trtp	Time of resetting to be low before powering on	100		us
Tpon	Time of starting to report point after powering on	200		ms
Tvdr	Reset time after VDD powering on	1		ms
Trsi	Time of starting to report point after resetting	200		ms
Trst	Reset time	2		ms

## 5 Absolute Maximum Ratings

PARAMETER	SYMBOL	MIN	MAX	UNIT
LCD Supply Voltage	VCC_IN-GND	-0.3	3.3	V
Touch screen Supply Voltage	VCC-GND	-0.3	3.3	V
Operating Temperature	TOP	-20	70	°C
Storage Temperature	TST	-30	80	°C
Storage Humidity	RH	-	90%(Max 60°C)	RH

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for LED converter (Refer to 7 for further information).

## 6 Electrical Characteristics

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Analog operating voltage	VCC	2.5	2.8	3.3	V
Logic operating voltage	IOVCC	1.65	1.8	3.3	V
Input Current	IDD	-	TBD	-	mA
Input Voltage ' H ' level	VIH	0.7IOVCC	-	IOVCC	V
Input Voltage ' L ' level	VIL	GND	-	0.3IOVCC	
Output Voltage ' H ' level	VOH	0.8IOVCC	-	IOVCC	
Output Voltage ' L ' level	VOL	GND	-	0.2IOVCC	

## 7 Backlight Characteristics

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
LED Voltage	Vf	-	3.2	-	V
LED Current	If	-	160	-	mA
Power consumption	Wbl	-	512	-	mW
LED Life Time		30000	-	-	Hrs

Note (1) LED current is measured by utilizing a high frequency current meter as shown below:

Note (2) The lifetime of LED is defined as the time when it continues to operate under the conditions at

$T_a = 25 \pm 2$  °C and  $I_{LED} = 55\text{mA}_{DC}$ (LED forward current) until the brightness becomes  $\leq 50\%$  of its original value.

Note (3) Please note that LED life will be shorter than the average life described in the specification if operate in higher ambient temperature.

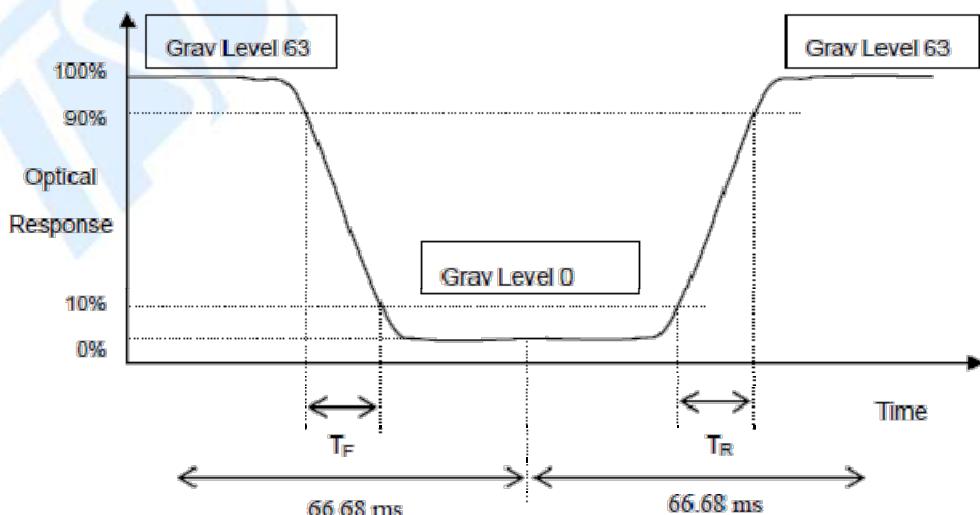
## 8 LCD Optical specifications

Item	Symbol	Condition	Specification			Unit	Remark
			Min	Typ	Max		
Response time	Tr+Tf	$\theta = 0^\circ$	-	20	30	ms	Note3
Contrast ratio	CR	$\theta = 0^\circ$	-	700	-		Note4
Color Gamut	S	$\theta = 0^\circ$		70		%	
Luminance( White)	L	$\theta = 0^\circ$	-	550	-	cd/m <sup>2</sup>	Note2,6
Uniformity (White)	-	$\theta = 0^\circ$	75	-	-	%	Note7
Viewing angle	Top	CR $\geq 10$	-	80	-	Deg.	Note5
	Bottom	CR $\geq 10$	-	80	-		
	Left	CR $\geq 10$	-	80	-		
	Right	CR $\geq 10$	-	80	-		
Color chromaticity (CIE1931)	Wx	$\theta = 0^\circ$	-0.04	TBD	+0.04		
	Wy			TBD			
	Rx			TBD			
	Ry			TBD			
	Gx			TBD			
	Gy			TBD			
	Bx			TBD			
	By			TBD			

Note 1: Ambient temperature = 25°C.

Note 2: To be measured with a viewing cone of 2° by Topcon luminance meter BM-7.

Note 3: Definition of Response Time (TR, TF) and measurement method:

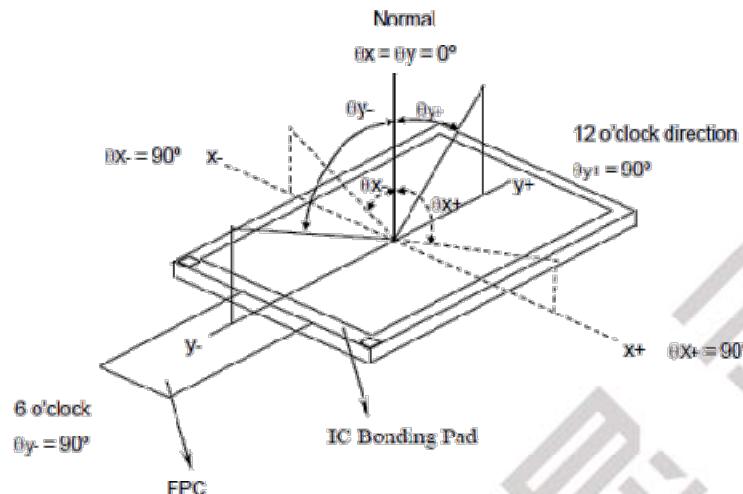


Note 4: Definition of contrast ratio:

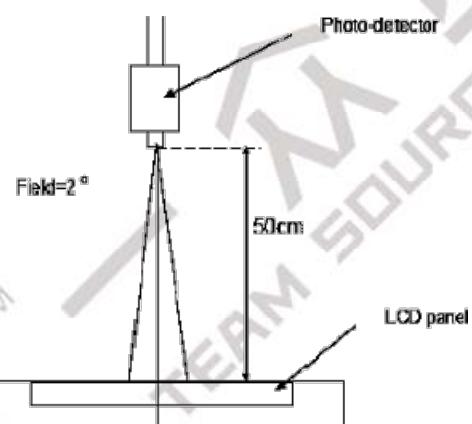
Contrast ratio is calculated by the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "white" state}}{\text{Brightness on the "black" state}}$$

Note 5: Definition of viewing angle



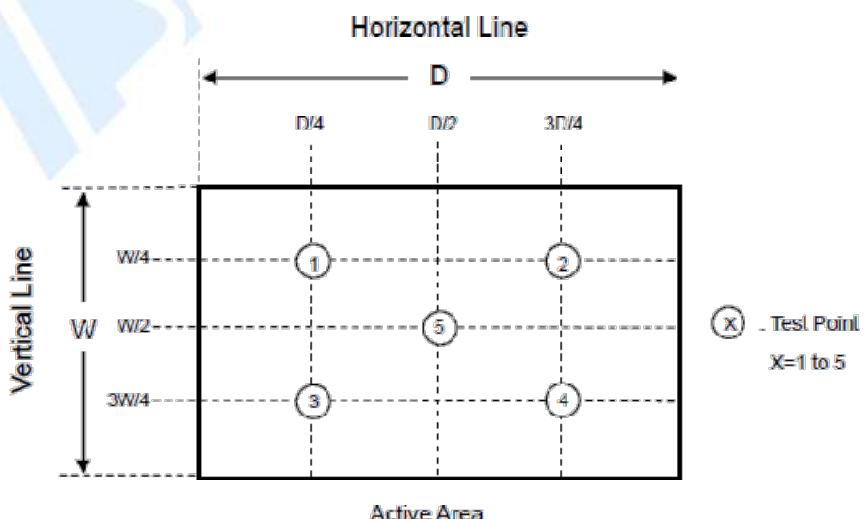
Note 6: Optical characteristic measurement setup.



Note 7: Definition of White Variation ( $\delta W$ ):

Measure the luminance of gray level 63 at 5 points

$$\delta W = \text{Maximum } [L(1), L(2), L(3), L(4), L(5)] / \text{Minimum } [L(1), L(2), L(3), L(4), L(5)]$$



## 9 RELIABILITY TEST

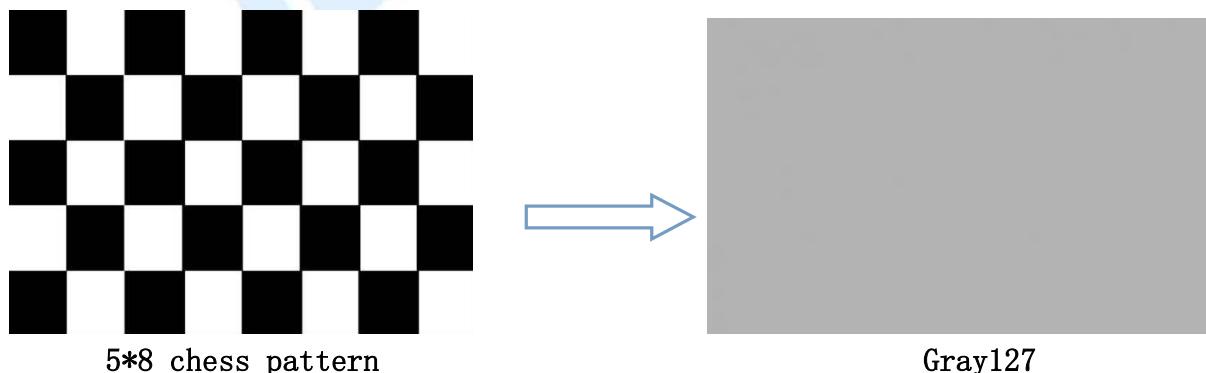
NO.	TEST ITEM	TEST CONDITION	Criterion
1	High Temperature Storage	80±2°C/96 hours	IEC60068-2-1, GB/T2423.2
2	Low Temperature Storage	-30±2°C/96 hours	IEC60068-2-1, GB/T2423.1
3	High Temperature Operating	70±2°C/96 hours	IEC60068-2-1, GB/T2423.2
4	Low Temperature Operating	-20±2°C/96 hours	IEC60068-2-1, GB/T2423.1
5	Temperature Cycle	-20±2°C(30 min.) , 70± 2°C (30min.) , 10 cycles	IEC60068-2-14, GB/T2423.22
6	High Temperature & Humidity Storage	60°C ±2°C × 90%RH/96 hours	IEC60068-2-78, GB/T2423.3
7	Vibration Test	Frequency 10Hz~55Hz Stroke: 1.5mm Sweep: 10Hz~150 Hz~10Hz 2 hours For each direction of X, Y, Z	IEC60068-2-6, GB/T2423.10
8	Shock Test (non operation )	"100G.6msmec,1/2 Sine Wave ±X ±Y ±Z each axis 3 times"	IEC60068-2-27,GB/T 2423.5
9	Electrostatic Discharge Test	C=150pF, R=330 Ω Air: ±8KV 150pF/330Ω 9 times Contact: ±4KV,9 times	IEC61000-4-2, GB/T17626.2
10	Image Sticking(残影)	25°C,60%RH/30 minutes	Note2

**Note 1:** Inspection after 2~4 hours storage at room temperature and humidity. The condensation is not accepted. The sample shall be free from defects:

1. Air bubble in the LCD
2. Seal leak
3. Non-display
4. Missing segments
5. Glass crack

**Note 2:** Switch the image to Grey 127 after displaying the 5\*8 chess pattern for 30 minutes, the after image disappears within 10 minutes.

采用 5x8 的黑白棋盘格画面保持 30 分钟, 然后切换到 127 灰阶(16 位色则是 63 灰阶), 10 分钟内图像消失。



## 9.1 About Image Sticking(关于残影)

### 9.1.1 What is Image Sticking?

If you remain a fixed image on LCD Display for a long period of time, you may experience a phenomenon called Image Sticking. Image Sticking - sometimes also called “image retention” or “ghosting” - is a phenomenon where a faint outline of a previously displayed image remains visible on the screen when the image is changed. It can occur at variable levels of intensity depending on the specific image makeup, as well as the amount of time the core image elements are allowed to remain unchanged on the screen. In POS applications, for example, a button menu which remains fixed, or in which the “frame” elements (core image) remain fixed and the buttons may change, may be susceptible to image sticking. It is important to note that if the screen is used exclusively for this application, the user may never notice this phenomenon since the screen never displays other content. It is only when an image other than the “retained” image is shown on the screen that this issue becomes evident. Image sticking is different than the “burn-in” effect commonly associated with phosphor based devices.

### 9.1.2 What causes Image Sticking?

Image sticking is an intrinsic behavior of LCD displays due to the susceptibility to polarization of the interior materials (liquid crystals) when used under static, charged conditions (continuously displaying the same image). The individual liquid crystals in an LCD panel have unique electrical properties. Displaying a fixed pattern - such as the POS menu described above - over prolonged periods can cause a parasitic charge build-up (polarization) within the liquid crystals which affects the crystals’ optical properties and ultimately prevents the liquid crystal from returning to its normal, relaxed state when the pattern is finally changed. This effect takes place at a cellular level within the LCD, and the effect can cause charged crystal alignment at the bottom or top of a crystal cell in the “z” axis, or even crystal migration to the edges of a cell, again based on their polarity. These conditions can cause image sticking over an entire area, or at boundaries of distinct color change respectively. In either case, when the liquid crystals in the pixels and sub-pixels utilized to display the static image are polarized such that they can not return fully to their “relaxed” state upon deactivation, the result is a faint, visible, retained image on the panel upon presentation of a new, different image. The actual rate of image retention depends on variation factors such as the specific image, how long it is displayed unchanged, the temperature within the panel and even the specific panel brand due to manufacturing differences amongst panel manufacturers.

### 9.1.3 How to Avoid Image Sticking? (如何避免残影)

- Try not to operate the LCD with a “fixed” image on the screen for more than 30 minutes.  
- 尽量不要在屏幕上显示“固定”图像的情况下操作 LCD 超过 30 分钟。
- If you are operating the monitor in an elevated temperature environment and with a displayed image which is contrary to the recommendations in “For Software Developers” below, image stick can occur in as little as 30 minutes. Adjust your screen saver settings accordingly.  
- 如果在高温环境中操作显示器，并且显示的图像与下面对“软件开发人员”中的建议相反，则图像粘贴可能在 30 分钟内发生。相应地调整屏幕保护程序设置。
- 1) Power down the unit during prolonged periods of inactivity such as the hours a store is closed or a shift during which the piece of equipment isn’t used.  
- 在长时间不活动时，如商店关门或不使用设备的轮班时，应关闭设备电源。
- 2) Use a screensaver with a black or medium gray background that is automatically set to come on if the device is inactive for more than 5-10 minutes.

- 使用黑色或中灰色背景的屏幕保护程序，如果设备处于非活动状态超过 5-10 分钟，屏幕保护程序会自动设置为打开。

3) Avoid placing the monitor in poorly ventilated areas or in areas that will create excess heat around the monitor for software developers.

- 避免将显示器放置在通风不良的区域或显示器周围产生过多热量的区域。

4) In defining the icons, buttons, or windows in the screen, try to utilize block patterns instead of distinct lines as borders for dividing the display into distinct areas.

- 在定义屏幕中的图标、按钮或窗口时，尝试使用块模式而不是不同的线作为边界，将显示划分为不同的区域。

5) If it is necessary to display a static image, try to use colors that are symmetric to the middle grey level at the boundary of two different colors, and slightly shift the borders line once in a while.

- 如果需要显示静态图像，请尝试在两种不同颜色的边界处使用与中间灰度级对称的颜色，并偶尔稍微移动边界线。

6) Try to utilize medium gray hues for those areas that will have prolonged display times or remain static as other menu elements change.

- 对于那些显示时间较长或随着其他菜单元素的变化而保持静止的区域，请尝试使用中等灰度色调。

#### 9.1.4 How to Fix the Image Sticking?

Unlike the usually irreversible “burn-in” effects commonly associated with direct view phosphor display devices such as CRTs, an image retained on an LCD display can be reversed – often to a point of total invisibility. However, the severity of the underlying causes (as described above) of the image retained on a specific display, as well as the variation factors (see “For Software Developers” above) under which the retained image was created, will dictate the final level of retention reversal. One way to erase a retained image on a panel is to run the screen (monitor “on”) in an “all black” pattern for 4-6 hours. It is also helpful to do this in an elevated temperature environment of approximately 35° to 50° C. Again, utilizing a dynamic screen saver with an all black background during prolonged idle display periods is a good way to avoid image retention issues.

#### 9.1.5 Is Image Sticking Covered by TSD RMA Warranty?

Image sticking is a phenomenon inherent to LCD Display technology itself, and as such, the occurrence of this “ghosting” effect is considered normal operation by the manufacturers of the LCD display modules which are integrated into today’s monitor solutions. TSD does not warrant any display against the occurrence of image sticking. We strongly advise that you follow the operating recommendations listed above to avoid the occurrence of this phenomenon.

### 9.2 Others

1. Issues that are not defined in this document shall be discussed and agreed with both parties. (Customer and supplier) 本文件中未定义的问题应由双方讨论并达成一致。（客户和供应商）。

2. Unless otherwise agreed upon in writing, the criteria shall be applied to both parties. (Customer and supplier) 除非另有书面约定，否则该标准适用于双方。（客户和供应商）。

## 10 Suggestions for using LCD modules

### 10.1 Handling of LCM

1. The LCD screen is made of glass. Don't give excessive external shock, or drop from a high place.
2. If the LCD screen is damaged and the liquid crystal leaks out, do not lick and swallow. When the liquid is attach to your hand, skin, cloth etc, wash it off by using soap and water thoroughly and immediately.
3. Don't apply excessive force on the surface of the LCM.
4. If the surface is contaminated, clean it with soft cloth. If the LCM is severely contaminated, use Isopropyl alcohol/Ethyl alcohol to clean. Other solvents may damage the polarizer. The following solvents is especially prohibited: water , ketone Aromatic solvents etc.
5. Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
6. Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
7. Don't disassemble the LCM.
8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - Be sure to ground the body when handling the LCD modules.
  - Tools required for assembling, such as soldering irons, must be properly grounded.
  - To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
  - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
9. Do not alter, modify or change the shape of the tab on the metal frame.
10. Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
11. Do not damage or modify the pattern writing on the printed circuit board.
12. Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector
13. Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
14. Do not drop, bend or twist LCM.

### 10.2 Storage

1. Store in an ambient temperature of 5 to 25 °C, and in a relative humidity of 40% to 60%. Don't expose to sunlight or fluorescent light.
2. Storage in a clean environment, free from dust, active gas, and solvent.
3. Store in anti-static container.

## 11 Limited Warranty

1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
2. If possible, we suggest customer to use up all LCD modules as soon as possible. If the LCD module storage time over twelve months, we suggest to recheck it before being used.
3. Any product issues must be feedback to TSD within 12 months since delivery, otherwise, we will not be responsible for the subsequent or consequential events.