

## **Innovating Energy Technology**

# FMW60N059S2FDHF

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**FUJI POWER MOSFET** 

## Super J MOS® S2 series

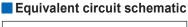
### N-Channel enhancement mode power MOSFET

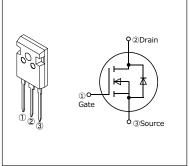
### Features

Pb-free lead terminal RoHS compliant uses Halogen-free molding compound

### Applications

For switching





### ■ Absolute Maximum Ratings at T<sub>c</sub>=25°C (unless otherwise specified)

Parameter	Symbol	Characteristics	Unit	Remarks
Dunin Course Voltage	<b>V</b> <sub>DS</sub>	600	V	
Drain-Source Voltage	<b>V</b> <sub>DSX</sub>	600	V	V <sub>GS</sub> =-30V
0.11. 0.1	,	64.4	Α	T <sub>c</sub> =25°C Note*1,2
Continuous Drain Current	I <sub>D</sub>	40.7	Α	T <sub>c</sub> =100°C Note*1,2
Pulsed Drain Current	<b>I</b> DP	200	Α	Note *2
Gate-Source Voltage	<b>V</b> GS	±30	V	
Non-Repetitive Maximum Avalanche Current	<b>I</b> AS	6.8	Α	Note *3
Non-Repetitive Maximum Avalanche Energy	Eas	1765.9	mJ	Note *4
Maximum Drain-Source dV/dt	dV⊳s/dt	50	V/ns	V <sub>DS</sub> ≤ 600V
Continuous	<b>I</b> sp	64.4	Α	T <sub>c</sub> =25°C Note*1,2
Diode Forward Current	<b>I</b> SD	40.7	Α	<i>T</i> <sub>c</sub> =100°C Note*1,2
Pulsed Diode Forward Current	<b>I</b> SDP	200	Α	Note *2
Peak Diode Recovery dV/dt	dV/dt	30	V/ns	Note *5
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note *6
Maximum Power Dissipation	P□	2.50	W	<i>T</i> <sub>a</sub> =25°C
	FD	340	VV	<i>T</i> <sub>c</sub> =25°C
Onereting and Stayone Temperature range	<b>T</b> ch	150	°C	
Operating and Storage Temperature range	T <sub>stg</sub>	-55 to +150	°C	

Note \*1: Maximum duty cycle D=0.6
Note \*2: Limited by maximum channel temperature.
Note \*3: T<sub>ch</sub>≤150°C, See Fig.1 and Fig.2
Note \*4: Starting T<sub>ch</sub>=25°C, I<sub>AS</sub>=4.1A, L=193mH, V<sub>DD</sub>=60V, R<sub>G</sub>=50Ω, See Fig.1 and Fig.2

E<sub>AS</sub> limited by maximum channel temperature and avalanche current.

Note \*5 : /so≤49.9A, -di/dt≤100A/µs, Vos peak≤ 600V, 7ch≤150°C.
Note \*6 : /so≤49.9A, dV/dt≤30V/ns, Vos peak≤ 600V, 7ch≤150°C.

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# ■ Electrical Characteristics at *T*<sub>c</sub>=25°C (unless otherwise specified) • Static Ratings

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>ss</sub> =0V I <sub>b</sub> =250μA		600	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> I <sub>D</sub> =7.9mA		3.0	4.0	5.0	V
Zero Gate Voltage Drain Current	<b>I</b> oss	V <sub>DS</sub> =600V V <sub>GS</sub> =0V	T <sub>ch</sub> =25°C	-	-	25	μА
		V <sub>DS</sub> =480V V <sub>GS</sub> =0V	T <sub>ch</sub> =125°C	-	73	-	
Gate-Source Leakage Current	IGSS	V <sub>DS</sub> =0V V <sub>GS</sub> = ± 30V		-	10	100	nA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V I <sub>D</sub> =25A		-	0.052	0.059	Ω
Gate resistance	<b>R</b> <sub>G</sub>	f=1MHz, open drain		-	6.2	-	Ω

### Dynamic Ratings

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =25V I <sub>D</sub> =25A	18	36	-	S
Input Capacitance	Ciss	V <sub>DS</sub> =400V	-	2680	-	
Output Capacitance	Coss	V <sub>GS</sub> =0V	-	92	-	
Reverse Transfer Capacitance	Crss	f=250kHz	-	10.8	-	
Effective output capacitance, energy related (Note *7)	C <sub>o(er)</sub>	V <sub>DS</sub> =0400V V <sub>GS</sub> =0V	-	222	-	pF
Effective output capacitance, time related (Note *8)	C <sub>o(tr)</sub>	V <sub>DS</sub> =0400V V <sub>GS</sub> =0V J <sub>D</sub> =constant	-	926	-	
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =400V, V <sub>GS</sub> =10V	-	28	-	
Turn-On Time	<b>t</b> r	I₀=25A,	-	111	-	no
Turn-Off Time $\frac{t_{\text{d(off)}}}{t_{\text{f}}}$	t <sub>d(off)</sub>	$R_{\rm e}$ =8.2 $\Omega$ See Fig.3 and Fig.4	-	183	-	ns
	<b>t</b> f		-	25	-	
Total Gate Charge	<b>Q</b> <sub>G</sub>		-	129	-	
Gate-Source Charge	Q <sub>GS</sub>	V <sub>DD</sub> =400V, V <sub>GS</sub> =10V I <sub>D</sub> =49.9A See Fig.5	-	41	-	200
Gate-Drain Charge	<b>Q</b> <sub>GD</sub>		-	61	-	nC
Drain-Source crossover Charge	<b>Q</b> sw		-	28	-	

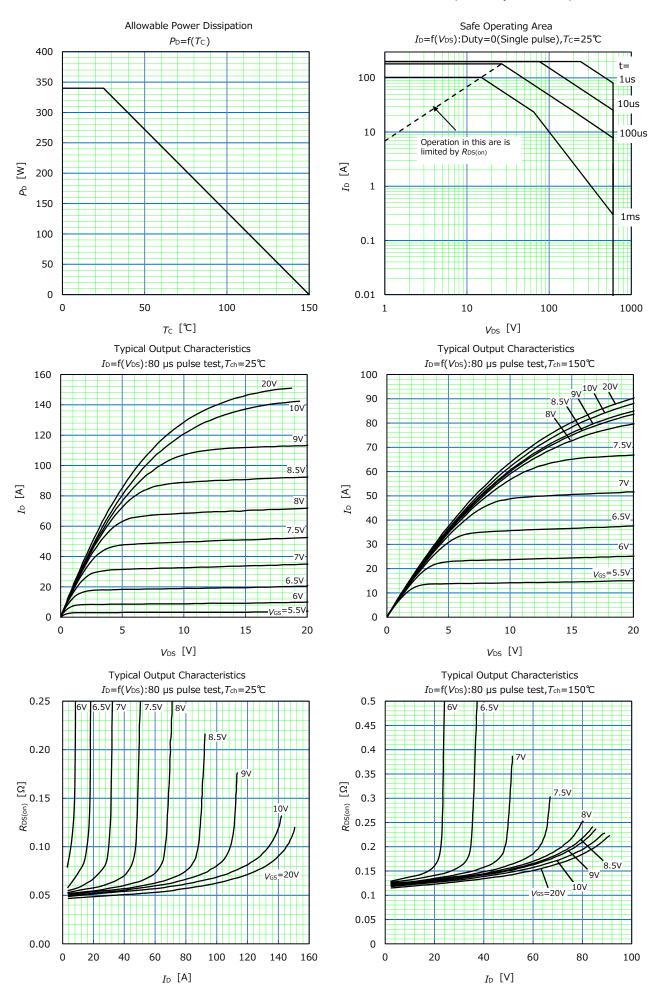
Note  $^*7$ :  $C_{0(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{oss}$  while Vos is rising from 0 to 400V. Note  $^*8$ :  $C_{o(er)}$  is a fixed capacitance that gives the same charging times as  $C_{oss}$  while Vos is rising from 0 to 400V.

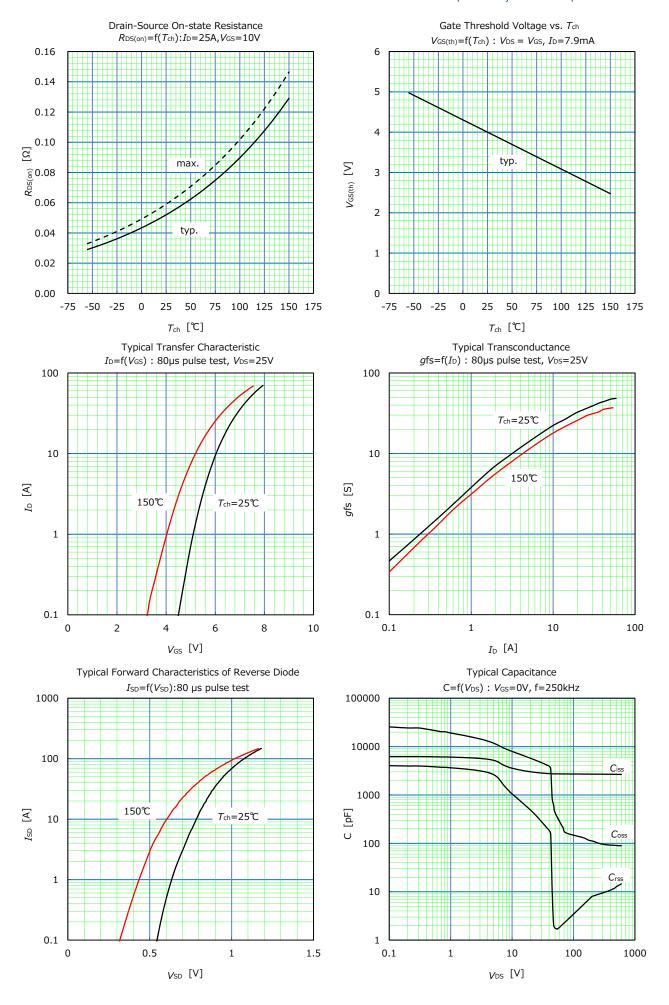
### • Reverse Diode

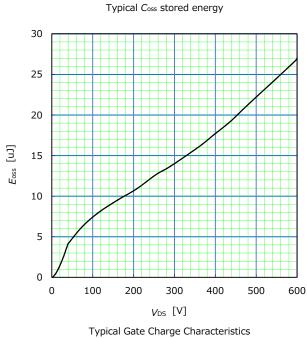
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Diode Forward On-Voltage	<b>V</b> <sub>SD</sub>	I <sub>SD</sub> =49.9A, V <sub>GS</sub> =0V T <sub>ch</sub> =25°C	-	0.95	1.35	V
Reverse Recovery Time	<b>t</b> rr	- V <sub>DD</sub> =400V, I <sub>SD</sub> =49.9A -di/dt=100A/μs Τ <sub>ch</sub> =25°C See Fig.6 and Fig.7	-	215	-	ns
Reverse Recovery Charge	Qrr		-	2.1	-	μC
Peak Reverse Recovery Current	<b>I</b> rp		-	19	-	А

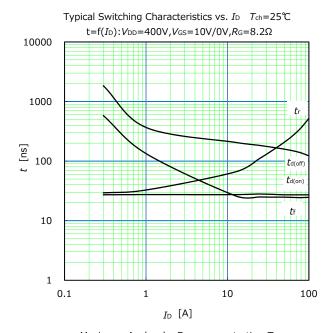
### ■ Thermal Resistance

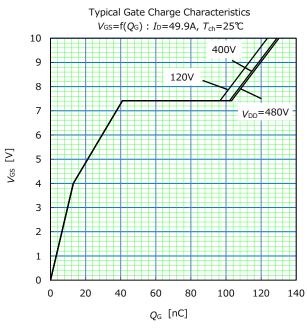
Parameter	Symbol	Min.	Тур.	Max.	Unit
Channel to Case	R <sub>th(ch-c)</sub>	-	-	0.368	°C/W
Channel to Ambient	R <sub>th(ch-a)</sub>	-	-	50	°C/W

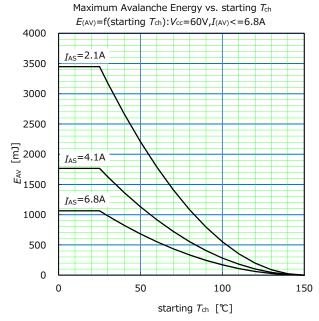


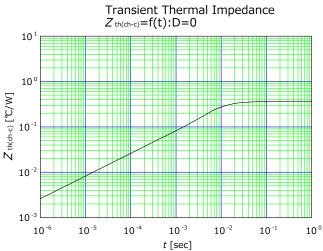












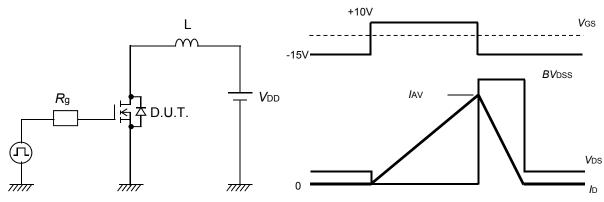


Fig.1 Avalanche Test circuit

Fig.2 Operating waveforms of Avalanche Test

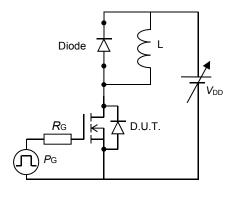


Fig.3 Switching Test circuit

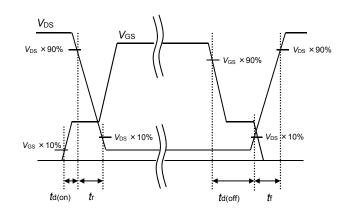


Fig.4 Operating waveform of Switching Test

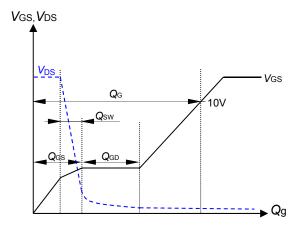


Fig.5 Operating waveform of Gate charge Test

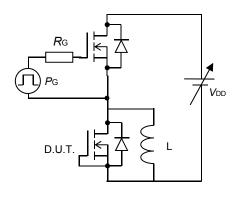


Fig.6 Reverse recovery Test circuit

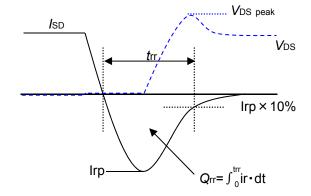
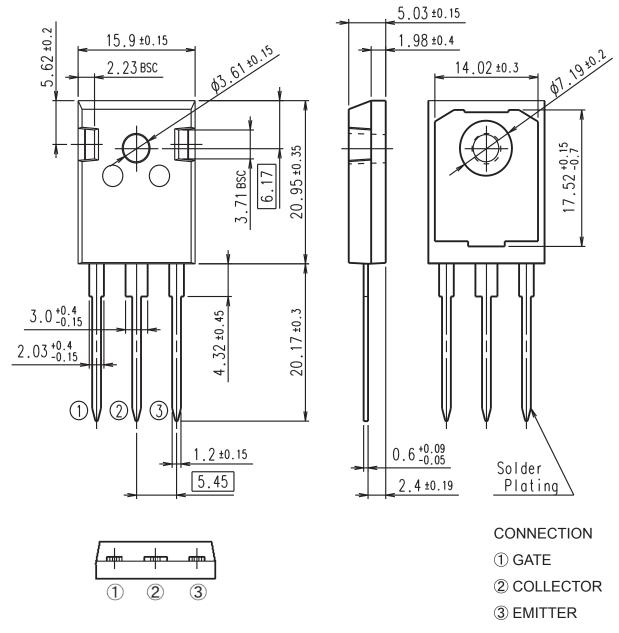


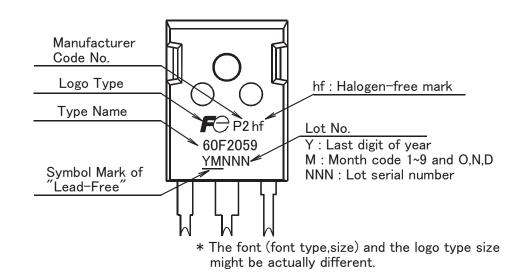
Fig.7 Operating waveform of Reverse recovery Test

### Outview: TO-247-P/TO-247-P2 Package



DIMENSIONS ARE IN MILLIMETERS.

### Marking



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  - ......
- Emergency equipment for responding to disasters and anti-burglary devices
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