### Innovating Energy Technology

# **FMP60N160S2HF**

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**FUJI POWER MOSFET** 

## Super J MOS® S2 series

### N-Channel enhancement mode power MOSFET

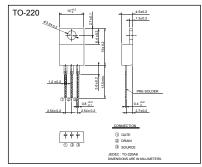
### Features

Pb-free lead terminal RoHS compliant uses Halogen-free molding compound

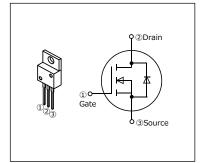
### Applications

For switching

### Outline Drawings [mm]



### Equivalent circuit schematic



### ■ Absolute Maximum Ratings at T<sub>c</sub>=25°C (unless otherwise specified)

Parameter	Symbol	Characteristics	Unit	Remarks
Dunin Course Voltage	<b>V</b> <sub>DS</sub>	600	V	
Drain-Source Voltage	<b>V</b> <sub>DSX</sub>	600	V	V <sub>GS</sub> =-30V
Continuous Drain Current	l <sub>o</sub>	23.9	Α	T <sub>c</sub> =25°C Note*1,2
Continuous Drain Current		15.1	Α	Tc=100°C Note*1,2
Pulsed Drain Current	<b>I</b> DP	71.6	Α	Note *2
Gate-Source Voltage	V <sub>GS</sub>	±30	V	
Non-Repetitive Maximum Avalanche Current	<b>I</b> AS	2.7	А	Note *3
Non-Repetitive Maximum Avalanche Energy	<b>E</b> as	618	mJ	Note *4
Maximum Drain-Source dV/dt	dV⊳s/dt	50	V/ns	V <sub>DS</sub> ≤ 600V
Continuous	,	23.9	Α	T <sub>c</sub> =25°C Note*1,2
Diode Forward Current	<b>I</b> SD	15.1	Α	T <sub>c</sub> =100°C Note*1,2
Pulsed Diode Forward Current	<b>I</b> SDP	71.6	Α	Note *2
Peak Diode Recovery dV/dt	dV/dt	15	V/ns	Note *5
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note *6
	Po	2.02	W	<i>T</i> <sub>a</sub> =25°C
Maximum Power Dissipation	<b>F</b> D	127	VV	<i>T</i> c=25°C
Oneveting and Stayons Townsysters young	T <sub>ch</sub>	150	°C	
Operating and Storage Temperature range	T <sub>stg</sub>	-55 to +150	°C	

Note \*1 : Maximum duty cycle D=0.56

Note \*1: Imited by maximum channel temperature.

Note \*3: T<sub>ch</sub>≤150°C, See Fig.1 and Fig.2

Note \*4: Starting T<sub>ch</sub>=25°C, I<sub>A</sub>s=1.7A, L=392mH, V<sub>DD</sub>=60V, R<sub>G</sub>=50Ω, See Fig.1 and Fig.2

E<sub>AS</sub> limited by maximum channel temperature and avalanche current.

Note \*5: I<sub>SD</sub>≤17.9A, -di/dt≤100A/µs, V<sub>DS</sub> peak≤600V, T<sub>ch</sub>≤150°C.

Note \*6: I<sub>SD</sub>≤17.9A, dV/dt≤15V/ns, V<sub>DS</sub> peak≤600V, T<sub>ch</sub>≤150°C.

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# ■ Electrical Characteristics at *T*<sub>c</sub>=25°C (unless otherwise specified) • Static Ratings

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA		600	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> I <sub>D</sub> =0.95mA		3.5	4.0	4.5	V
Zero Gate Voltage Drain Current	<b>I</b> oss	V <sub>DS</sub> =600V V <sub>GS</sub> =0V	T <sub>ch</sub> =25°C	-	-	25	μΑ
		V <sub>DS</sub> =480V V <sub>GS</sub> =0V	T <sub>ch</sub> =125°C	-	-	250	
Gate-Source Leakage Current	<b>I</b> GSS	V <sub>DS</sub> =0V V <sub>GS</sub> = ± 30V		-	10	100	nA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V I <sub>D</sub> =9.0A		-	0.146	0.160	Ω
Gate resistance	<b>R</b> G	f=1MHz, open drain		-	9.8	-	Ω

### Dynamic Ratings

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =25V I <sub>D</sub> =9.0A	5.7	11.5	-	S
Input Capacitance	Ciss	V <sub>DS</sub> =400V	-	990	-	
Output Capacitance	Coss	V <sub>GS</sub> =0V	-	35	-	
Reverse Transfer Capacitance	Crss	f=250kHz	-	5.3	-	
Effective output capacitance, energy related (Note *7)	C <sub>o(er)</sub>	V <sub>DS</sub> =0400V V <sub>GS</sub> =0V	-	83	-	pF
Effective output capacitance, time related (Note *8)	C <sub>o(tr)</sub>	V <sub>DS</sub> =0400V V <sub>GS</sub> =0V J <sub>D</sub> =constant	-	308	-	
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =400V, V <sub>GS</sub> =10V I <sub>D</sub> =9.0A, R <sub>G</sub> =15Ω See Fig.3 and Fig.4	-	19	-	ns
Turn-On Time	<b>t</b> r		-	63	-	
Turn-Off Time	t <sub>d(off)</sub>		-	87	-	
Turn-On Time	<b>t</b> f		-	23	-	
Total Gate Charge	<b>Q</b> <sub>G</sub>	V <sub>DD</sub> =400V, V <sub>GS</sub> =10V I <sub>D</sub> =17.9A See Fig.5	-	43	-	
Gate-Source Charge	Q <sub>GS</sub>		-	17	-	]
Gate-Drain Charge	<b>Q</b> <sub>GD</sub>		-	16	-	nC
Drain-Source crossover Charge	<b>Q</b> sw		-	11	-	

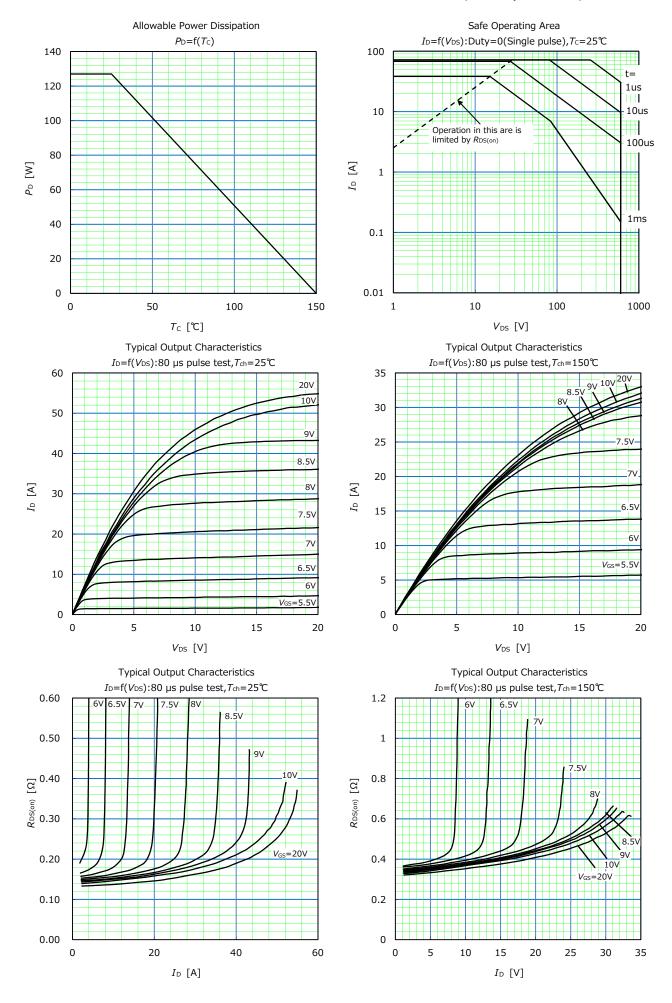
Note  $^*7$ :  $C_{0(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{oss}$  while Vos is rising from 0 to 400V. Note  $^*8$ :  $C_{o(er)}$  is a fixed capacitance that gives the same charging times as  $C_{oss}$  while Vos is rising from 0 to 400V.

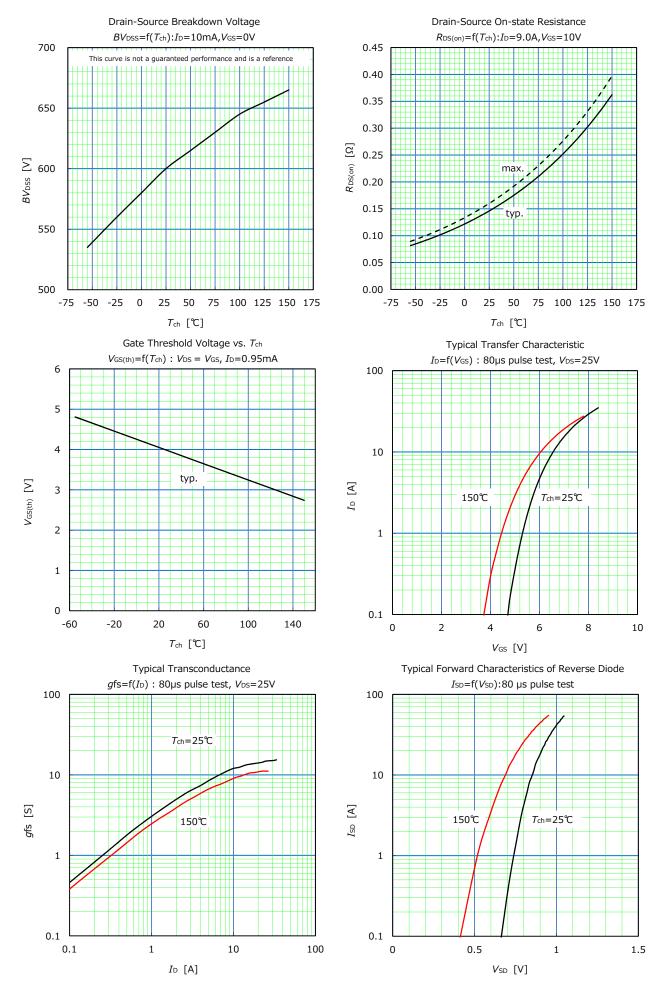
### Reverse Diode

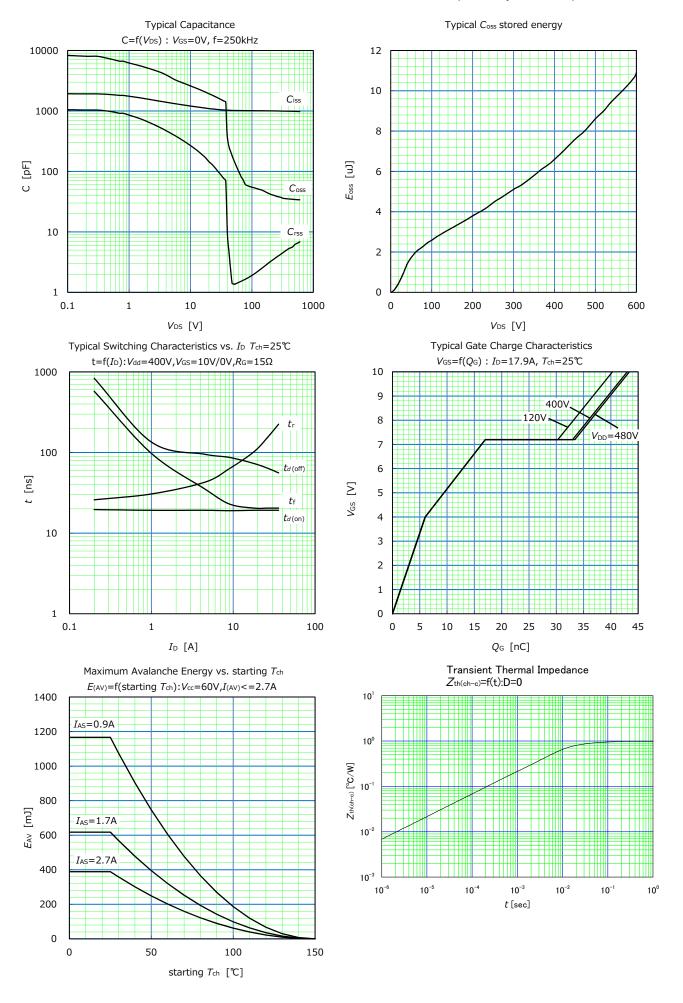
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Diode Forward On-Voltage	<b>V</b> <sub>SD</sub>	I <sub>SD</sub> =17.9A, V <sub>GS</sub> =0V T <sub>ch</sub> =25°C	-	0.90	1.35	V
Reverse Recovery Time	<b>t</b> rr	- V <sub>DD</sub> =400V, I <sub>SD</sub> =17.9A -di/dt=100A/μs Τ <sub>ch</sub> =25°C See Fig.6 and Fig.7	-	285	-	ns
Reverse Recovery Charge	Qrr		-	3.7	-	μC
Peak Reverse Recovery Current	<b>I</b> rp		-	25	-	А

### ■ Thermal Resistance

Parameter	Symbol	Min.	Тур.	Max.	Unit
Channel to Case	Rth(ch-c)	-	-	0.984	°C/W
Channel to Ambient	Rth(ch-a)	-	-	62	°C/W







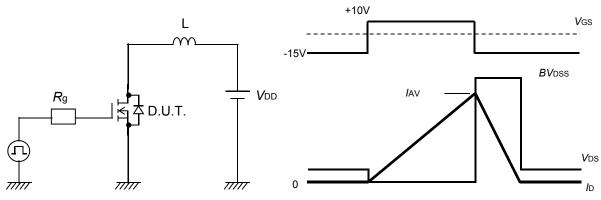


Fig.1 Avalanche Test circuit

Fig.2 Operating waveforms of Avalanche Test

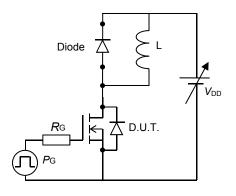


Fig.3 Switching Test circuit

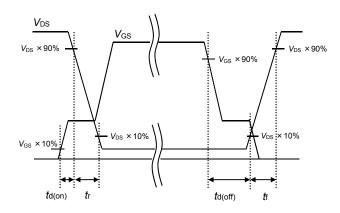


Fig.4 Operating waveform of Switching Test

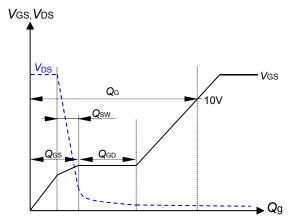


Fig.5 Operating waveform of Gate charge Test

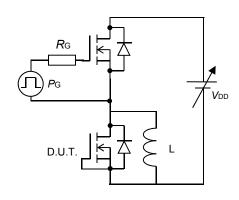


Fig.6 Reverse recovery Test circuit

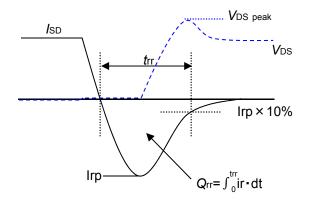
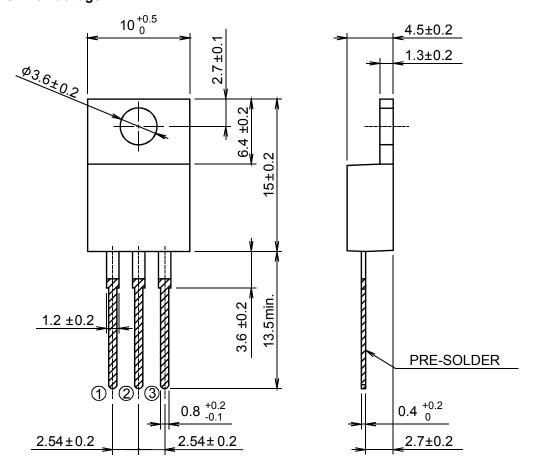
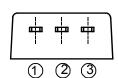


Fig.7 Operating waveform of Reverse recovery Test

### Outview: TO-220 Package





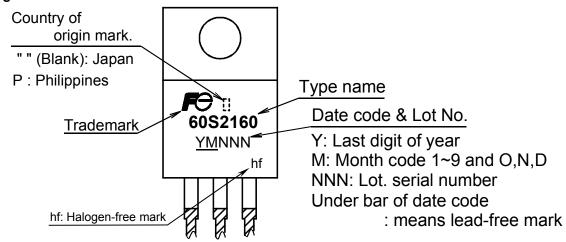
### CONNECTION

- 1 GATE
- 2 DRAIN
- 3 SOURCE

JEDEC: TO-220AB

DIMENSIONS ARE IN MILLIMETERS.

### Marking



\* The font (font type,size) and the trademark-size might be actually different.

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